

## **General Purpose Transistors**

#### **PNP Silicon**

### **MMBT3906TT1**

This transistor is designed for general purpose amplifier applications. It is housed in the SOT-416/SC-75 package which is designed for low power surface mount applications.

#### **Features**

- NSVM Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

#### MAXIMUM RATINGS (T<sub>A</sub> = 25°C)

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	$V_{CEO}$	-40	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	-40	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	-5.0	Vdc
Collector Current - Continuous	I <sub>C</sub>	-200	mAdc

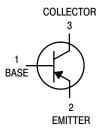
#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation, FR-4 Board (Note 1) @T <sub>A</sub> = 25°C Derated above 25°C	P <sub>D</sub>	200 1.6	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	600	°C/W
Total Device Dissipation, FR-4 Board (Note 2) @T <sub>A</sub> = 25°C Derated above 25°C	P <sub>D</sub>	300 2.4	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	400	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-65 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. FR-4 @ Minimum Pad
- 2. FR-4 @ 1.0 × 1.0 Inch Pad

# GENERAL PURPOSE AMPLIFIER TRANSISTORS SURFACE MOUNT





CASE 463 SOT-416/SC-75 STYLE 1

#### MARKING DIAGRAM



2A = Device Code
M = Date Code\*

= Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation may vary depending upon manufacturing location.

#### ORDERING INFORMATION

Device	Package	Shipping <sub>†</sub>
MMBT3906TT1G	SOT-416 (Pb-Free)	3000 / Tape & Reel
NSVMMBT3906TT1G	SOT-416 (Pb-Free)	3000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit	
OFF CHARACTERIST	ics	<u>.</u>			•	
Collector – Emitter Brea (I <sub>C</sub> = –1.0 mAdc, I <sub>B</sub>	akdown Voltage (Note 3) = 0)	V <sub>(BR)CEO</sub>	-40	-	Vdc	
Collector – Base Break (I <sub>C</sub> = –10 μAdc, I <sub>E</sub> :		V <sub>(BR)</sub> CBO	-40	-	Vdc	
Emitter – Base Breakdo ( $I_E = -10 \mu Adc, I_C =$		V <sub>(BR)EBO</sub>	-5.0	-	Vdc	
Base Cutoff Current (V <sub>CE</sub> = -30 Vdc, V <sub>E</sub>	<sub>EB</sub> = -3.0 Vdc)	I <sub>BL</sub>	_	-50	nAdc	
Collector Cutoff Currer (V <sub>CE</sub> = -30 Vdc, V <sub>E</sub>		I <sub>CEX</sub>	_	-50	nAdc	
ON CHARACTERISTI	CS (Note 3)		•	•	•	
DC Current Gain	<sub>CE</sub> = -1.0 Vdc) <sub>CE</sub> = -1.0 Vdc) <sub>CE</sub> = -1.0 Vdc)	h <sub>FE</sub>	60 80 100 60 30	- 300 - -	_	
Collector – Emitter Saturation (I <sub>C</sub> = -10 mAdc, I <sub>B</sub> (I <sub>C</sub> = -50 mAdc, I <sub>B</sub>	= -1.0 mAdc)	V <sub>CE(sat)</sub>	- -	-0.25 -0.4	Vdc	
Base – Emitter Saturation Voltage $ \begin{pmatrix} I_C = -10 \text{ mAdc}, I_B = -1.0 \text{ mAdc} \\ (I_C = -50 \text{ mAdc}, I_B = -5.0 \text{ mAdc} \end{pmatrix} $		V <sub>BE(sat)</sub>	-0.65 -	-0.85 -0.95	Vdc	
SMALL-SIGNAL CHA	ARACTERISTICS					
Current – Gain – Band (I <sub>C</sub> = –10 mAdc, V <sub>C</sub>	width Product c <sub>E</sub> = −20 Vdc, f = 100 MHz)	f <sub>T</sub>	250	-	MHz	
Output Capacitance (V <sub>CB</sub> = -5.0 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)		$C_{ m obo}$	_	4.5	pF	
Input Capacitance1 (V <sub>EB</sub> = -0.5 Vdc, I <sub>C</sub>	; = 0, f = 1.0 MHz)	C <sub>ibo</sub>	-	10.0	pF	
Input Impedance (V <sub>CE</sub> = -10 Vdc, I <sub>C</sub>	= -1.0 mAdc, f = 1.0 kHz)	h <sub>ie</sub>	2.0	12	kΩ	
Voltage Feedback Ratio $(V_{CE} = -10 \text{ Vdc}, I_{C} = -1.0 \text{ mAdc}, f = 1.0 \text{ kHz})$		h <sub>re</sub>	0.1	10	X 10-	
Small – Signal Current Gain (V <sub>CE</sub> = -10 Vdc, I <sub>C</sub> = -1.0 mAdc, f = 1.0 kHz)		h <sub>fe</sub>	100	400	-	
Output Admittance $(V_{CE} = -10 \text{ Vdc}, I_{C} = -1.0 \text{ mAdc}, f = 1.0 \text{ kHz})$		h <sub>oe</sub>	3.0	60	μmho	
Noise Figure (V <sub>CE</sub> = -5.0 Vdc, I <sub>C</sub> = -100 $\mu$ Adc, R <sub>S</sub> = 1.0 k $\Omega$ , f = 1.0 kHz)		NF	-	4.0	dB	
SWITCHING CHARAC	CTERISTICS				-	
Delay Time	$(V_{CC} = -3.0 \text{ Vdc}, V_{BE} = 0.5 \text{ Vdc})$	t <sub>d</sub>	_	35		
Rise Time	$(I_C = -10 \text{ mAdc}, I_{B1} = -1.0 \text{ mAdc})$	t <sub>r</sub>	_	35	ns	
Storage Time	$(V_{CC} = -3.0 \text{ Vdc}, I_C = -10 \text{ mAdc})$	t <sub>s</sub>	_	225	ns	
Fall Time $(I_{B1} = I_{B2} = -1.0 \text{ mAdc})$		t <sub>f</sub>	_	75		

<sup>3.</sup> Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2.0%.

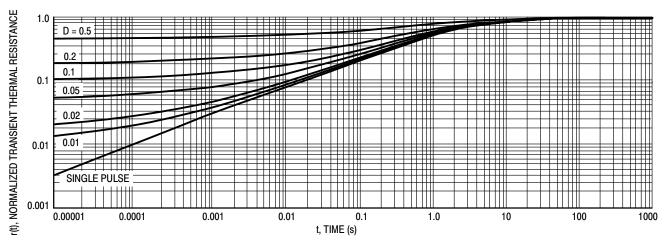
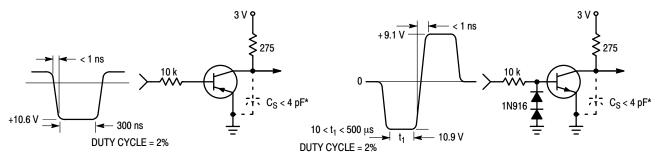


Figure 1. Normalized Thermal Response



\* Total shunt capacitance of test jig and connectors

Figure 2. Delay and Rise Time Equivalent Test Circuit

Figure 3. Storage and Fall Time Equivalent Test Circuit

#### **TYPICAL TRANSIENT CHARACTERISTICS**

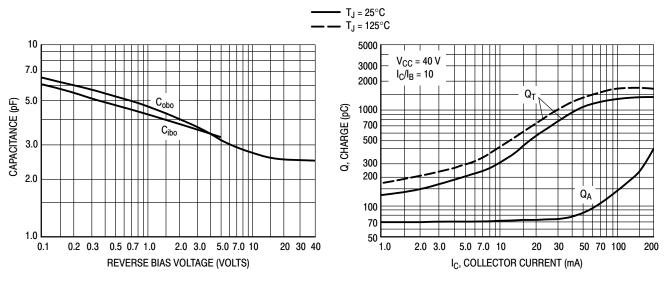
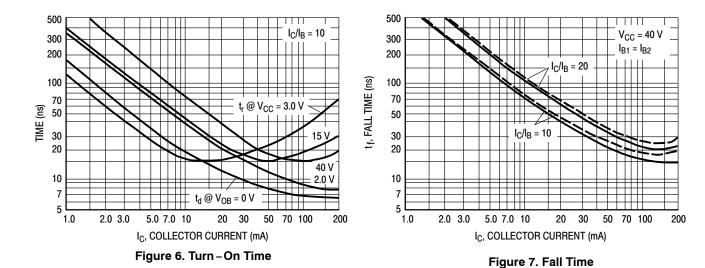


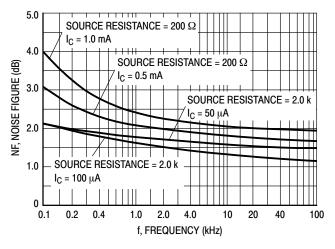
Figure 4. Capacitance

Figure 5. Charge Data



#### TYPICAL AUDIO SMALL-SIGNAL CHARACTERISTICS **NOISE FIGURE VARIATIONS**

 $(V_{CE} = -5.0 \text{ Vdc}, T_A = 25^{\circ}\text{C}, Bandwidth = 1.0 \text{ Hz})$ 



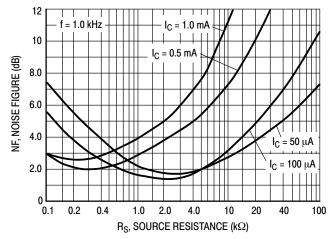
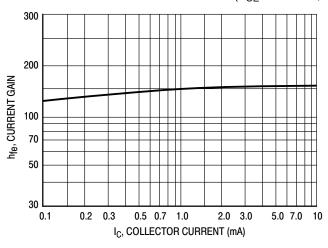


Figure 8.

Figure 9.

#### **h PARAMETERS**

 $(V_{CE} = -10 \text{ Vdc}, f = 1.0 \text{ kHz}, T_A = 25^{\circ}\text{C})$ 



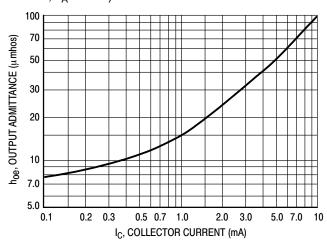
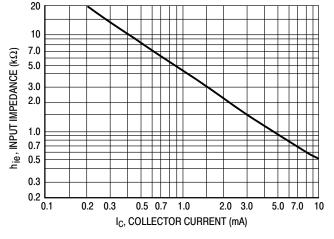


Figure 10. Current Gain

Figure 11. Output Admittance 10



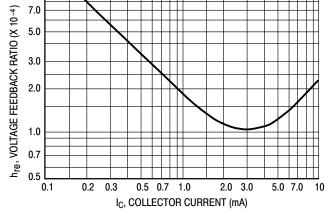


Figure 12. Input Impedance

Figure 13. Voltage Feedback Ratio

#### STATIC CHARACTERISTICS

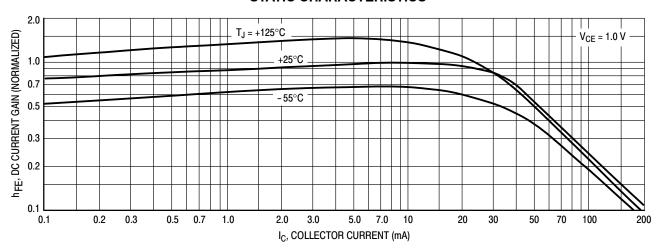


Figure 14. DC Current Gain

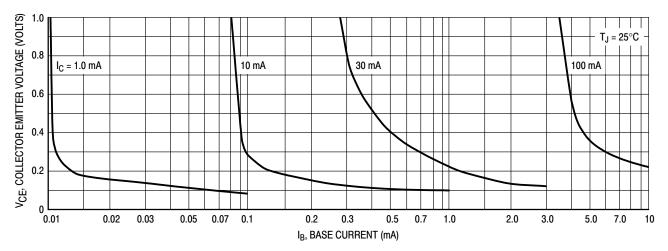


Figure 15. Collector Saturation Region

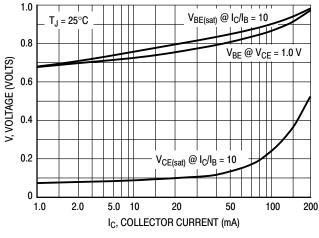


Figure 16. "ON" Voltages

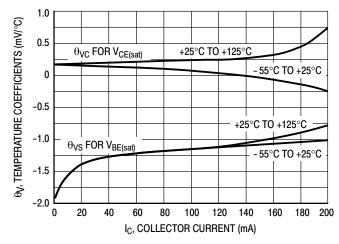


Figure 17. Temperature Coefficients



#### SC75-3 1.60x0.80x0.80, 1.00P

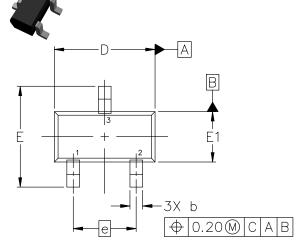
**CASE 463 ISSUE H** 

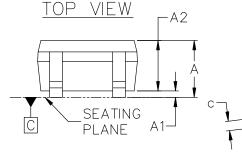
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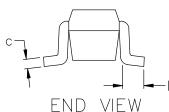
#### NOTES:

- DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5-2018.
- ALL DIMENSION ARE IN MILLIMETERS.

DIM	MILLIMETERS			
UIIVI	MIN.	NOM.	MAX.	
А	0.70	0.80	0.90	
A1	0.00	0.05	0.10	
A2	0.80 REF.			
b	0.15	0.20	0.30	
С	0.10	0.15	0.25	
D	1.55	1.60	1.65	
Е	1.50	1.60	1.70	
E1	0.70	0.80	0.90	
е	1.00 BSC			
L	0.10	0.15	0.20	

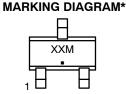






# **GENERIC**

SIDE VIEW



XX = Specific Device Code

Μ = Date Code = Pb-Free Package

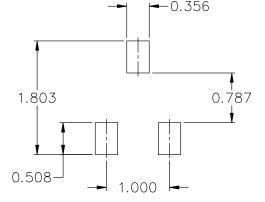
\*This information is generic. Please refer to device data sheet for actual part marking.

device data sileet for actual part marking
Pb-Free indicator, "G" or microdot "■", may
or may not be present. Some products may
not follow the Generic Marking.

STYLE 1: PIN 1. BASE 2. EMITTER 3. COLLECTOR STYLE 2: PIN 1. ANODE 2. N/C

3. CATHODE

STYLE 3: PIN 1. ANODE 2. ANODE 3 CATHODE



#### RECOMMENDED MOUNTING FOOTPRINT\*

FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

STILE 5:
PIN 1. GATE
2. SOURCE
<ol><li>DRAIN</li></ol>

**DOCUMENT NUMBER:** 

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**DESCRIPTION:** SC75-3 1.60x0.80x0.80, 1.00P

98ASB15184C

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