General Purpose Transistors

PNP Silicon

MMBT3906TT1

This transistor is designed for general purpose amplifier applications. It is housed in the SOT−416/SC−75 package which is designed for low power surface mount applications.

Features

- NSVM Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- These Devices are Pb−Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS (T_A = 25°C)

Rating	Symbol	Value	Unit
Collector−Emitter Voltage	V_{CEO}	−40	Vdc
Collector−Base Voltage	V_{CBO}	−40	Vdc
Emitter−Base Voltage	V_{EBO}	−5.0	Vdc
Collector Current − Continuous	I_C	−200	mAdc

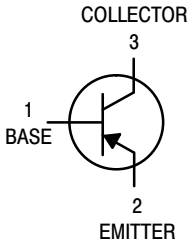
THERMAL CHARACTERISTICS

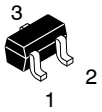
Characteristic	Symbol	Max	Unit
Total Device Dissipation, FR−4 Board (Note 1) @T_A = 25°C Derated above 25°C	P_D	200 1.6	mW mW/°C
Thermal Resistance, Junction−to−Ambient (Note 1)	$R_{\theta JA}$	600	°C/W
Total Device Dissipation, FR−4 Board (Note 2) @T_A = 25°C Derated above 25°C	P_D	300 2.4	mW mW/°C
Thermal Resistance, Junction−to−Ambient (Note 2)	$R_{\theta JA}$	400	°C/W
Junction and Storage Temperature Range	T_J, T_{stg}	−65 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. FR−4 @ Minimum Pad
2. FR−4 @ 1.0 × 1.0 Inch Pad

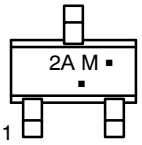
GENERAL PURPOSE AMPLIFIER TRANSISTORS SURFACE MOUNT





CASE 463
SOT−416/SC−75
STYLE 1

MARKING DIAGRAM



2A = Device Code
M = Date Code*
▪ = Pb−Free Package

(Note: Microdot may be in either location)

*Date Code orientation may vary depending up-on manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping†
MMBT3906TT1G	SOT−416 (Pb−Free)	3000 / Tape & Reel
NSVMMBT3906TT1G	SOT−416 (Pb−Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector – Emitter Breakdown Voltage (Note 3) (I_C = –1.0 mAdc, I_B = 0)	$V_{(BR)CEO}$	–40	–	Vdc
Collector – Base Breakdown Voltage (I_C = –10 μAdc, I_E = 0)	$V_{(BR)CBO}$	–40	–	Vdc
Emitter – Base Breakdown Voltage (I_E = –10 μAdc, I_C = 0)	$V_{(BR)EBO}$	–5.0	–	Vdc
Base Cutoff Current (V_{CE} = –30 Vdc, V_{EB} = –3.0 Vdc)	I_{BL}	–	–50	nAdc
Collector Cutoff Current (V_{CE} = –30 Vdc, V_{EB} = –3.0 Vdc)	I_{CEX}	–	–50	nAdc
ON CHARACTERISTICS (Note 3)				
DC Current Gain (I_C = –0.1 mAdc, V_{CE} = –1.0 Vdc) (I_C = –1.0 mAdc, V_{CE} = –1.0 Vdc) (I_C = –10 mAdc, V_{CE} = –1.0 Vdc) (I_C = –50 mAdc, V_{CE} = –1.0 Vdc) (I_C = –100 mAdc, V_{CE} = –1.0 Vdc)	h_{FE}	 60 80 100 60 30	 – – 300 – –	–
Collector – Emitter Saturation Voltage (I_C = –10 mAdc, I_B = –1.0 mAdc) (I_C = –50 mAdc, I_B = –5.0 mAdc)	$V_{CE(sat)}$	 – –	 –0.25 –0.4	Vdc
Base – Emitter Saturation Voltage (I_C = –10 mAdc, I_B = –1.0 mAdc) (I_C = –50 mAdc, I_B = –5.0 mAdc)	$V_{BE(sat)}$	 –0.65 –	 –0.85 –0.95	Vdc
SMALL– SIGNAL CHARACTERISTICS				
Current – Gain – Bandwidth Product (I_C = –10 mAdc, V_{CE} = –20 Vdc, f = 100 MHz)	f_T	250	–	MHz
Output Capacitance (V_{CB} = –5.0 Vdc, I_E = 0, f = 1.0 MHz)	C_{obo}	–	4.5	pF
Input Capacitance1 (V_{EB} = –0.5 Vdc, I_C = 0, f = 1.0 MHz)	C_{ibo}	–	10.0	pF
Input Impedance (V_{CE} = –10 Vdc, I_C = –1.0 mAdc, f = 1.0 kHz)	h_{ie}	2.0	12	k Ω
Voltage Feedback Ratio (V_{CE} = –10 Vdc, I_C = –1.0 mAdc, f = 1.0 kHz)	h_{re}	0.1	10	X 10^{-4}
Small – Signal Current Gain (V_{CE} = –10 Vdc, I_C = –1.0 mAdc, f = 1.0 kHz)	h_{fe}	100	400	–
Output Admittance (V_{CE} = –10 Vdc, I_C = –1.0 mAdc, f = 1.0 kHz)	h_{oe}	3.0	60	μmhos
Noise Figure (V_{CE} = –5.0 Vdc, I_C = –100 μAdc, R_S = 1.0 k Ω, f = 1.0 kHz)	NF	–	4.0	dB
SWITCHING CHARACTERISTICS				
Delay Time	(V_{CC} = –3.0 Vdc, V_{BE} = 0.5 Vdc)	t_d	–	35	ns
Rise Time	(I_C = –10 mAdc, I_{B1} = –1.0 mAdc)	t_r	–	35	
Storage Time	(V_{CC} = –3.0 Vdc, I_C = –10 mAdc)	t_s	–	225	ns
Fall Time	(I_{B1} = I_{B2} = –1.0 mAdc)	t_f	–	75	

3. Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2.0%.

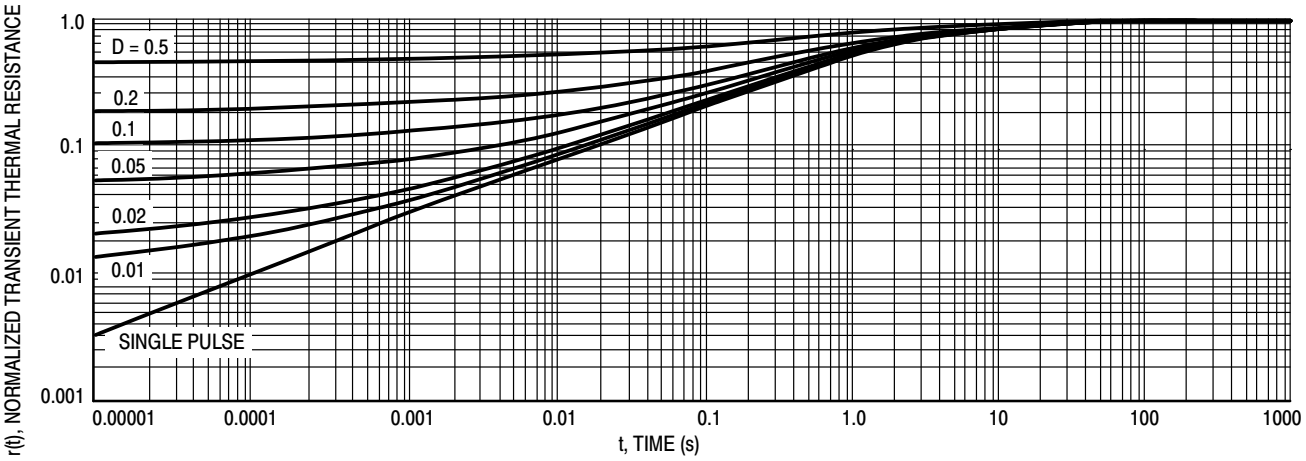


Figure 1. Normalized Thermal Response

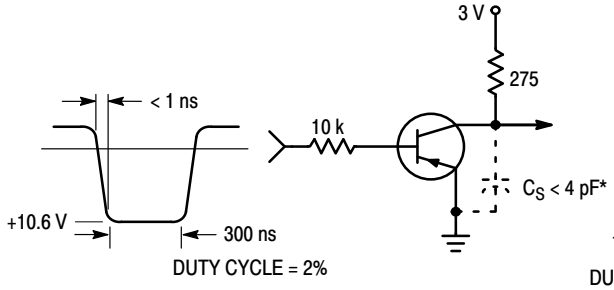


Figure 2. Delay and Rise Time Equivalent Test Circuit

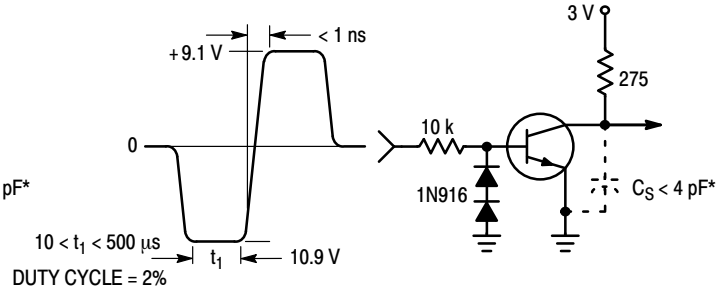


Figure 3. Storage and Fall Time Equivalent Test Circuit

* Total shunt capacitance of test jig and connectors

TYPICAL TRANSIENT CHARACTERISTICS

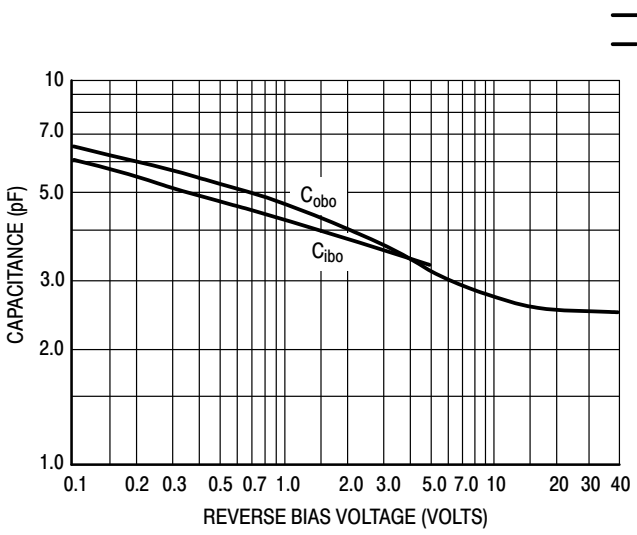


Figure 4. Capacitance

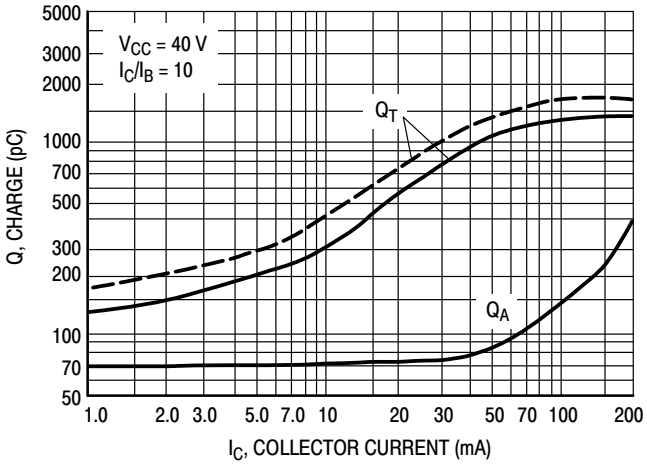


Figure 5. Charge Data

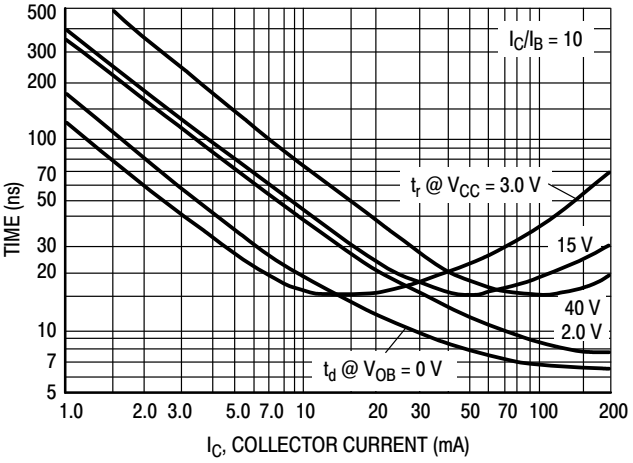


Figure 6. Turn – On Time

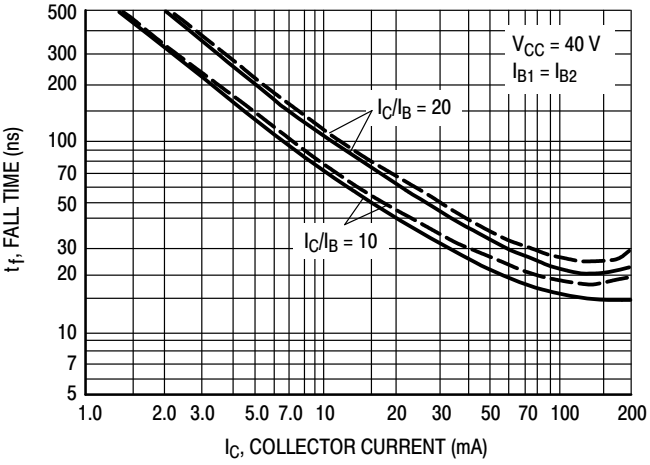


Figure 7. Fall Time

TYPICAL AUDIO SMALL−SIGNAL CHARACTERISTICS
NOISE FIGURE VARIATIONS

(V_{CE} = −5.0 Vdc, T_A = 25°C, Bandwidth = 1.0 Hz)

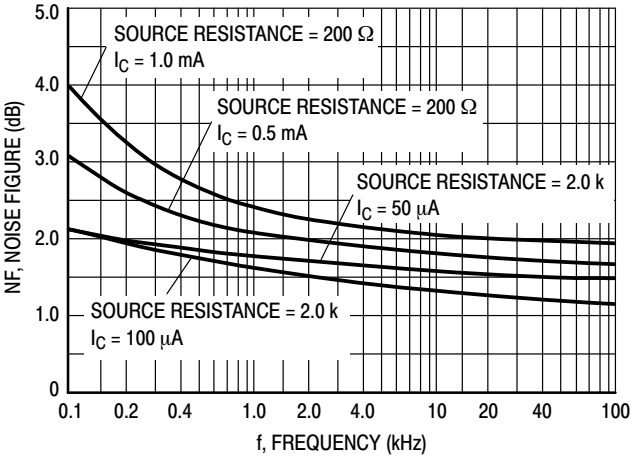


Figure 8.

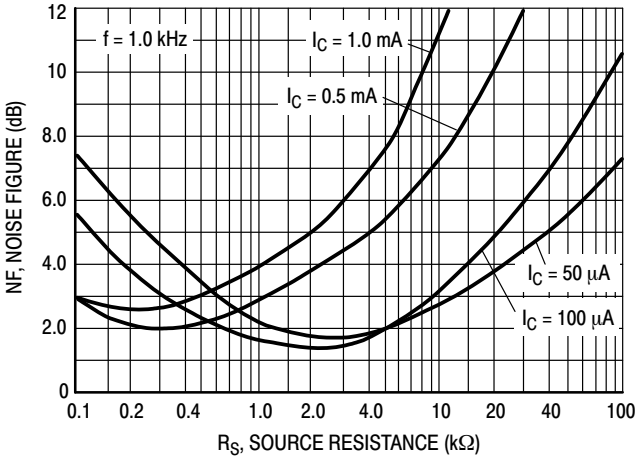


Figure 9.

h PARAMETERS

(V_{CE} = −10 Vdc, f = 1.0 kHz, T_A = 25°C)

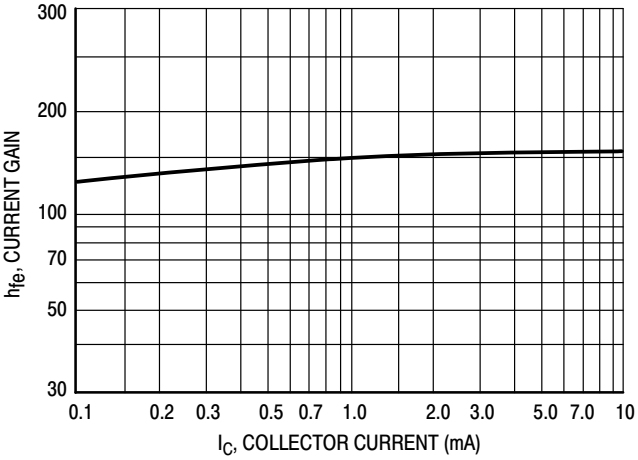


Figure 10. Current Gain

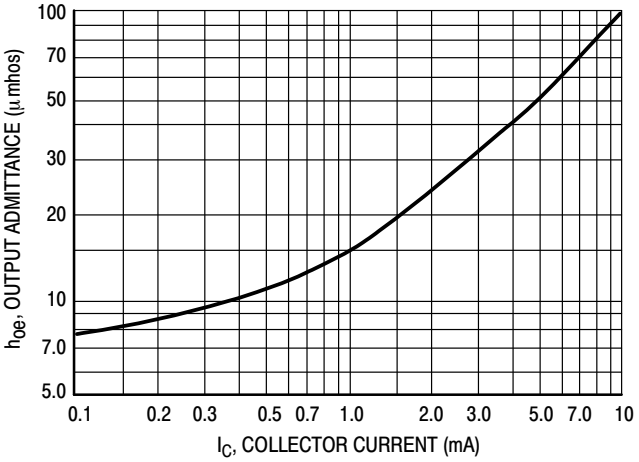


Figure 11. Output Admittance

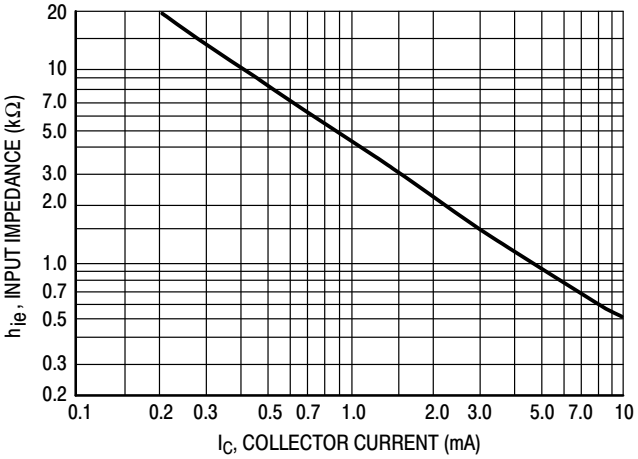


Figure 12. Input Impedance

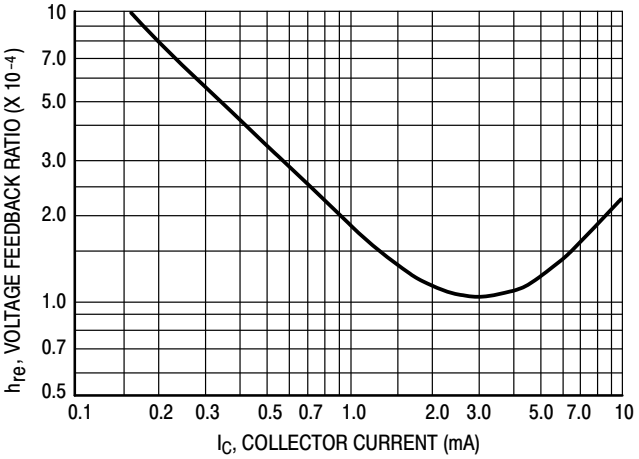


Figure 13. Voltage Feedback Ratio

STATIC CHARACTERISTICS

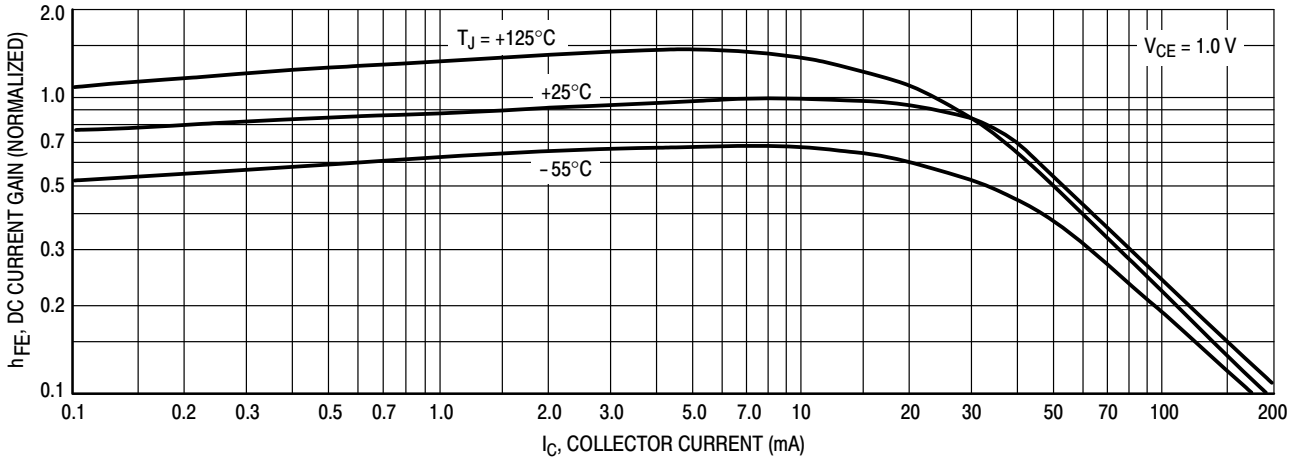


Figure 14. DC Current Gain

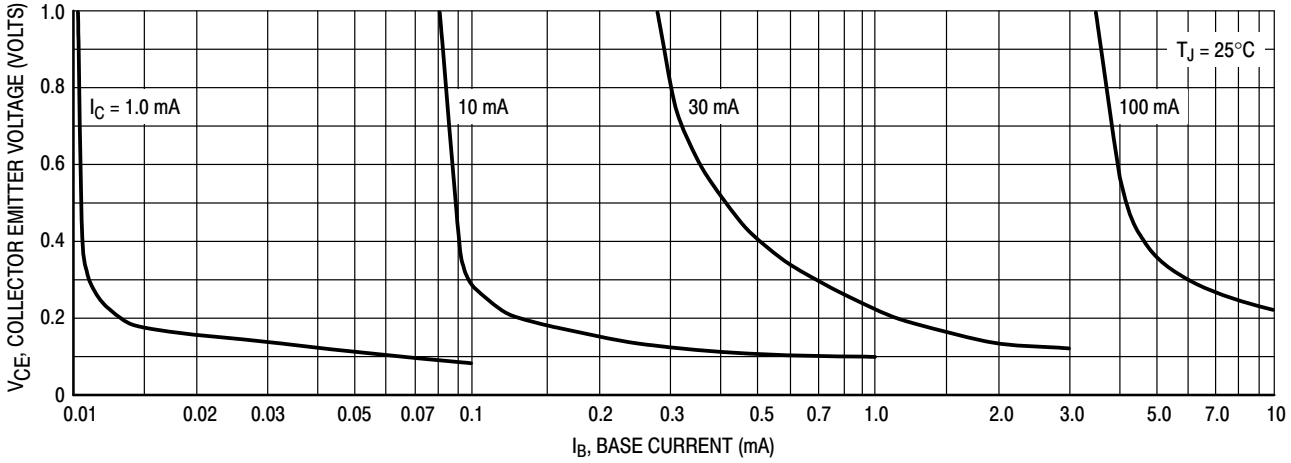


Figure 15. Collector Saturation Region

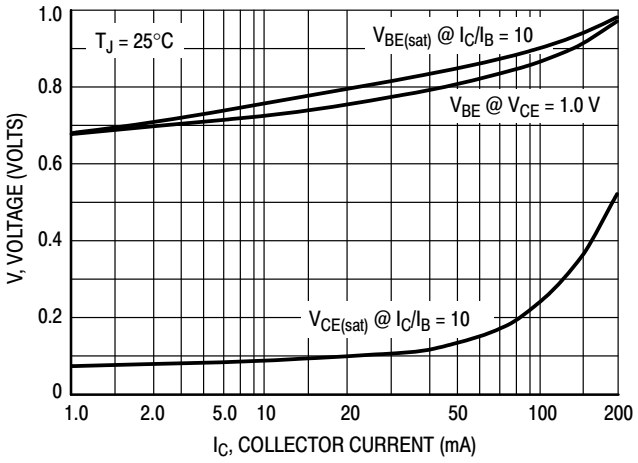


Figure 16. “ON” Voltages

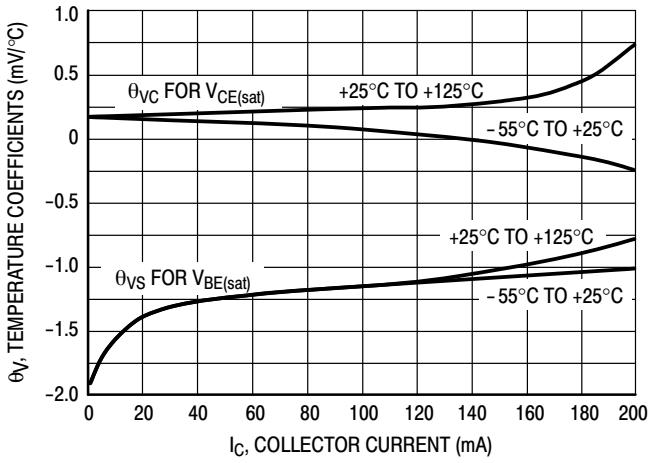
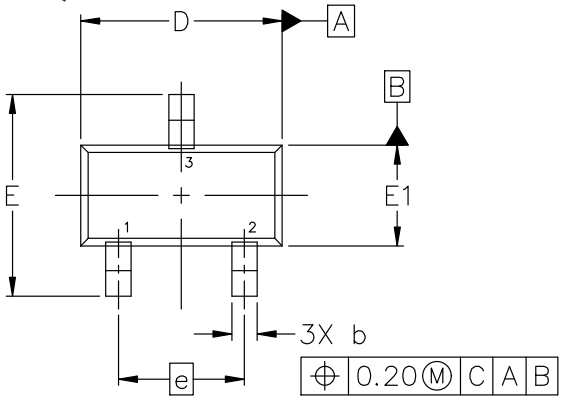


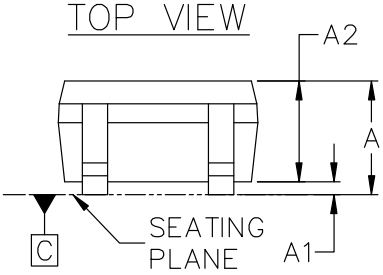
Figure 17. Temperature Coefficients

SC75−3 1.60x0.80x0.80, 1.00P
CASE 463
ISSUE H

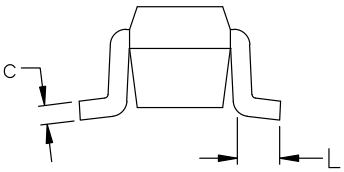
DATE 01 FEB 2024



TOP VIEW



SIDE VIEW



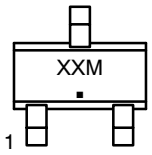
END VIEW

NOTES:

1. DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5−2018.
2. ALL DIMENSION ARE IN MILLIMETERS.

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.70	0.80	0.90
A1	0.00	0.05	0.10
A2		0.80 REF.	
b	0.15	0.20	0.30
c	0.10	0.15	0.25
D	1.55	1.60	1.65
E	1.50	1.60	1.70
E1	0.70	0.80	0.90
e		1.00 BSC	
L	0.10	0.15	0.20

GENERIC
MARKING DIAGRAM*



XX = Specific Device Code
M = Date Code
▪ = Pb−Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb−Free indicator, “G” or microdot “▪”, may or may not be present. Some products may not follow the Generic Marking.

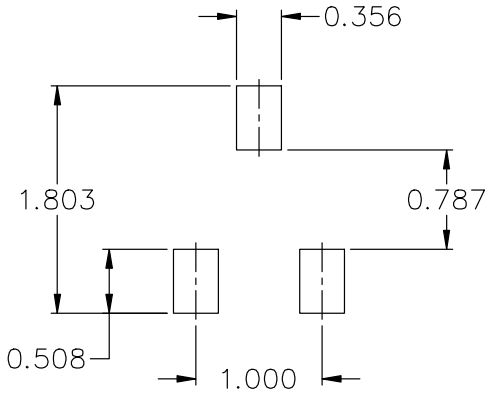
STYLE 1:
PIN 1. BASE
2. EMITTER
3. COLLECTOR

STYLE 2:
PIN 1. ANODE
2. N/C
3. CATHODE

STYLE 3:
PIN 1. ANODE
2. ANODE
3. CATHODE

STYLE 4:
PIN 1. CATHODE
2. CATHODE
3. ANODE

STYLE 5:
PIN 1. GATE
2. SOURCE
3. DRAIN



RECOMMENDED MOUNTING FOOTPRINT*

* FOR ADDITIONAL INFORMATION ON OUR Pb−FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

DOCUMENT NUMBER:	98ASB15184C	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped “CONTROLLED COPY” in red.
DESCRIPTION:	SC75−3 1.60x0.80x0.80, 1.00P	PAGE 1 OF 1

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