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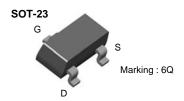


July 2011

## MMBFJ305 N-Channel RF Amplifier

#### **Features**

- This device is designed primarily for electronic switching applications such as low On Resistance analog switching.
- Sourced from process 50.



Note: Drain & Source are interchangeable.

### **Absolute Maximum Ratings\*** T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
$V_{DG}$	Drain-Gate Voltage	30	V
V <sub>GS</sub>	Gate-Source Voltage	-30	V
I <sub>GF</sub>	Forward Gate Current	10	mA
T <sub>J,</sub> T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

<sup>\*</sup> These ratings are limiting values above which the serviceability of any semiconductor device may be impaired. **NOTES:** 

- 1) These ratings are based on a maximum junction temperature of 150 degrees C.
- 2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

### Thermal Characteristics\* T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
P <sub>D</sub>	Total Device Dissipation Derate above 25°C	225 1.8	mW mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	556	°C/W

<sup>\*</sup> Device mounted on FR-4 PCB 1.6" x 1.6" x 0.06".

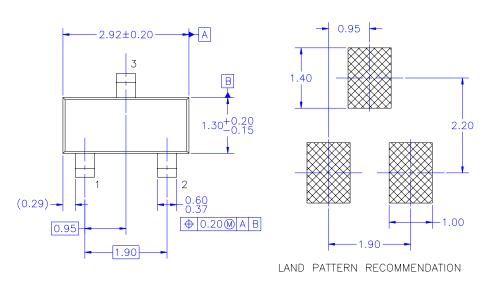
#### **Electrical Characteristics** T<sub>A</sub>=25°C unless otherwise noted

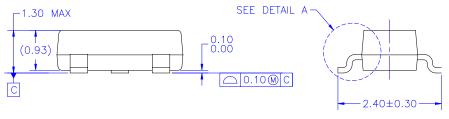
Symbol	Parameter	Conditions	Min.	Max.	Units			
Off Characte	Off Characteristics							
V <sub>(BR)GSS</sub>	Gate-Source Breakdown Voltage	$I_G = -1.0 \mu A, V_{DS} = 0$	-30		V			
I <sub>GSS</sub>	Gate Reverse Current	$V_{GS} = -20V, V_{DS} = 0$		-100	pА			
V <sub>GS</sub> (off)	Gate-Source Cutoff Voltage	$V_{DS} = 15V, I_{D} = 1.0nA$	-0.5	-3.0	V			
On Characte	On Characteristics							
I <sub>DSS</sub>	Zero-Gate Voltage Drain Current*	$V_{DS} = 15V, V_{GS} = 0$	1.0	8.0	mA			
Small Signal Characteristics								
gfs	Forward Transfer Conductance	$V_{DS} = 15V, V_{GS} = 0, f = 1.0kHz$	3000		μmhos			
9oss	Output Conductance	$V_{DS} = 15V, V_{GS} = 0, f = 1.0kHz$		50	μmhos			

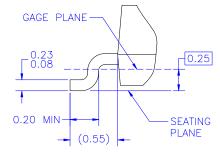
<sup>\*</sup> Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2.0%

### **Physical Dimensions**

### **SOT-23**







DETAIL A

NOTES: UNLESS OTHERWISE SPECIFIED

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  ALL DIMENSIONS ARE IN MILLIMETERS. DIMENSIONS ARE INCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR EXTRUSIONS. DIMENSIONING AND TOLERANCING PER ASME Y14.5M 1994.
  DRAWING FILE NAME: MAO3DREV9

Dimensions in Millimeters





Sync-Lock™

p we

TinyBoost™

TinyBuck™

TinyCalc™

TinyLogic®

TINYOPTO™

TinyPower™

TinyPWM™

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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
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Rev. I56

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