

# MC10H115

## Quad Line Receiver

### Description

The MC10H115 is a quad differential amplifier designed for use in sensing differential signals over long lines. This 10H part is a functional/pinout duplication of the standard MECL 10K™ family part, with 100% improvement in counting frequency and no increase in power supply current.

The base bias supply ( $V_{BB}$ ) is made available at Pin 9 to make the device useful as a Schmitt trigger, or in other applications where a stable reference voltage is necessary. Active current sources provide the MC10H115 with excellent common mode rejection. If any amplifier in a package is not used, one input of that amplifier must be connected to  $V_{BB}$  (Pin 9) to prevent upsetting the current source bias network.

The MC10H115 is designed to be used in sensing differential signals over long lines. The bias supply ( $V_{BB}$ ) is made available to make the device useful as a Schmitt trigger, or in other applications where a stable reference voltage is necessary.

Active current sources provide these receivers with excellent common-mode noise rejection. If any amplifier in a package is not used, one input of that amplifier must be connected to  $V_{BB}$  to prevent unbalancing the current source bias network.

The MC10H115 does not have internal input pulldown resistors. This provides high impedance to the amplifier input and facilitates differential connections.

### Features

- Propagation Delay, 1.0 ns Typical
- Power Dissipation 110 mW Typ/Pkg (No Load)
- Improved Noise Margin 150 mV (Over Operating Voltage and Temperature Range)
- Voltage Compensated
- MECL 10K Compatible

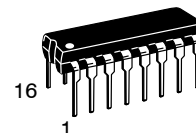
### Applications

- Low Level Receiver
- Voltage Level Interface
- Schmitt Trigger
- These Devices are Pb-Free, Halogen Free and are RoHS Compliant

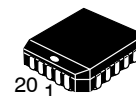


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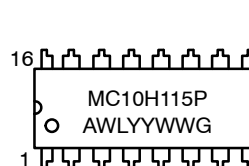


PDIP-16  
P SUFFIX  
CASE 648-08

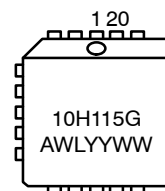


PLL-20  
FN SUFFIX  
CASE 775-02

### MARKING DIAGRAMS\*



PDIP-16



PLL-20

A = Assembly Location  
WL, L = Wafer Lot  
YY, Y = Year  
WW, W = Work Week  
G = Pb-Free Package

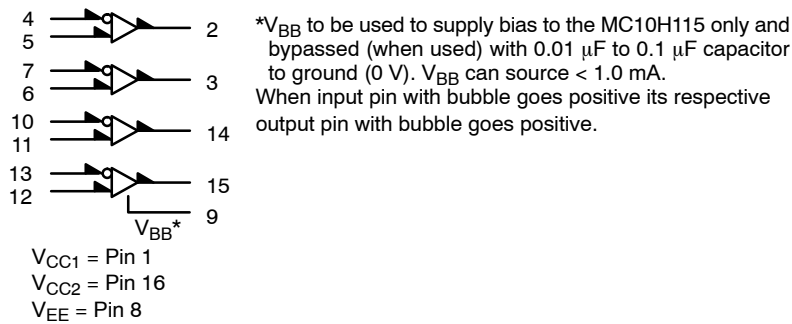
\*For additional marking information, refer to Application Note [AND8002/D](#).

### ORDERING INFORMATION

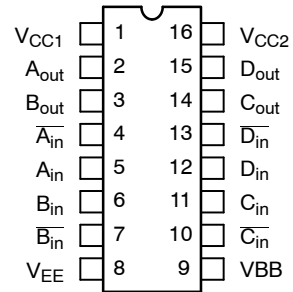
Device	Package	Shipping†
MC10H115FNG	PLL-20 (Pb-Free)	46 Units / Tube
MC10H115FNR2G	PLL-20 (Pb-Free)	500 Tape & Reel
MC10H115PG	PDIP-16 (Pb-Free)	25 Units / Tube

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, [BRD8011/D](#).

# MC10H115



**Figure 1. Logic Diagram**



Pin assignment is for Dual-in-Line Package.

**Figure 2. Pin Assignment**

**Table 1. MAXIMUM RATINGS**

Symbol	Characteristic	Rating	Unit
$V_{EE}$	Power Supply ( $V_{CC} = 0$ )	-8.0 to 0	Vdc
$V_I$	Input Voltage ( $V_{CC} = 0$ )	0 to $V_{EE}$	Vdc
$I_{out}$	Output Current Continuous Surge	50 100	mA
$T_A$	Operating Temperature Range	0 to +75	°C
$T_{stg}$	Storage Temperature Range Plastic Ceramic	-55 to +150 -55 to +165	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

**Table 2. ELECTRICAL CHARACTERISTICS** ( $V_{EE} = -5.2\text{ V} \pm 5\%$ ) (Note 2)

Symbol	Characteristic	0°		25°		75°		Unit
		Min	Max	Min	Max	Min	Max	
$I_E$	Power Supply Current	-	29	-	26	-	29	mA
$I_{inH}$	Input Current High	-	150	-	95	-	95	μA
$I_{CBO}$	Input Leakage Current	-	1.5	-	1.0	-	1.0	μA
$V_{BB}$	Reference Voltage	-1.38	-1.27	-1.35	-1.25	-1.31	-1.19	Vdc
$V_{OH}$	High Output Voltage	-1.02	-0.84	-0.98	-0.81	-0.92	-0.735	Vdc
$V_{OL}$	Low Output Voltage	-1.95	-1.63	-1.95	-1.63	-1.95	-1.60	Vdc
$V_{IH}$	High Input Voltage (Note 1)	-1.17	-0.84	-1.13	-0.81	-1.07	-0.735	Vdc
$V_{IL}$	Low Input Voltage (Note 1)	-1.95	-1.48	-1.95	-1.48	-1.95	-1.45	Vdc
$V_{CMR}$	Common Mode Range (Note 3)	-	-	-2.85 to -0.8		-	-	Vdc
$V_{PP}$	Input Sensitivity (Note 4)	-	-	150 typ		-	-	mV <sub>PP</sub>

- When  $V_{BB}$  is used as the reference voltage.
- Each MECL 10H™ series circuit has been designed to meet the specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 linear fpm is maintained. Outputs are terminated through a 50 Ω resistor to -2.0 V.
- Differential input not to exceed 1.0 Vdc.
- 150 mV<sub>p-p</sub> differential input required to obtain full logic swing on output.

# MC10H115

**Table 3. AC PARAMETERS**

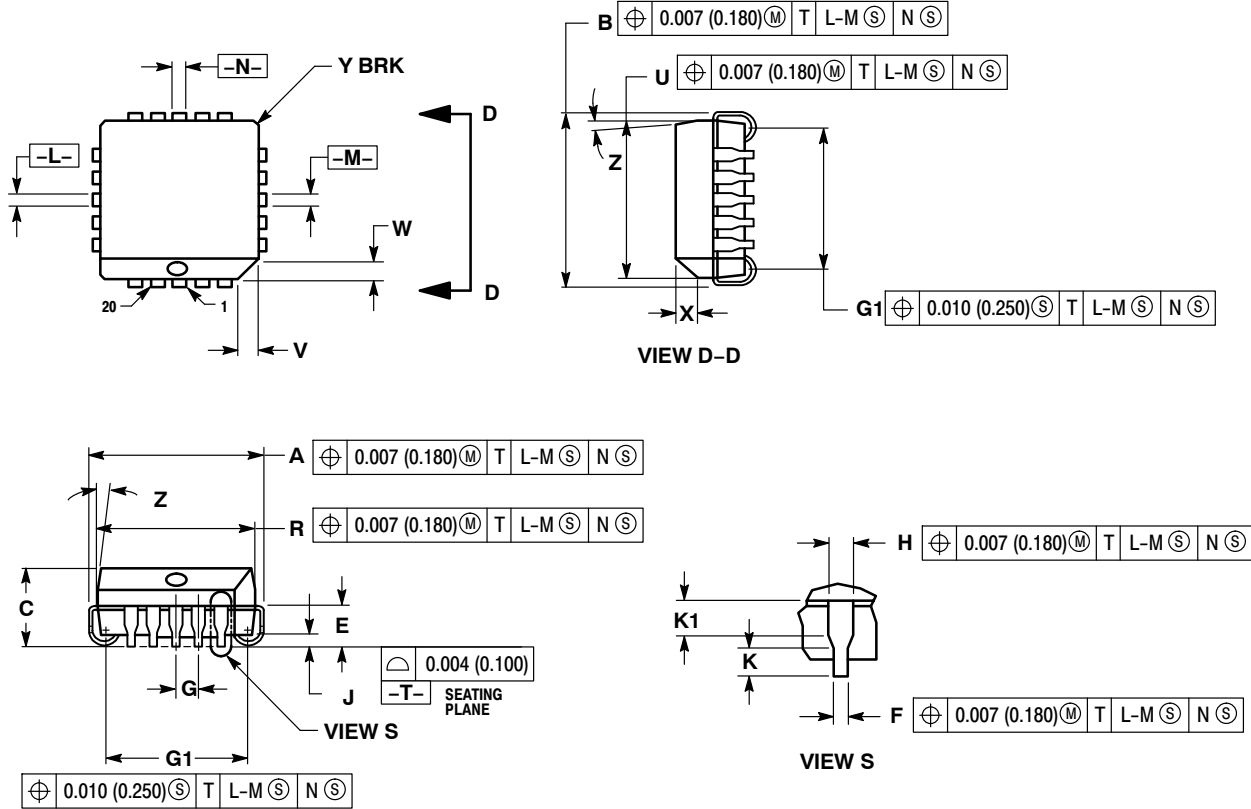
Symbol	Characteristic	0°		25°		75°		Unit
		Min	Max	Min	Max	Min	Max	
$t_{pd}$	Propagation Delay	0.4	1.3	0.4	1.3	0.45	1.45	ns
$t_r$	Rise Time	0.5	1.4	0.5	1.5	0.5	1.6	ns
$t_f$	Fall Time	0.5	1.4	0.5	1.5	0.5	1.6	ns

NOTE: Device will meet the specifications after thermal equilibrium has been established when mounted in a test socket or printed circuit board with maintained transverse airflow greater than 500 lfm. Electrical parameters are guaranteed only over the declared operating temperature range. Functional operation of the device exceeding these conditions is not implied. Device specification limit values are applied individually under normal operating conditions and not valid simultaneously.

# MC10H115

## PACKAGE DIMENSIONS

20 LEAD PLLC  
FN SUFFIX  
CASE 775-02  
ISSUE F



**NOTES:**

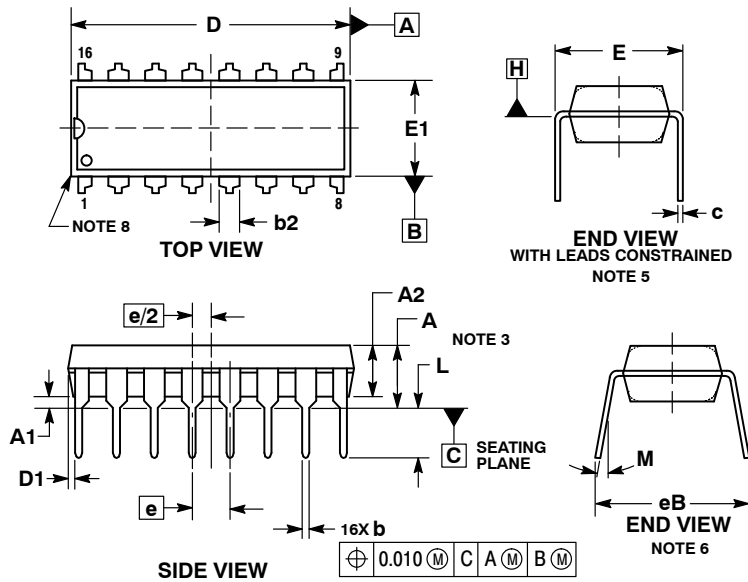
1. DIMENSIONS AND TOLERANCING PER ANSI Y14.5M, 1982.
2. DIMENSIONS IN INCHES.
3. DATUMS -L-, -M-, AND -N- DETERMINED WHERE TOP OF LEAD SHOULDER EXITS PLASTIC BODY AT MOLD PARTING LINE.
4. DIMENSION G1, TRUE POSITION TO BE MEASURED AT DATUM -T-, SEATING PLANE.
5. DIMENSIONS R AND U DO NOT INCLUDE MOLD FLASH. ALLOWABLE MOLD FLASH IS 0.010 (0.250) PER SIDE.
6. DIMENSIONS IN THE PACKAGE TOP MAY BE SMALLER THAN THE PACKAGE BOTTOM BY UP TO 0.012 (0.300). DIMENSIONS R AND U ARE DETERMINED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY EXCLUSIVE OF MOLD FLASH, TIE BAR BURRS, GATE BURRS AND INTERLEAD FLASH, BUT INCLUDING ANY MISMATCH BETWEEN THE TOP AND BOTTOM OF THE PLASTIC BODY.
7. DIMENSION H DOES NOT INCLUDE DAMBAR PROTRUSION OR INTRUSION. THE DAMBAR PROTRUSION(S) SHALL NOT CAUSE THE H DIMENSION TO BE GREATER THAN 0.037 (0.940). THE DAMBAR INTRUSION(S) SHALL NOT CAUSE THE H DIMENSION TO BE SMALLER THAN 0.025 (0.635).

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.385	0.395	9.78	10.03
B	0.385	0.395	9.78	10.03
C	0.165	0.180	4.20	4.57
E	0.090	0.110	2.29	2.79
F	0.013	0.021	0.33	0.53
G	0.050 BSC		1.27 BSC	
H	0.026	0.032	0.66	0.81
J	0.020	----	0.51	----
K	0.025	----	0.64	----
R	0.350	0.356	8.89	9.04
U	0.350	0.356	8.89	9.04
V	0.042	0.048	1.07	1.21
W	0.042	0.048	1.07	1.21
X	0.042	0.056	1.07	1.42
Y	----	0.020	----	0.50
Z	2° 10°		2° 10°	
G1	0.310	0.330	7.88	8.38
K1	0.040	----	1.02	----

# MC10H115

## PACKAGE DIMENSIONS

PDIP-16  
P SUFFIX  
CASE 648-08  
ISSUE V



### NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: INCHES.
3. DIMENSIONS A, A1 AND L ARE MEASURED WITH THE PACKAGE SEATED IN JEDEC SEATING PLANE GAUGE GS-3.
4. DIMENSIONS D, D1 AND E1 DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS. MOLD FLASH OR PROTRUSIONS ARE NOT TO EXCEED 0.10 INCH.
5. DIMENSION E IS MEASURED AT A POINT 0.015 BELOW DATUM PLANE H WITH THE LEADS CONSTRAINED PERPENDICULAR TO DATUM C.
6. DIMENSION eB IS MEASURED AT THE LEAD TIPS WITH THE LEADS UNCONSTRAINED.
7. DATUM PLANE H IS COINCIDENT WITH THE BOTTOM OF THE LEADS, WHERE THE LEADS EXIT THE BODY.
8. PACKAGE CONTOUR IS OPTIONAL (ROUNDED OR SQUARE CORNERS).

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	----	0.210	----	5.33
A1	0.015	----	0.38	----
A2	0.115	0.195	2.92	4.95
b	0.014	0.022	0.35	0.56
b2	0.060 TYP		1.52 TYP	
C	0.008	0.014	0.20	0.36
D	0.735	0.775	18.67	19.69
D1	0.005	----	0.13	----
E	0.300	0.325	7.62	8.26
E1	0.240	0.280	6.10	7.11
e	0.100 BSC		2.54 BSC	
eB	----	0.430	----	10.92
L	0.115	0.150	2.92	3.81
M	----	10°	----	10°

### STYLE 1:

PIN 1. CATHODE	PIN 1. COMMON DRAIN
2. CATHODE	2. COMMON DRAIN
3. CATHODE	3. COMMON DRAIN
4. CATHODE	4. COMMON DRAIN
5. CATHODE	5. COMMON DRAIN
6. CATHODE	6. COMMON DRAIN
7. CATHODE	7. COMMON DRAIN
8. CATHODE	8. COMMON DRAIN
9. ANODE	9. GATE
10. ANODE	10. SOURCE
11. ANODE	11. GATE
12. ANODE	12. SOURCE
13. ANODE	13. GATE
14. ANODE	14. SOURCE
15. ANODE	15. GATE
16. ANODE	16. SOURCE

### STYLE 2:

PIN 1. COMMON DRAIN
2. COMMON DRAIN
3. COMMON DRAIN
4. COMMON DRAIN
5. COMMON DRAIN
6. COMMON DRAIN
7. COMMON DRAIN
8. COMMON DRAIN
9. GATE
10. SOURCE
11. GATE
12. SOURCE
13. GATE
14. SOURCE
15. GATE
16. SOURCE

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