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FAIRCHILD

SEMICONDUCTOR®

KST13/14

Darlington Amplifier Transistor



1. Base 2. Emitter 3. Collector

NPN Epitaxial Silicon Transistor

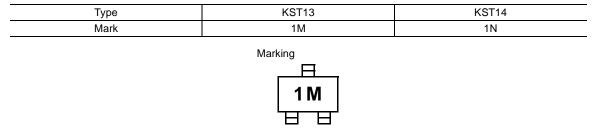
Absolute Maximum Ratings $T_a=25^{\circ}C$ unless otherwise noted

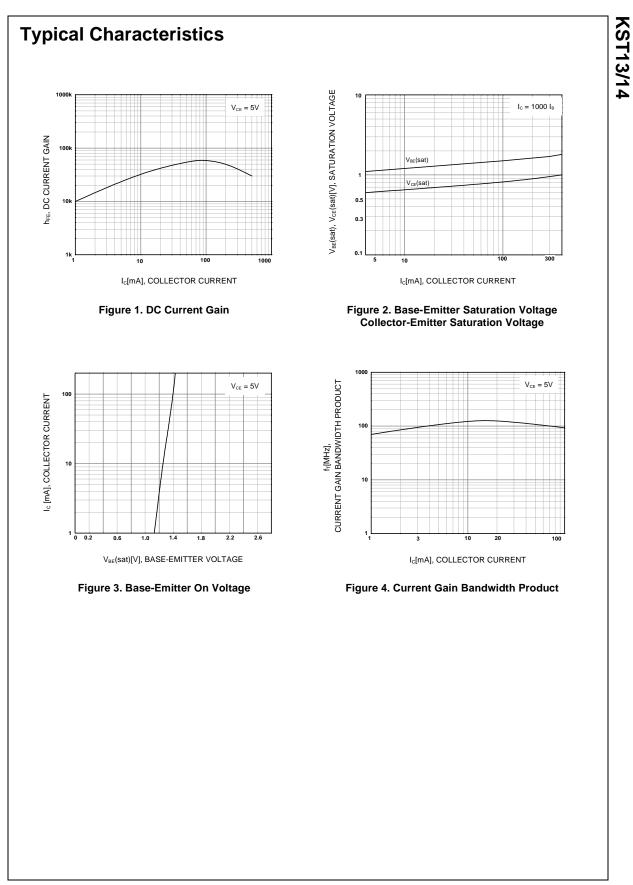
Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	30	V
V _{CES}	Collector-Emitter Voltage	30	V
V _{EBO}	Emitter-Base Voltage	10	V
I _C	Collector Current	300	mA
Pc	Collector Power Dissipation	350	mW
T _{STG}	Storage Temperature	150	°C

Electrical Characteristics $T_a=25^{\circ}C$ unless otherwise noted

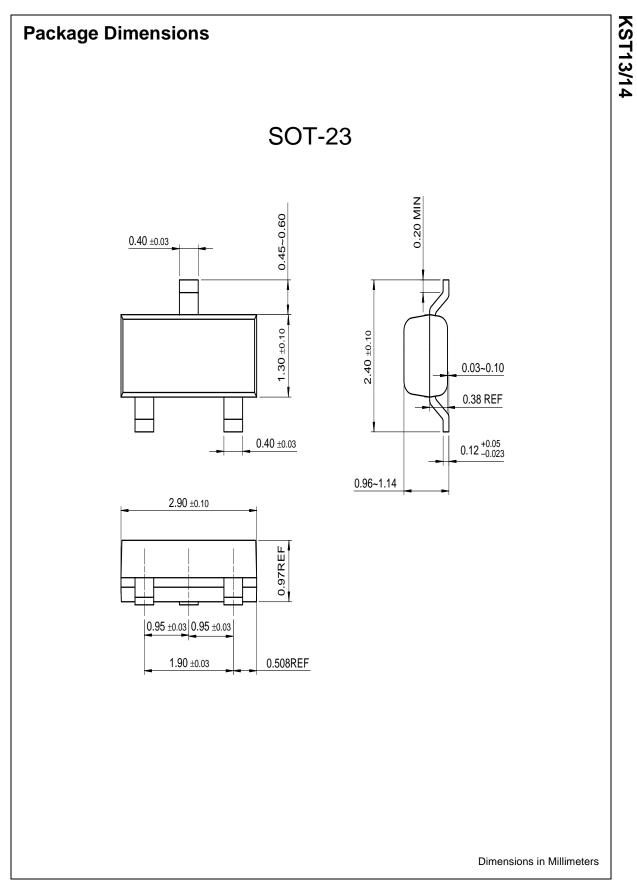
Symbol	Parameter	Test Condition	Min.	Max.	Units
BV _{CES}	Collector-Emitter Breakdown Voltage	I _C =100μA, V _{BE} =0	30		V
I _{CBO}	Collector Cut-off Current	V _{CB} =30V, I _E =0		100	nA
I _{EBO}	Emitter Cut-off Current	V _{EB} =10V, I _C =0		100	nA
h _{FE}	DC Current Gain : KST13 : KST14 : KST13 : KST14	V _{CE} =5V, I _C =10mA V _{CE} =5V, I _C =100mA	5K 10K 10K 20K		
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C =100mA, I _B =0.1mA		1.5	V
V _{BE} (on)	Base-Emitter On Voltage	V _{CE} =5V, I _C =100mA		2.0	V
f _T	Current Gain Bandwidth Product	V _{CE} =5V, I _C =10mA f=100MHz	125		MHz

Marking Code





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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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