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### KSD985/986

### **Low Frequency Power Amplifier**

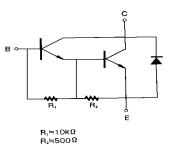
• Low Speed Switching Industrial Use



## **NPN Epitaxial Silicon Darlington Transistor**

### Absolute Maximum Ratings T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CBO</sub>	Collector-Base Voltage	150	V
V <sub>CEO</sub>	Collector-Emitter Volage		
	: KSD985	60	V
	: KSD986	80	V
V <sub>EBO</sub>	Emitter-Base Voltage	8.0	V
I <sub>C</sub>	Collector Current (DC)	1.5	Α
I <sub>CP</sub>	*Collector Current (Pulse)	3.0	Α
I <sub>B</sub>	Base Current	0.15	Α
P <sub>C</sub>	Collector Dissipation (T <sub>a</sub> =25°C)	1.0	W
P <sub>C</sub>	Collector Dissipation (T <sub>C</sub> =25°C)	10	W
T <sub>J</sub>	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	- 55 ~ 150	°C



### Electrical Characteristics T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
I <sub>CBO</sub>	Collector Cut-off Current	$V_{CB} = 60V, I_{E} = 0$			10	μΑ
I <sub>CER</sub>	Collector Cut-off Current	$V_{CE} = 60V, R_{BE} = 51\Omega$ @ $T_{C} = 125^{\circ}C$			1.0	mA
I <sub>CEX1</sub>	Collector Cut-off Current	$V_{CE} = 60V, V_{BE}(off) = -1.5A$ $V_{CE} = 60V, V_{BE}(off) = -1.5A$ @ $T_{C} = 125^{\circ}C$			10 1.0	μA mA
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{EB} = 5V, I_{C} = 0$			1.0	mA
h <sub>FE1</sub> h <sub>FE2</sub>	*DC Current Gain	$V_{CE} = 2V, I_{C} = 0.5A$ $V_{CE} = 2V, I_{C} = 1A$	1000 2000		30000	
V <sub>CE</sub> (sat)	*Collector-Emitter Saturation Voltage	$I_C = 1A$ , $I_B = 1mA$			1.5	V
V <sub>BE</sub> (sat)	*Base-Emitter Saturation Voltage	$I_C = 1A$ , $I_B = 1mA$			2.0	V
t <sub>ON</sub>	Turn ON Time	$V_{CC} = 50V, I_{C} = 1A$		0.5		μs
t <sub>STG</sub>	Storage Time	$I_{B1} = -I_{B2} = 1mA$		1.0		μs
t <sub>F</sub>	Fall Time	$R_L = 50\Omega$		1.0		μs

<sup>\*</sup> Pulse Test: PW≤350μs, Duty Cycle≤2%

### **h**<sub>FE</sub> Classification

Classification	R	0	Y
h <sub>FE2</sub>	2000 ~ 5000	4000 ~ 10000	8000 ~ 30000

<sup>\*</sup> PW≤300μs, Duty Cycle10%

# **Typical Characteristics**

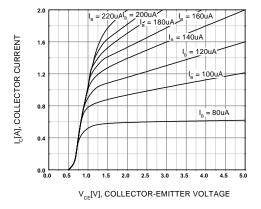


Figure 1. Static Characteristic

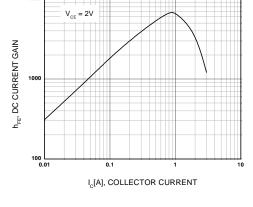


Figure 2. DC current Gain

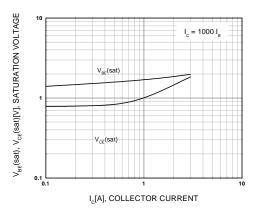


Figure 3. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

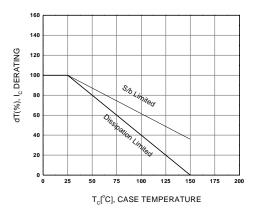


Figure 4. Derating Curve Of Safe Operating Areas

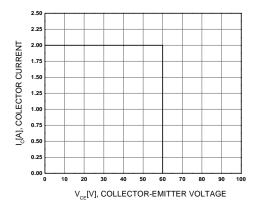


Figure 5. Reverse Bias Safe Operating Areas

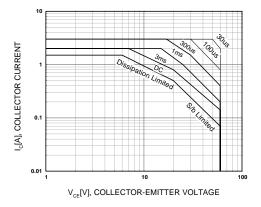


Figure 6. Safe Operating Area

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# Typical Characteristics (Continued)

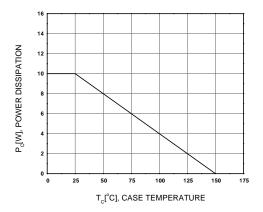


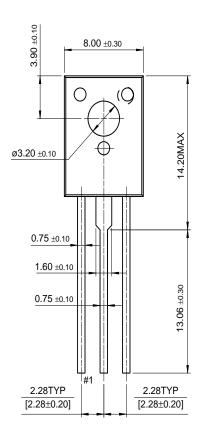
Figure 7. Power Derating

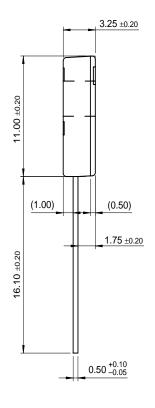
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KSD985/986

# **Package Demensions**

TO-126





Dimensions in Millimeters

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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