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# FPF1C2P5MF07AM F1 Module solution for PV-Application

## **General Description**

Fairchild's brand-new DC-AC module is designed for a power stage that needs more compact design. And the Press-fit technology provides simple and reliable mounting. This module is optimized for the application such as solar inverter where a high efficiency and robust design are needed.

## **Electrical Features**

- High Efficiency
- Low Conduction and Switching losses
- Low V<sub>CE(sat)</sub>: 1.1 V typ. @ Ic = 30 A
- Low  $R_{DS(ON)}$  : 90 m $\Omega$  max.
- Fast Recovery Body Diode

## **Mechanical Features**

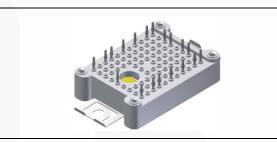
- Compact size : F1 Package
- Press-fit contact technology

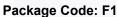
### Applications

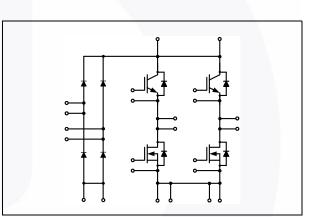
Solar Inverter

## Certification

• UL approved (E209204)







## Internal Circuit Diagram

## Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted.

Symbol	Description		Rating	Units
Rectifier Di	iode			
V <sub>RRM</sub>	Peak Repetitive Reverse Voltage		620	V
I <sub>Fav</sub>	Diode Continuous Forward Current	@ T <sub>C</sub> = 80°C	27	А
I <sub>FSM</sub>	Diode Maximum Forward Surge Current		245	A
l <sup>2</sup> t	I <sup>2</sup> t value		300	A <sup>2</sup> s
P <sub>D</sub>	Maximum Power Dissipation	@ T <sub>C</sub> = 25°C	77	W
TJ	Operating Junction Temperature		-40 to +150	°C

July. 2014

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PF1C2P5MF07AM F1 Module solution for PV-Application
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#### Symbol Units Description Rating **High-side IGBT** V V<sub>CES</sub> Collector-Emitter Voltage 620 Gate-Emitter Voltage ± 20 V V<sub>GES</sub> **Collector Current** @ T<sub>C</sub> = 80°C 39 А lc Pulsed Collector Current 90 А $I_{CM}$ Diode Continuous Forward Current $I_{F}$ @ $T_{\rm C} = 80^{\circ}{\rm C}$ 22 А **Diode Maximum Forward Current** 90 А I<sub>FM</sub> @ T<sub>C</sub> = 25°C 231 W $P_D$ Maximum Power Dissipation -40 to +150 °C $T_{J}$ **Operating Junction Temperature** Low-side MOSFET V V<sub>DSS</sub> Drain-Source Voltage 620 ± 20 V V<sub>GSS</sub> Gate-Source Voltage Continuous Drain Current @ $T_{C} = 25^{\circ}C$ 36 А $I_D$ @ $T_{C} = 80^{\circ}C$ 27 А **Pulsed Drain Current** Limited by T<sub>J</sub> max. 156 А I<sub>DM</sub> Continuous Source-Drain Forward Current 36 А $I_{S}$ Maximum Pulsed Source-Drain Forward Current 156 I<sub>SM</sub> А $\mathsf{P}_\mathsf{D}$ Maximum Power Dissipation @ T<sub>C</sub> = 25°C 250 W °C ТJ **Operating Junction Temperature** -40 to +150 Module -40 to +125 °C Storage Temperature T<sub>STG</sub> @ AC 1<sub>MIN</sub> VISO Isolation Voltage 2500 V Iso. Material Internal Isolation Material Al<sub>2</sub>O<sub>3</sub> Mounting Force per Clamp 20 to 50 Ν **F**MOUNT Weight 22 Тур. g Terminal to Heatsink 11.5 mm Creepage Terminal to Terminal 6.3 mm Clearance Terminal to Heatsink 10.0 mm Terminal to Terminal 5.0 mm

Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted. (Continued)

# Package Marking and Ordering Information

Device	Device Marking	Package	Packing Type	Quantity / Tray
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Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
Rectifier	Diode					
V <sub>F</sub>	Diode Forward Voltage	I <sub>F</sub> = 30 A	-	-	1.9	V
		I <sub>F</sub> = 30 A @T <sub>C</sub> = 125°C	-	1.45	-	V
I <sub>R</sub>	Reverse Leakage Current	V <sub>R</sub> = 620 V	-	-	25	μA
$R_{\theta JC}$	Thermal Resistance of Junction to Case	per Diode	-	-	1.62	°C/W
High-side						
Off Charac	Collector-Emitter Breakdown Voltage	V <sub>GE</sub> = 0 V, I <sub>C</sub> = 1 mA	620		r	V
BV <sub>CES</sub>	Collector Cut-off Current			-	- 25	-
ICES		$V_{CE} = V_{CES}, V_{GE} = 0 V$	-	-	-	μA
I <sub>GES</sub>	Gate-Emitter Leakage Current	$V_{GE} = V_{GES}, V_{CS} = 0 V$	-	-	2.5	μA
On Charac	teristics					
V <sub>GE(th)</sub>	Gate-Emitter Threshold Voltage	$V_{GE} = V_{CE}$ , $I_C = 30$ mA	4	5.7	7	V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	$I_{\rm C} = 30$ A, $V_{\rm GE} = 15$ V	-	1.1	1.6	V
02(000)		$I_{\rm C}$ = 30 A, $V_{\rm GE}$ = 15 V @T <sub>C</sub> = 125°C	-	1.0	-	V
		I <sub>C</sub> = 60 A, V <sub>GE</sub> = 15 V	-	1.4	-	V
Switching	Characteristics					
Qg	Total Gate Charge	$V_{DS}$ = 380 V, $V_{GS}$ = 0V+15 V, I <sub>D</sub> = 30 A		214	-	nC
R <sub>θJC</sub>	Thermal Resistance of Junction to Case	per IGBT		-	0.54	°C/W

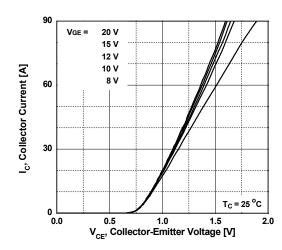
High-Sic	te FWD					
V <sub>FM</sub>	Diode Forward Voltage	I <sub>F</sub> = 15 A, V <sub>GS</sub> = 0 V	-	1.75	2.25	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> = 15 A	-	30	-	ns
l <sub>rr</sub>	Reverse Recovery Current	dI <sub>F</sub> /dt = 1650 A/μs		27	-	A
Q <sub>rr</sub>	Reverse Recovery Charge		-	405	-	nC
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> = 15 A	-	43		ns
Irr	Reverse Recovery Current	dI <sub>F</sub> /dt = 1500 A/μs @T <sub>C</sub> = 125°C	-	38	-	Α
Q <sub>rr</sub>	Reverse Recovery Charge		-	814	-	nC
$R_{\theta JC}$	Thermal Resistance of Junction to Case	per Diode	-	-	1.61	°C/W

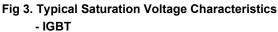
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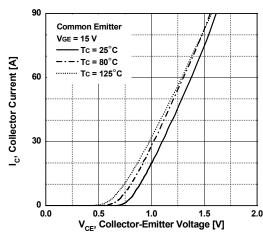
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
Low-Side	MOSFET					
Off Charac	cteristics					
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 1 mA	620	-	-	V
IDSS	Zero Gate Voltage Drain Current	$V_{DS} = 620 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	25	μA
I <sub>GSS</sub>	Gate-Body Leakage Current, Forward	V <sub>GS</sub> = 20 V, V <sub>DS</sub> = 0 V			2.5	μA
On Charac			1	1	1	
V <sub>GS(th)</sub>	Gate-Source Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250 \text{ mA}$	2.7	3.8	5.3	V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	I <sub>D</sub> = 27 A, V <sub>GS</sub> = 10 V	-	-	90	mΩ
		$I_D = 27 \text{ A}, V_{GS} = 10 \text{ V} @T_C = 125^{\circ}\text{C}$	-	135	-	mΩ
		$I_{D}$ = 47 A, $V_{GS}$ = 10 V	-	76	-	mΩ
$V_{SD}$	Source-Drain Diode Forward Voltage	I <sub>SD</sub> = 27 A, V <sub>GS</sub> = 0 V	-	-	1.5	V
		I <sub>SD</sub> = 47 A, V <sub>GS</sub> = 0 V	-	1.3	-	V
Switching	Characteristics					
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>CC</sub> = 380 V	-	57	-	ns
t <sub>r</sub>	Rise Time	I <sub>D</sub> = 27 A V <sub>GS</sub> = 10 V	-	14	-	ns
t <sub>d(off)</sub>	Turn-Off Delay Time		-	240	-	ns
t <sub>f</sub>	Fall Time	$R_{G} = 10 \Omega$ Inductive Load	-	20	-	ns
E <sub>ON</sub>	Turn-On Switching Loss per Pulse	$T_{\rm C} = 25^{\circ}{\rm C}$	-	440	-	μJ
E <sub>OFF</sub>	Turn-Off Switching Loss per Pulse	_	-	113	-	μJ
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>CC</sub> = 380 V	-	53	-	ns
t <sub>r</sub>	Rise Time	$I_D = 27 A$	-	16	-	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	$-V_{GS} = 10 V$	-	257	-	ns
t <sub>f</sub>	Fall Time	_ R <sub>G</sub> = 10 Ω Inductive Load	-	20	-	ns
E <sub>ON</sub>	Turn-On Switching Loss per Pulse	$T_{\rm C} = 125^{\circ}{\rm C}$	-	719	-	μJ
E <sub>OFF</sub>	Turn-Off Switching Loss per Pulse	_	-	124	-	μJ
Qg	Total Gate Charge	V <sub>DS</sub> = 380 V, V <sub>GS</sub> = 0V+10 V, I <sub>D</sub> = 27 A	-	155	-	nC
R <sub>θJC</sub>	Thermal Resistance of Junction to Case	per Chip	-	-	0.5	°C/W

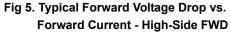
# **Typical Performance Characteristic**

## Fig 1. Typical Output Characteristics - IGBT









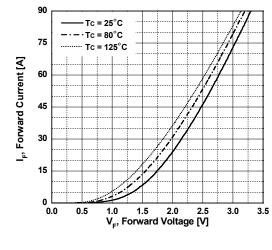


Fig 2. Typical Output Characteristics - IGBT

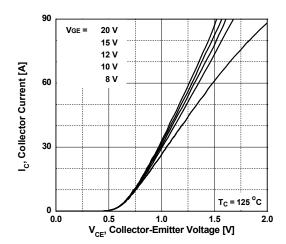
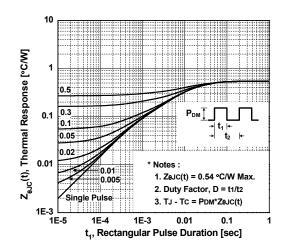
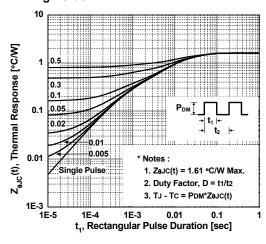


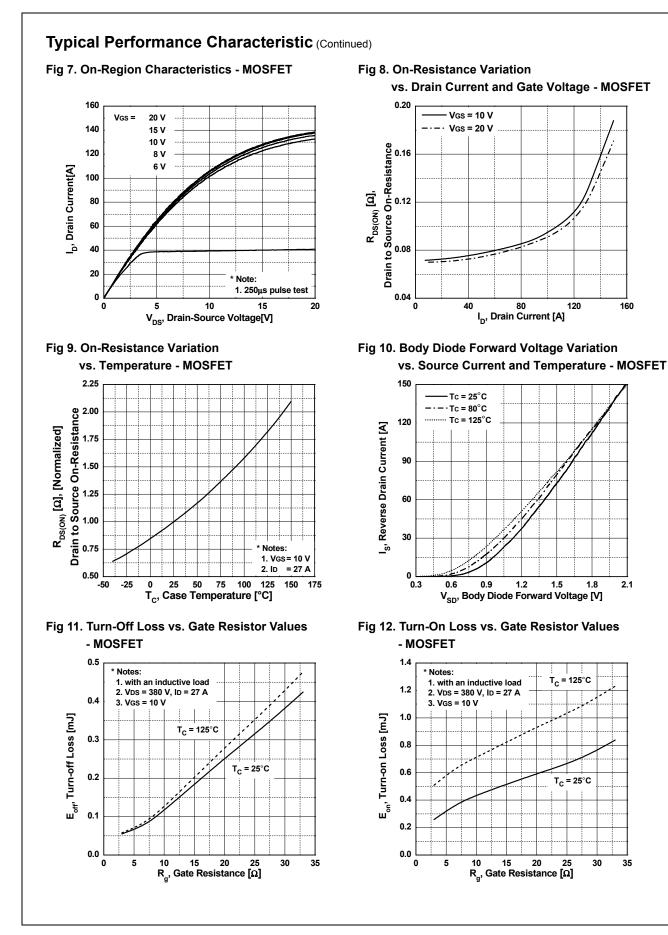
Fig 4. Transient Thermal Response Curve - IGBT







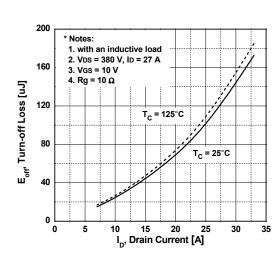
FPF1C2P5MF07AM F1 Module solution for PV-Application

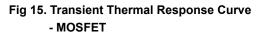


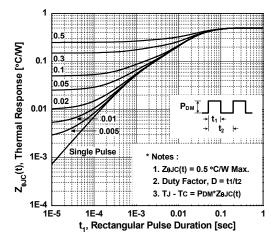
## Typical Performance Characteristic (Continued)

Fig 13. Turn-Off Loss vs. Drain Current - MOSFET

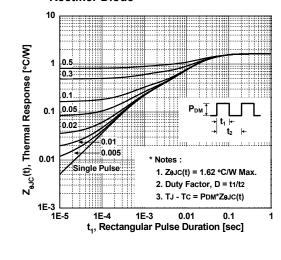
Fig 14. Turn-On Loss vs. Drain Current - MOSFET

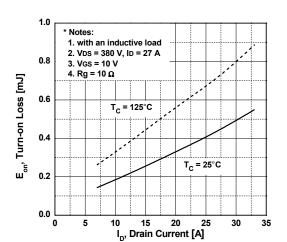




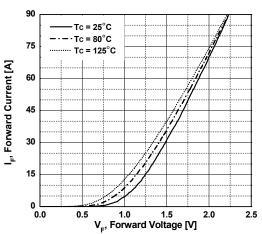


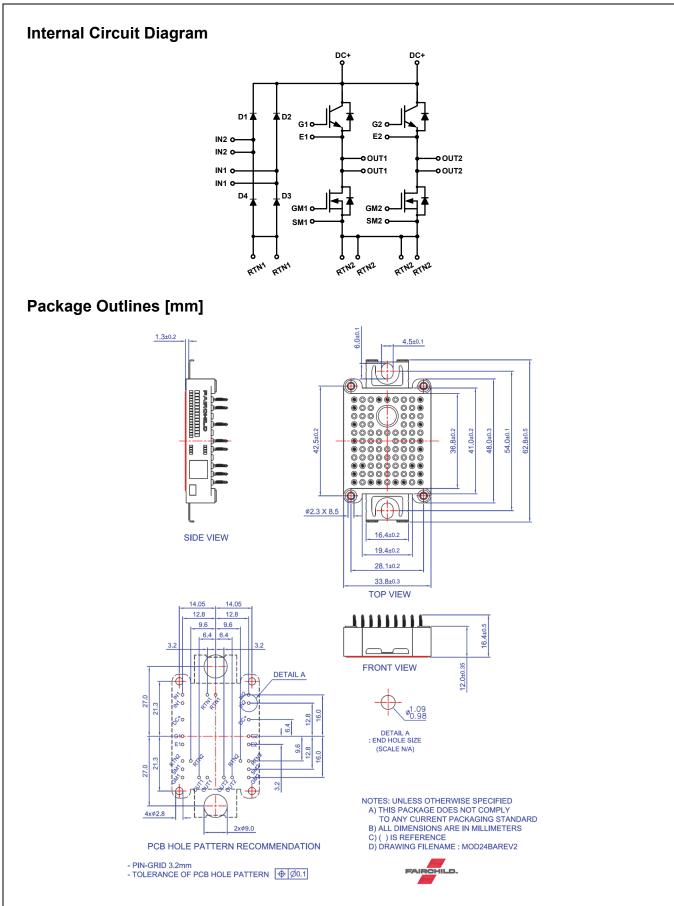














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Definition of Terms						
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No Identification Needed Full Production		Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.				
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