

Is Now Part of



# **ON Semiconductor**®

# To learn more about ON Semiconductor, please visit our website at <u>www.onsemi.com</u>

Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (\_), the underscore (\_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (\_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at <a href="mailto:www.onsemi.com">www.onsemi.com</a>. Please email any questions regarding the system integration to <a href="mailto:Fairchild\_questions@onsemi.com">Fairchild\_questions@onsemi.com</a>.

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or unavteries, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out or i, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor and is officers, employees, uniotificated use, even if such claim any manner.



## FDMA86108LZ

### Single N-Channel PowerTrench<sup>®</sup> MOSFET

### 100 V, 2.2 A, 243 m $\Omega$

### Features

- Max r<sub>DS(on)</sub> = 243 mΩ at V<sub>GS</sub> = 10 V, I<sub>D</sub> = 2.2 A
- Max  $r_{DS(on)}$  = 366 m $\Omega$  at V<sub>GS</sub> = 4.5 V, I<sub>D</sub> = 1.8 A
- Low Profile 0.8 mm Maximum in the New Package MicroFET 2x2 mm
- Free from Halogenated Compounds and Antimony Oxides
- RoHS Compliant

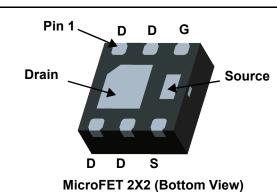


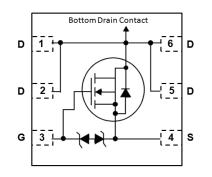
### General Description This device has been designed to p

This device has been designed to provide maximum efficiency and thermal performance for synchronous buck converters. The low  $r_{DS(on)}$  and gate charge provide excellent switching performance.

### Application

■ DC – DC Buck Converters





# MOSFET Maximum Ratings T<sub>A</sub> = 25 °C unless otherwise noted

Symbol	Parameter			Ratings	Units	
V <sub>DS</sub>	Drain to Source Voltage			100	V	
V <sub>GS</sub>	Gate to Source Voltage			±20	V	
ID	Drain Current -Continuous	T <sub>A</sub> = 25 °C	(Note 1a)	2.2	٨	
	-Pulsed		(Note 3)	6	Α	
P <sub>D</sub>	Power Dissipation	T <sub>A</sub> = 25 °C	(Note 1a)	2.4		
	Power Dissipation	T <sub>A</sub> = 25 °C	(Note 1b)	0.9		
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range			-55 to +150	°C	

### **Thermal Characteristics**

$R_{ ext{ heta}JA}$	Thermal Resistance, Junction-to-Ambient	(Note 1a)	52	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	(Note 1b)	145	0/00

### Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
108	FDMA86108LZ	MicroFET 2X2	7 "	8 mm	3000 units

March 2015

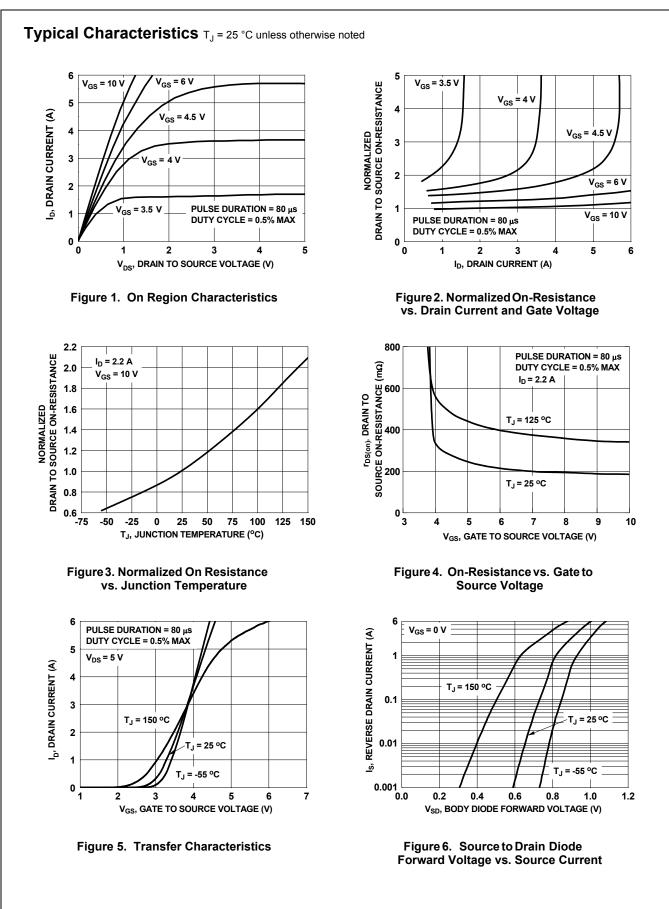
В
ž
IA8610
86
10
18
08LZ \$
βu
Single N
-Ch
<sup>s</sup>
Ξ
ne
annel Pow
õ
n
Ť
renc
nc
ĥ
_
MOSFET
Ň
щ
-

$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$ $\frac{\Delta BV_{DSS}}{\Delta T_{J}}$ $\frac{I_{DSS}}{I_{GSS}}$ $On Charac$ $V_{GS(th)}$ $\Delta V_{GS(th)}$	Drain to Source Breakdown Voltage Breakdown Voltage Temperature Coefficient Zero Gate Voltage Drain Current Gate to Source Leakage Current	$I_{D} = 250 \ \mu\text{A}, \ V_{GS} = 0 \ V$ $I_{D} = 250 \ \mu\text{A}, \ \text{referenced to } 25 \ ^{\circ}\text{C}$ $V_{DS} = 80 \ V, \ V_{GS} = 0 \ V$ $V_{GS} = \pm 20 \ V, \ V_{DS} = 0 \ V$ $V_{GS} = V_{DS}, \ I_{D} = 250 \ \mu\text{A}$ $I_{D} = 250 \ \mu\text{A}, \ \text{referenced to } 25 \ ^{\circ}\text{C}$	100	2.2	1 ±10	V mV/°C μΑ μΑ
$ \frac{\Delta BV_{DSS}}{\Delta T_{J}} $ $ \frac{\Delta T_{J}}{DSS} $ $ \frac{I_{GSS}}{I_{GSS}} $ $ On Charac $ $ \frac{V_{GS(th)}}{\Delta V_{GS(th)}} $	Breakdown Voltage Temperature Coefficient Zero Gate Voltage Drain Current Gate to Source Leakage Current Eteristics Gate to Source Threshold Voltage Gate to Source Threshold Voltage	$I_{D} = 250 \ \mu\text{A}, \text{ referenced to } 25 \ ^{\circ}\text{C}$ $V_{DS} = 80 \ \text{V}, \ V_{GS} = 0 \ \text{V}$ $V_{GS} = \pm 20 \ \text{V}, \ V_{DS} = 0 \ \text{V}$ $V_{GS} = V_{DS}, \ I_{D} = 250 \ \mu\text{A}$			±10	mV/°C μA
ΔT <sub>J</sub> I <sub>DSS</sub> I <sub>GSS</sub> On Charac V <sub>GS(th)</sub> ΔV <sub>GS(th)</sub>	Coefficient Zero Gate Voltage Drain Current Gate to Source Leakage Current :teristics Gate to Source Threshold Voltage Gate to Source Threshold Voltage	$V_{DS} = 80 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$ $V_{GS} = \pm 20 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$ $V_{GS} = V_{DS}, \text{ I}_{D} = 250 \mu\text{A}$	1.0		±10	μA
I <sub>GSS</sub> On Charac V <sub>GS(th)</sub> ΔV <sub>GS(th)</sub>	Gate to Source Leakage Current teristics Gate to Source Threshold Voltage Gate to Source Threshold Voltage	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$ $V_{GS} = V_{DS}, I_D = 250 \mu\text{A}$	1.0	2.2	±10	
On Charac V <sub>GS(th)</sub> ΔV <sub>GS(th)</sub>	Gate to Source Threshold Voltage Gate to Source Threshold Voltage	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> = 250 μA	1.0	2.2	1	μA
$V_{GS(th)}$ $\Delta V_{GS(th)}$	Gate to Source Threshold Voltage Gate to Source Threshold Voltage		1.0	2.2	1	
$V_{GS(th)}$ $\Delta V_{GS(th)}$	Gate to Source Threshold Voltage Gate to Source Threshold Voltage		1.0	2.2	1	
$\Delta V_{GS(th)}$	-	$I_D$ = 250 $\mu$ A, referenced to 25 °C			3.0	V
				-5		mV/°C
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 2.2 A		188	243	
r	Static Drain to Source On Resistance	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 1.8 A		275	366	mΩ
r <sub>DS(on)</sub> S	Static Drain to Source On Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 2.2 A, T <sub>J</sub> = 125 °C		345	446	
9 <sub>FS</sub>	Forward Transconductance	V <sub>DD</sub> = 5 V, I <sub>D</sub> = 2.2 A		3.7		S
Dvnamic C	haracteristics					
•	Input Capacitance			116	163	pF
C <sub>oss</sub>	Output Capacitance	$V_{\rm DS} = 50  \rm V,  V_{\rm GS} = 0  \rm V,$		23	35	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	f = 1 MHz		1	5	pF
	Gate Resistance		0.1	1.0	3.0	Ω
*	Characteristics					
	Characteristics Turn-On Delay Time			4.2	10	
u(011)	Rise Time			4.2	10	ns
-1		V <sub>DD</sub> = 50V, I <sub>D</sub> = 2.2 A, V <sub>GS</sub> = 10 V, R <sub>GEN</sub> = 6 Ω			-	ns
u(011)	Turn-Off Delay Time Fall Time	V <sub>GS</sub> = 10 V, 1V <sub>GEN</sub> = 0.52		7.6	15 10	ns
1		V( = 0)(to 10)(		1.7	-	ns
9(101)	Total Gate Charge	$V_{GS} = 0 V \text{ to } 10 V$		2.1	3.0	nC
9(101)	Total Gate Charge	$V_{GS} = 0 V \text{ to } 4.5 V$ $V_{DD} = 50 V$ , $I_D = 2.2 A$		1.1	1.6	nC
90	Gate to Source Charge	I <sub>D</sub> = 2.2 A		0.5		nC
90	Gate to Drain "Miller" Charge			0.5		nC
	rce Diode Characteristics			0.0	1.0	N (
00	Source to Drain Diode Forward Voltage	$V_{GS} = 0 V, I_S = 2.2 A$ (Note 2)		0.9	1.2	V
	Reverse Recovery Time	— I <sub>F</sub> = 2.2 A, di/dt = 100 A/μs		32	51	ns
Q <sub>rr</sub>	Reverse Recovery Charge			20	32	nC
NOTES: 1. R <sub>θJA</sub> is determine the user's board	ed with the device mounted on a 1 in <sup>2</sup> pad 2 oz copper pa design.	ad on a 1.5 x 1.5 in. board of FR-4 material. $R_{\rm 0JC}$ is	sguaranteed	by design wl	hile $R_{\theta JA}$ is d	etermined b
	a. 52 °C/W when mo	unted b 1	145 °C/W whe	en mounted c	n a	
	on a 1 in <sup>2</sup> pad of 2			l of 2 oz copp		

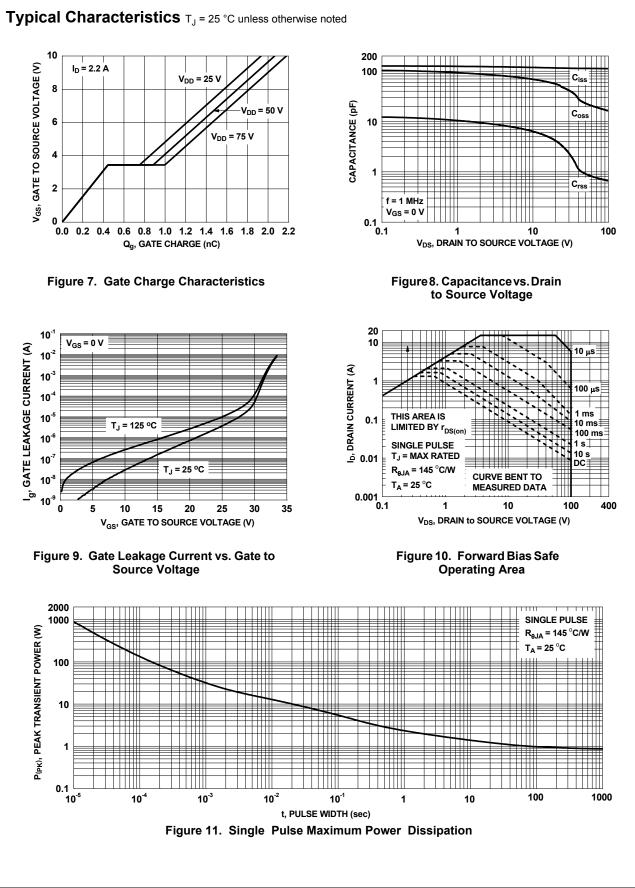
G DE SE SS

2. Pulse Test: Pulse Width < 300  $\mu s,$  Duty cycle < 2.0%. 3. Pulse Id measured at 250  $\mu s,$  refer to Fig 11 SOA graph for more details.

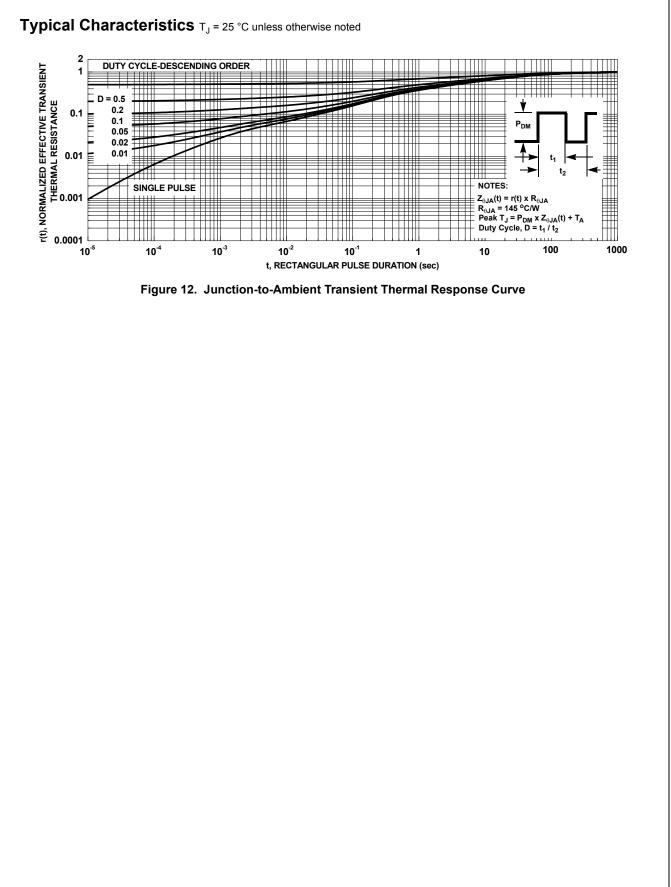
ଦ ମ୍ମ୍ର ମ୍ମ ଓ ଜ ମ୍ମ୍ର ମ୍ମ ଓ

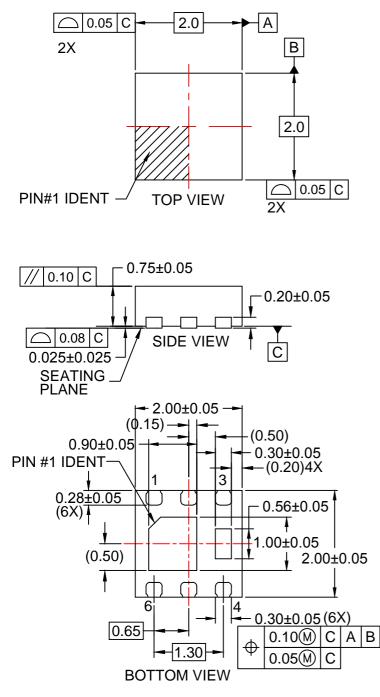


©2015 Fairchild Semiconductor Corporation FDMA86108LZ Rev.1.0



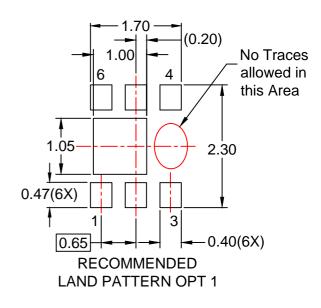
FDMA86108LZ Single N-Channel PowerTrench<sup>®</sup> MOSFET

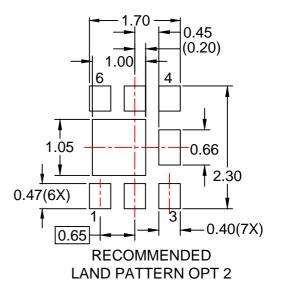




### NOTES:

- A. PACKAGE DOES NOT FULLY CONFORM TO JEDEC MO-229 REGISTRATION
- B. DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 2009.
- D. LAND PATTERN RECOMMENDATION IS EXISTING INDUSTRY LAND PATTERN.
- E. DRAWING FILENAME: MKT-MLP06Lrev4.







ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at <u>www.onsemi.com/site/pdf/Patent-Marking.pdf</u>. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor haves against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death a

#### PUBLICATION ORDERING INFORMATION

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800–282–9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Japan Customer Focus Center Phone: 81-3-5817-1050 ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

© Semiconductor Components Industries, LLC

### **Mouser Electronics**

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

ON Semiconductor: FDMA86108LZ