MOSFET – Power, N-Channel, SUPERFET III, Easy Drive

650 V, 14 A, 190 m Ω

Description

SUPERFET III MOSFET is ON Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provides superior switching performance, and withstand extreme dv/dt rate.

Consequently, SUPERFET III MOSFET Easy drive series helps manage EMI issues and allows for easier design implementation.

Features

- 700 V @ T_J = 150°C
- Typ. $R_{DS(on)} = 165 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. $Q_g = 30 \text{ nC}$)
- Low Effective Output Capacitance (Typ. C_{oss(eff.)} = 277 pF)
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

Applications

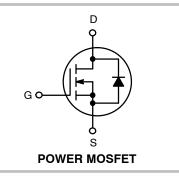
- Computing / Display Power Supplies
- Telecom / Server Power Supplies
- Industrial Power Supplies
- Lighting / Charger / Adapter



ON Semiconductor®

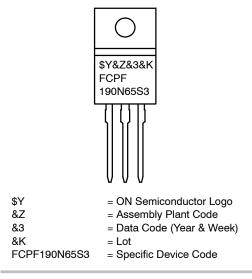
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V _{DSS}	R _{DS(ON)} MAX	I _D MAX
650 V	190 m Ω @ 10 V	14 A





MARKING DIAGRAM



ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

Symbol	Parameter	Value	Unit		
V _{DSS}	Drain to Source Voltage		650	V	
V _{GSS}	Gate to Source Voltage	– DC	±30	V	
		– AC (f > 1 Hz)	±30		
ID	Drain Current	– Continuous (T _C = 25°C)	14*	А	
		– Continuous (T _C = 100°C)	9*		
I _{DM}	Drain Current	– Pulsed (Note 1)	35*	А	
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		76	mJ	
I _{AS}	Avalanche Current (Note 2)		2.5	А	
E _{AR}	Repetitive Avalanche Energy (Note 1)		0.33	mJ	
dv/dt	MOSFET dv/dt		100	V/ns	
	Peak Diode Recovery dv/dt (Note 3)		20		
P _D	Power Dissipation	(T _C = 25°C)	33	W	
		– Derate Above 25°C	0.27	W/°C	
T _J , T _{STG}	Operating and Storage Temperature Range		–55 to +150	°C	
TL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 seconds		300	°C	

ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, Unless otherwise noted)

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality stresses exceeding those listed in the Maximum Hatings table may damage to should not be assumed, damage may occur and reliability may be affected. *Drain current limited by maximum junction temperature. 1. Repetitive rating: pulse-width limited by maximum junction temperature. 2. $I_{AS} = 2.5 \text{ A}$, $R_G = 25 \Omega$, starting $T_J = 25^{\circ}\text{C}$. 3. $I_{SD} \leq 7 \text{ A}$, di/dt $\leq 200 \text{ A}/\mu\text{s}$, $V_{DD} \leq 400 \text{ V}$, starting $T_J = 25^{\circ}\text{C}$.

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	3.76	°C/W
$R_{\theta JA}$	R _{0JA} Thermal Resistance, Junction to Ambient, Max.		

PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Packing Method	Reel Size	Tape Width	Quantity
FCPF190N65S3L1	FCPF190N65S3	TO-220F	Tube	N/A	N/A	50 Units

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
OFF CHARACT	ERISTICS	-				
BV _{DSS}	Drain to Source Breakdown Voltage	V_{GS} = 0 V, I_D = 1 mA, T_J = 25°C	650			V
		V_{GS} = 0 V, I_D = 1 mA, T_J = 150°C	700			V
$\Delta \text{BV}_{\text{DSS}}$ / $\Delta \text{T}_{\text{J}}$	Breakdown Voltage Temperature Coefficient	$I_D = 1$ mA, Referenced to $25^{\circ}C$		0.6		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 650 V, V_{GS} = 0 V			1	μΑ
		$V_{DS} = 520 \text{ V}, \text{ T}_{C} = 125^{\circ}\text{C}$		0.89		
I _{GSS}	Gate to Body Leakage Current	V_{GS} = ±30 V, V_{DS} = 0 V			±100	nA
ON CHARACTE	RISTICS					
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 0.36 \text{ mA}$	2.5		4.5	V
R _{DS(on)}	Static Drain to Source On Resistance	V_{GS} = 10 V, I _D = 7 A		165	190	mΩ
9 _{FS}	Forward Transconductance	$V_{DS} = 20 \text{ V}, \text{ I}_{D} = 7 \text{ A}$		10		S
DYNAMIC CHA	RACTERISTICS	-	-			•
C _{iss}	Input Capacitance	V_{DS} = 400 V, V_{GS} = 0 V, f = 1 MHz		1225		pF
C _{oss}	Output Capacitance			30		pF
C _{oss(eff.)}	Effective Output Capacitance	V_{DS} = 0 V to 400 V, V_{GS} = 0 V		277		pF
C _{oss(er.)}	Energy Related Output Capacitance	V_{DS} = 0 V to 400 V, V_{GS} = 0 V		43		pF
Q _{g(tot)}	Total Gate Charge at 10 V	$V_{DS} = 400 \text{ V}, \text{ I}_{D} = 7 \text{ A}, \text{ V}_{GS} = 10 \text{ V}$		30		nC
Q _{gs}	Gate to Source Gate Charge	$V_{DS} = 0 V \text{ to } 400 V, V_{GS} = 0 V$ $V_{DS} = 0 V \text{ to } 400 V, V_{GS} = 0 V$ $V_{DS} = 400 V, I_D = 7 \text{ A}, V_{GS} = 10 V$ (Note 4) $f = 1 \text{ MHz}$ $V_{DD} = 400 V, I_D = 7 \text{ A}, V_{GS} = 10 V,$		7.4		nC
Q _{gd}	Gate to Drain "Miller" Charge			13		nC
ESR	Equivalent Series Resistance	f = 1 MHz		7		Ω
WITCHING CH	IARACTERISTICS	-				
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 400 \text{ V}, \text{ I}_{D} = 7 \text{ A}, \text{ V}_{GS} = 10 \text{ V},$		19		ns
t _r	Turn-On Rise Time	- R _g = 4.7 Ω (Note 4)		23		ns
t _{d(off)}	Turn-Off Delay Time			52	1	ns
t _f	Turn-Off Fall Time	1		15	1	ns
SOURCE-DRAI	N DIODE CHARACTERISTICS	•	•	•	•	-
۱ _S	Maximum Continuous Source to Drain Diode Forward Current				14	Α
I _{SM}	Maximum Pulsed Source to Drain Diode Forward Current			1	35	А
V _{SD}	Source to Drain Diode Forward Voltage	$V_{GS} = 0 V, I_{SD} = 7 A$			1.2	V
t _{rr}	Reverse Recovery Time	$V_{DD} = 400 \text{ V}, \text{ I}_{SD} = 7 \text{ A},$		256		ns
Q _{rr}	Reverse Recovery Charge			3.5	1	μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

TYPICAL PERFORMANCE CHARACTERISTICS

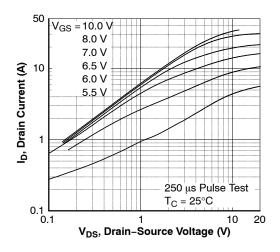
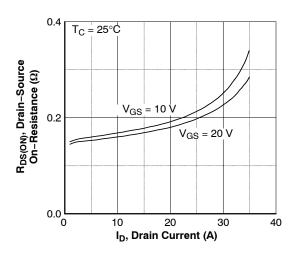
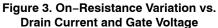


Figure 1. On-Region Characteristics





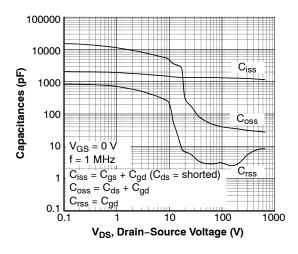


Figure 5. Capacitance Characteristics

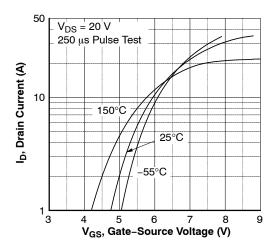
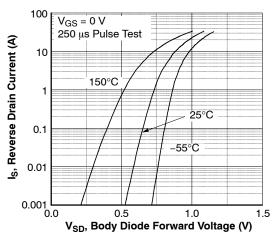


Figure 2. Transfer Characteristics





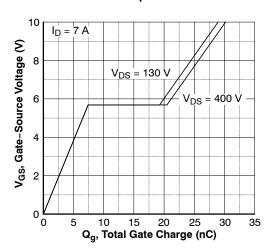
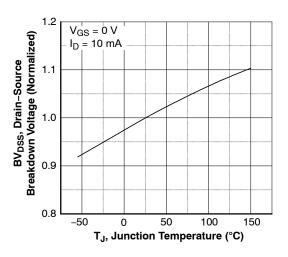
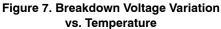


Figure 6. Gate Charge Characteristics

TYPICAL PERFORMANCE CHARACTERISTICS (continued)





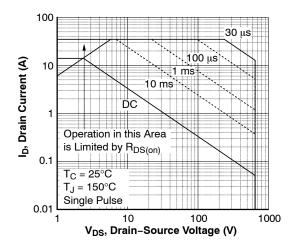


Figure 9. Maximum Safe Operating Area

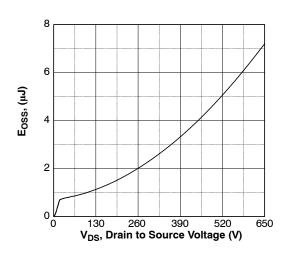


Figure 11. E_{OSS} vs. Drain to Source Voltage

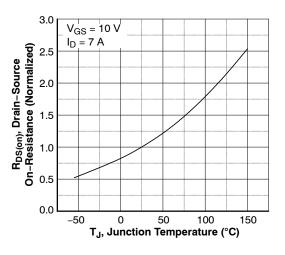


Figure 8. On–Resistance Variation vs. Temperature

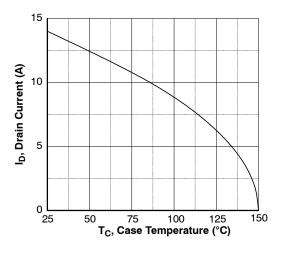


Figure 10. Maximum Drain Current vs. Case Temperature

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

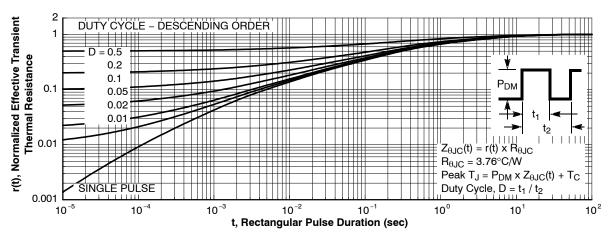


Figure 12. Transient Thermal Response Curve

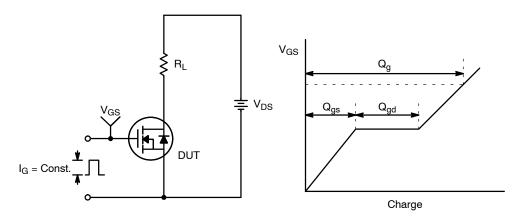


Figure 13. Gate Charge Test Circuit & Waveform

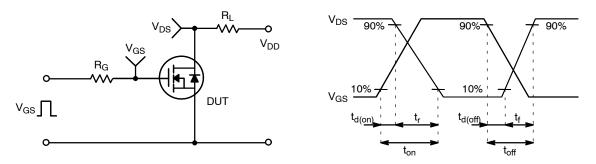


Figure 14. Resistive Switching Test Circuit & Waveforms

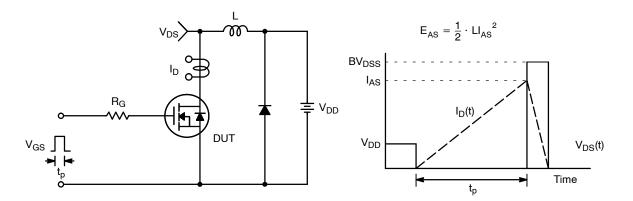


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

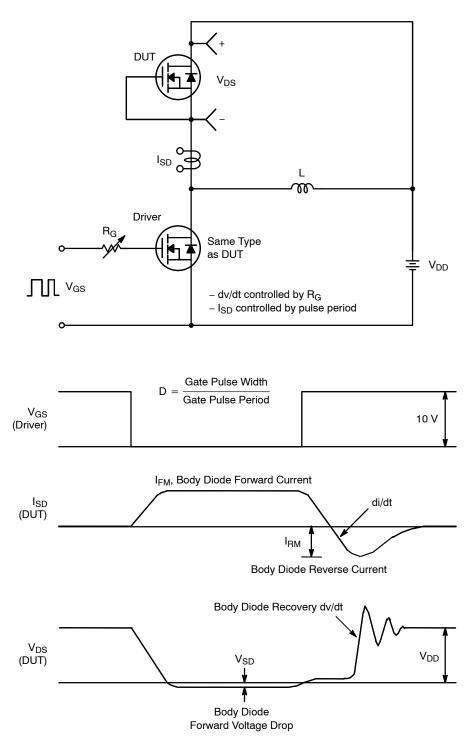


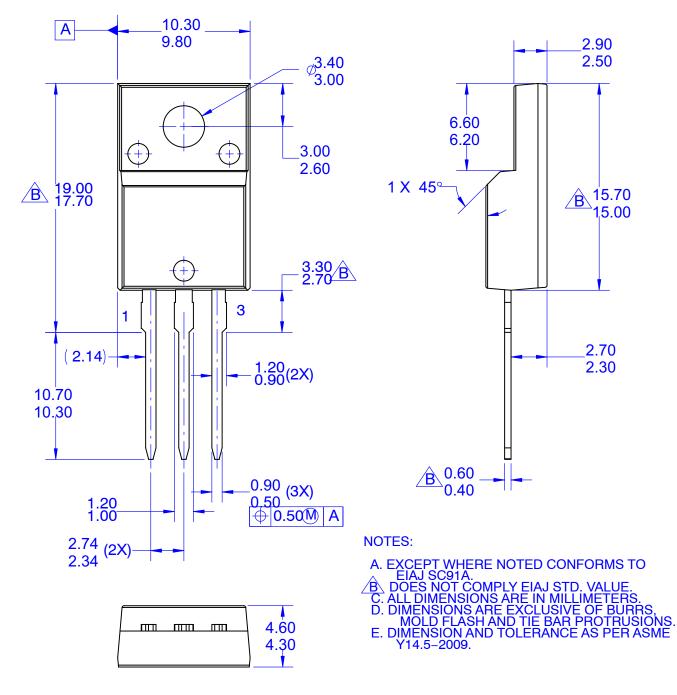
Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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