

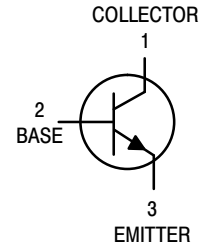
# Amplifier Transistors

## NPN Silicon

### BC546B, BC547A, B, C, BC548B, C

#### Features

- Pb-Free Packages are Available\*



#### MAXIMUM RATINGS

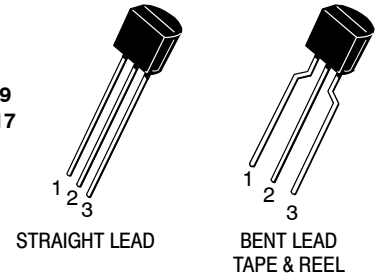
Rating	Symbol	Value	Unit
Collector - Emitter Voltage	$V_{CEO}$		Vdc
	BC546	65	
	BC547	45	
	BC548	30	
Collector - Base Voltage	$V_{CBO}$		Vdc
	BC546	80	
	BC547	50	
	BC548	30	
Emitter - Base Voltage	$V_{EBO}$	6.0	Vdc
Collector Current - Continuous	$I_C$	100	mAdc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	625	mW
		5.0	mW/ $^\circ\text{C}$
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	1.5	W
		12	mW/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	$T_J, T_{stg}$	-55 to +150	$^\circ\text{C}$

#### THERMAL CHARACTERISTICS

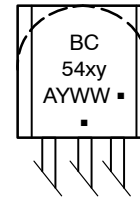
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	200	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	$^\circ\text{C}/\text{W}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

TO-92  
CASE 29  
STYLE 17



#### MARKING DIAGRAM



- x = 6, 7, or 8
- y = A, B or C
- A = Assembly Location
- Y = Year
- WW = Work Week
- = Pb-Free Package

(Note: Microdot may be in either location)

#### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

\*For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# BC546B, BC547A, B, C, BC548B, C

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit	
<b>OFF CHARACTERISTICS</b>						
Collector – Emitter Breakdown Voltage (I <sub>C</sub> = 1.0 mA, I <sub>B</sub> = 0)	BC546 BC547 BC548	V <sub>(BR)CEO</sub>	65 45 30	– – –	– – –	V
Collector – Base Breakdown Voltage (I <sub>C</sub> = 100 μA)	BC546 BC547 BC548	V <sub>(BR)CBO</sub>	80 50 30	– – –	– – –	V
Emitter – Base Breakdown Voltage (I <sub>E</sub> = 10 μA, I <sub>C</sub> = 0)	BC546 BC547 BC548	V <sub>(BR)EBO</sub>	6.0 6.0 6.0	– – –	– – –	V
Collector Cutoff Current (V <sub>CE</sub> = 70 V, V <sub>BE</sub> = 0) (V <sub>CE</sub> = 50 V, V <sub>BE</sub> = 0) (V <sub>CE</sub> = 35 V, V <sub>BE</sub> = 0) (V <sub>CE</sub> = 30 V, T <sub>A</sub> = 125°C)	BC546 BC547 BC548 BC546/547/548	I <sub>CES</sub>	– – – –	0.2 0.2 0.2 –	15 15 15 4.0	nA μA
<b>ON CHARACTERISTICS</b>						
DC Current Gain (I <sub>C</sub> = 10 μA, V <sub>CE</sub> = 5.0 V)	BC547A BC546B/547B/548B BC548C	h <sub>FE</sub>	– – –	90 150 270	– – –	–
(I <sub>C</sub> = 2.0 mA, V <sub>CE</sub> = 5.0 V)	BC546 BC547 BC548 BC547A BC546B/547B/548B BC547C/BC548C		110 110 110 110 200 420	– – – 180 290 520	450 800 800 220 450 800	
(I <sub>C</sub> = 100 mA, V <sub>CE</sub> = 5.0 V)	BC547A/548A BC546B/547B/548B BC548C		– – –	120 180 300	– – –	
Collector – Emitter Saturation Voltage (I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0.5 mA) (I <sub>C</sub> = 100 mA, I <sub>B</sub> = 5.0 mA) (I <sub>C</sub> = 10 mA, I <sub>B</sub> = See Note 1)		V <sub>CE(sat)</sub>	– – –	0.09 0.2 0.3	0.25 0.6 0.6	V
Base – Emitter Saturation Voltage (I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0.5 mA)		V <sub>BE(sat)</sub>	–	0.7	–	V
Base – Emitter On Voltage (I <sub>C</sub> = 2.0 mA, V <sub>CE</sub> = 5.0 V) (I <sub>C</sub> = 10 mA, V <sub>CE</sub> = 5.0 V)		V <sub>BE(on)</sub>	0.55 –	– –	0.7 0.77	V
<b>SMALL-SIGNAL CHARACTERISTICS</b>						
Current – Gain – Bandwidth Product (I <sub>C</sub> = 10 mA, V <sub>CE</sub> = 5.0 V, f = 100 MHz)	BC546 BC547 BC548	f <sub>T</sub>	150 150 150	300 300 300	– – –	MHz
Output Capacitance (V <sub>CB</sub> = 10 V, I <sub>C</sub> = 0, f = 1.0 MHz)		C <sub>obo</sub>	–	1.7	4.5	pF
Input Capacitance (V <sub>EB</sub> = 0.5 V, I <sub>C</sub> = 0, f = 1.0 MHz)		C <sub>ibo</sub>	–	10	–	pF
Small – Signal Current Gain (I <sub>C</sub> = 2.0 mA, V <sub>CE</sub> = 5.0 V, f = 1.0 kHz)	BC546 BC547/548 BC547A BC546B/547B/548B BC547C/548C	h <sub>fe</sub>	125 125 125 240 450	– – 220 330 600	500 900 260 500 900	–
Noise Figure (I <sub>C</sub> = 0.2 mA, V <sub>CE</sub> = 5.0 V, R <sub>S</sub> = 2 kΩ, f = 1.0 kHz, Δf = 200 Hz)	BC546 BC547 BC548	NF	– – –	2.0 2.0 2.0	10 10 10	dB

1. I<sub>B</sub> is value for which I<sub>C</sub> = 11 mA at V<sub>CE</sub> = 1.0 V.

# BC546B, BC547A, B, C, BC548B, C

## BC547/BC548

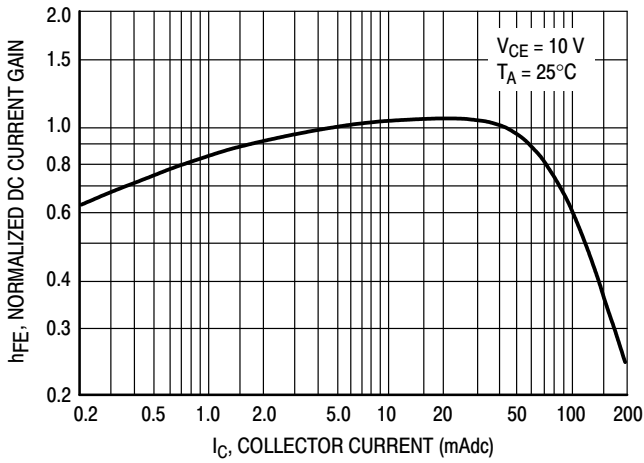


Figure 1. Normalized DC Current Gain

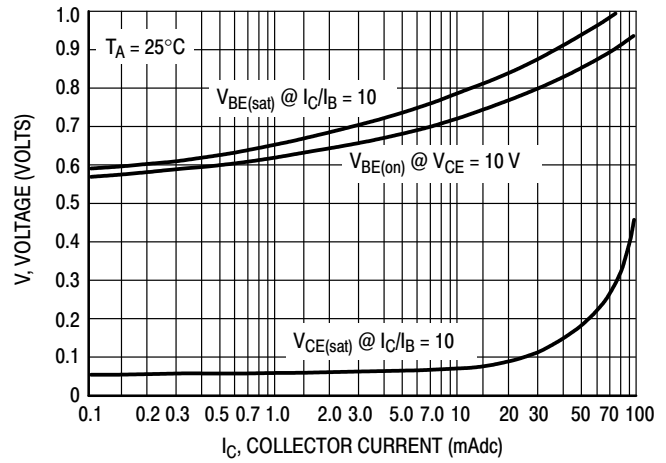


Figure 2. "Saturation" and "On" Voltages

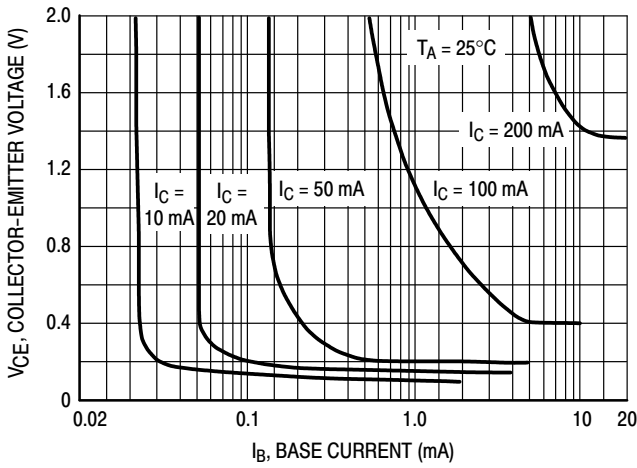


Figure 3. Collector Saturation Region

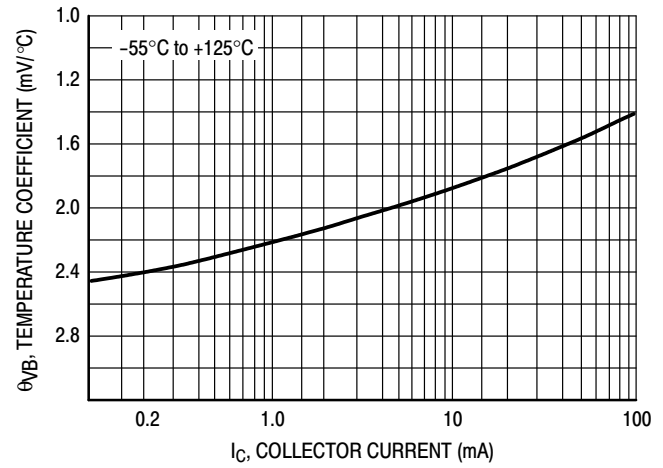


Figure 4. Base-Emitter Temperature Coefficient

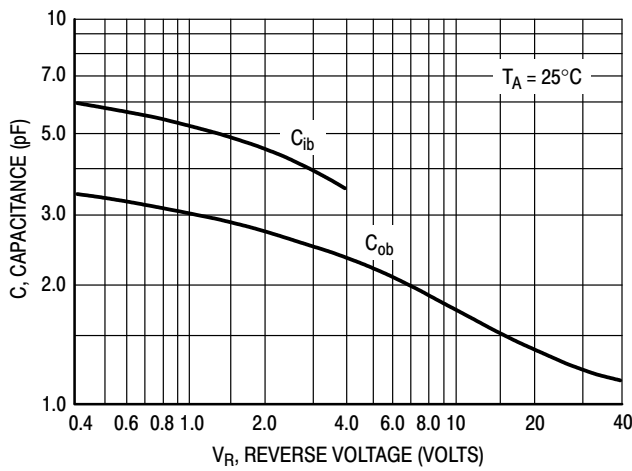


Figure 5. Capacitances

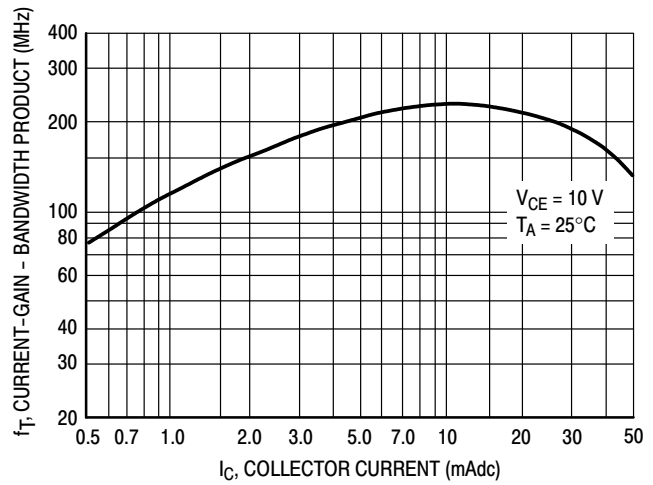


Figure 6. Current-Gain - Bandwidth Product

BC546

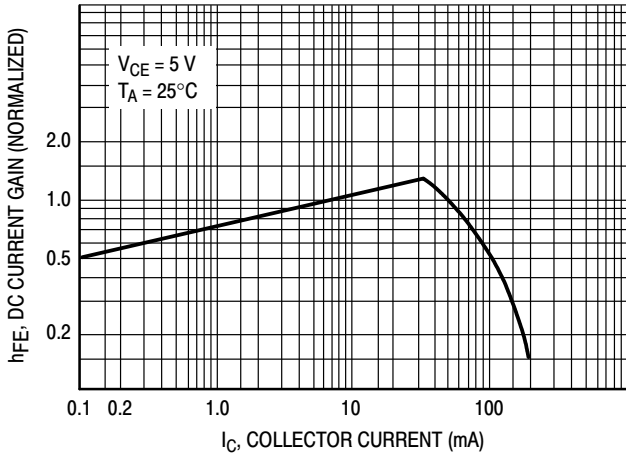


Figure 7. DC Current Gain

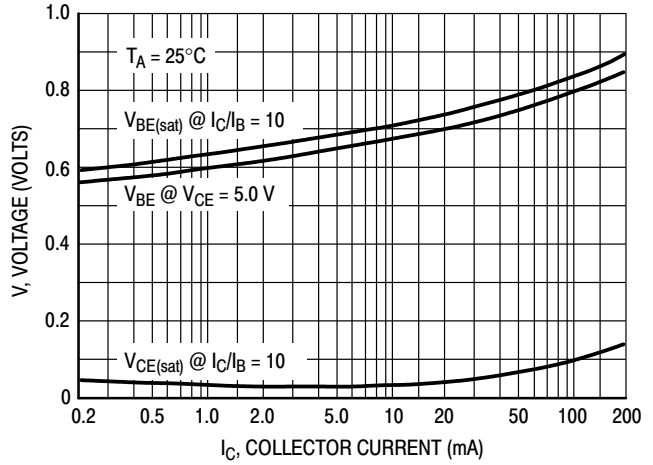


Figure 8. "On" Voltage

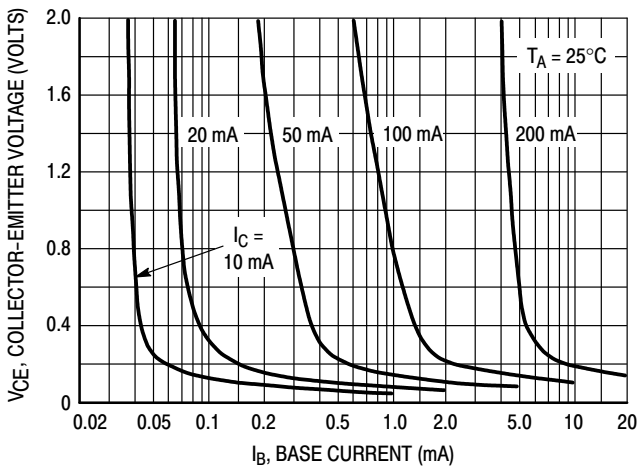


Figure 9. Collector Saturation Region

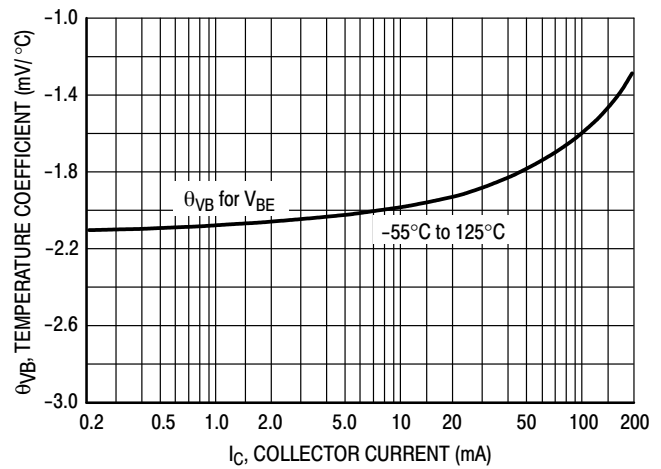


Figure 10. Base-Emitter Temperature Coefficient

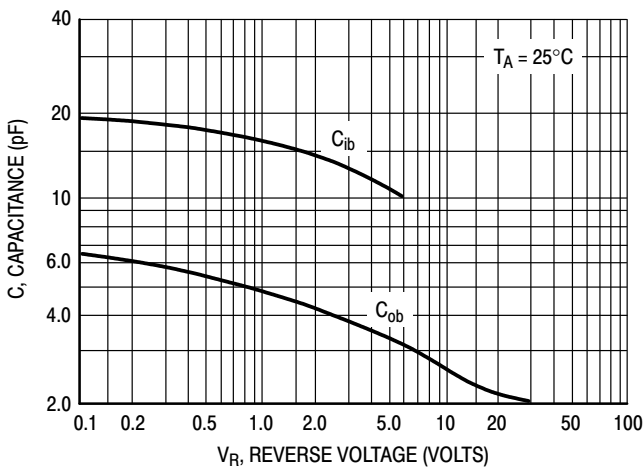


Figure 11. Capacitance

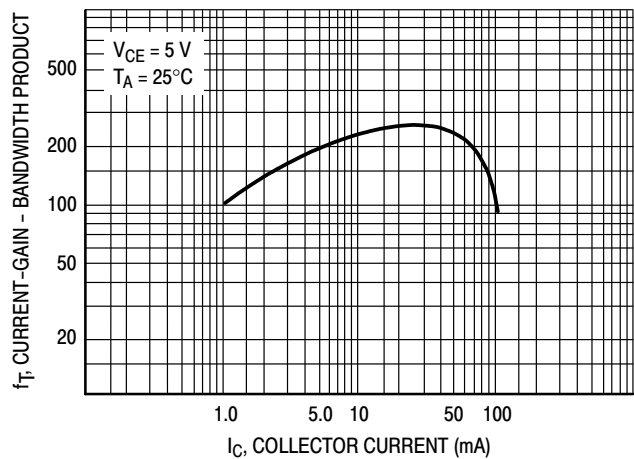


Figure 12. Current-Gain - Bandwidth Product

## BC546B, BC547A, B, C, BC548B, C

### ORDERING INFORMATION

Device	Package	Shipping†
BC546B	TO-92	5000 Units / Bulk
BC546BG	TO-92 (Pb-Free)	5000 Units / Bulk
BC546BRL1	TO-92	2000 / Tape & Reel
BC546BRL1G	TO-92 (Pb-Free)	2000 / Tape & Reel
BC546BZL1G	TO-92 (Pb-Free)	2000 / Ammo Box
BC547ARL	TO-92	2000 / Tape & Reel
BC547ARLG	TO-92 (Pb-Free)	2000 / Tape & Reel
BC547AZL1G	TO-92 (Pb-Free)	2000 / Ammo Box
BC547BG	TO-92 (Pb-Free)	5000 Units / Bulk
BC547BRL1G	TO-92 (Pb-Free)	2000 / Tape & Reel
BC547BZL1G	TO-92 (Pb-Free)	2000 / Ammo Box
BC547CG	TO-92 (Pb-Free)	5000 Units / Bulk
BC547CZL1G	TO-92 (Pb-Free)	2000 / Ammo Box
BC548BG	TO-92 (Pb-Free)	5000 Units / Bulk
BC548BRL1G	TO-92 (Pb-Free)	2000 / Tape & Reel
BC548BZL1G	TO-92 (Pb-Free)	2000 / Ammo Box
BC548CG	TO-92 (Pb-Free)	5000 Units / Bulk
BC548CZL1G	TO-92 (Pb-Free)	2000 / Ammo Box

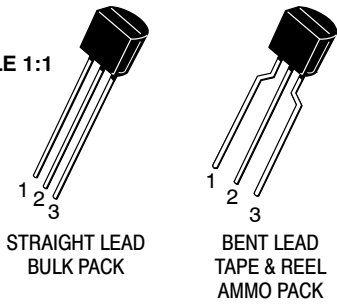
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

ON Semiconductor®



SCALE 1:1



TO-92 (TO-226)  
CASE 29-11  
ISSUE AM

DATE 09 MAR 2007



STRAIGHT LEAD  
BULK PACK



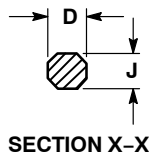
NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.175	0.205	4.45	5.20
B	0.170	0.210	4.32	5.33
C	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
H	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500	---	12.70	---
L	0.250	---	6.35	---
N	0.080	0.105	2.04	2.66
P	---	0.100	---	2.54
R	0.115	---	2.93	---
V	0.135	---	3.43	---



BENT LEAD  
TAPE & REEL  
AMMO PACK



NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

DIM	MILLIMETERS	
	MIN	MAX
A	4.45	5.20
B	4.32	5.33
C	3.18	4.19
D	0.40	0.54
G	2.40	2.80
J	0.39	0.50
K	12.70	---
N	2.04	2.66
P	1.50	4.00
R	2.93	---
V	3.43	---

STYLES ON PAGE 2

DOCUMENT NUMBER:	98ASB42022B	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
STATUS:	ON SEMICONDUCTOR STANDARD	
NEW STANDARD:		
DESCRIPTION:	TO-92 (TO-226)	PAGE 1 OF 3

**TO-92 (TO-226)**  
**CASE 29-11**  
**ISSUE AM**

DATE 09 MAR 2007

STYLE 1:  
 PIN 1. EMITTER  
 2. BASE  
 3. COLLECTOR

STYLE 2:  
 PIN 1. BASE  
 2. EMITTER  
 3. COLLECTOR

STYLE 3:  
 PIN 1. ANODE  
 2. ANODE  
 3. CATHODE

STYLE 4:  
 PIN 1. CATHODE  
 2. CATHODE  
 3. ANODE

STYLE 5:  
 PIN 1. DRAIN  
 2. SOURCE  
 3. GATE

STYLE 6:  
 PIN 1. GATE  
 2. SOURCE & SUBSTRATE  
 3. DRAIN

STYLE 7:  
 PIN 1. SOURCE  
 2. DRAIN  
 3. GATE

STYLE 8:  
 PIN 1. DRAIN  
 2. GATE  
 3. SOURCE & SUBSTRATE

STYLE 9:  
 PIN 1. BASE 1  
 2. EMITTER  
 3. BASE 2

STYLE 10:  
 PIN 1. CATHODE  
 2. GATE  
 3. ANODE

STYLE 11:  
 PIN 1. ANODE  
 2. CATHODE & ANODE  
 3. CATHODE

STYLE 12:  
 PIN 1. MAIN TERMINAL 1  
 2. GATE  
 3. MAIN TERMINAL 2

STYLE 13:  
 PIN 1. ANODE 1  
 2. GATE  
 3. CATHODE 2

STYLE 14:  
 PIN 1. EMITTER  
 2. COLLECTOR  
 3. BASE

STYLE 15:  
 PIN 1. ANODE 1  
 2. CATHODE  
 3. ANODE 2

STYLE 16:  
 PIN 1. ANODE  
 2. GATE  
 3. CATHODE

STYLE 17:  
 PIN 1. COLLECTOR  
 2. BASE  
 3. EMITTER

STYLE 18:  
 PIN 1. ANODE  
 2. CATHODE  
 3. NOT CONNECTED

STYLE 19:  
 PIN 1. GATE  
 2. ANODE  
 3. CATHODE

STYLE 20:  
 PIN 1. NOT CONNECTED  
 2. CATHODE  
 3. ANODE

STYLE 21:  
 PIN 1. COLLECTOR  
 2. EMITTER  
 3. BASE

STYLE 22:  
 PIN 1. SOURCE  
 2. GATE  
 3. DRAIN

STYLE 23:  
 PIN 1. GATE  
 2. SOURCE  
 3. DRAIN

STYLE 24:  
 PIN 1. EMITTER  
 2. COLLECTOR/ANODE  
 3. CATHODE

STYLE 25:  
 PIN 1. MT 1  
 2. GATE  
 3. MT 2

STYLE 26:  
 PIN 1. V<sub>CC</sub>  
 2. GROUND 2  
 3. OUTPUT

STYLE 27:  
 PIN 1. MT  
 2. SUBSTRATE  
 3. MT

STYLE 28:  
 PIN 1. CATHODE  
 2. ANODE  
 3. GATE

STYLE 29:  
 PIN 1. NOT CONNECTED  
 2. ANODE  
 3. CATHODE

STYLE 30:  
 PIN 1. DRAIN  
 2. GATE  
 3. SOURCE

STYLE 31:  
 PIN 1. GATE  
 2. DRAIN  
 3. SOURCE

STYLE 32:  
 PIN 1. BASE  
 2. COLLECTOR  
 3. EMITTER

STYLE 33:  
 PIN 1. RETURN  
 2. INPUT  
 3. OUTPUT

STYLE 34:  
 PIN 1. INPUT  
 2. GROUND  
 3. LOGIC

STYLE 35:  
 PIN 1. GATE  
 2. COLLECTOR  
 3. EMITTER

<b>DOCUMENT NUMBER:</b>	<b>98ASB42022B</b>	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
<b>STATUS:</b>	<b>ON SEMICONDUCTOR STANDARD</b>	
<b>NEW STANDARD:</b>		
<b>DESCRIPTION:</b>	<b>TO-92 (TO-226)</b>	<b>PAGE 2 OF 3</b>





**onsemi**, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## PUBLICATION ORDERING INFORMATION

### LITERATURE FULFILLMENT:

Email Requests to: [orderlit@onsemi.com](mailto:orderlit@onsemi.com)

**onsemi Website:** [www.onsemi.com](http://www.onsemi.com)

### TECHNICAL SUPPORT

**North American Technical Support:**  
Voice Mail: 1 800-282-9855 Toll Free USA/Canada  
Phone: 011 421 33 790 2910

**Europe, Middle East and Africa Technical Support:**

Phone: 00421 33 790 2910

For additional information, please contact your local Sales Representative

# Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

[onsemi:](#)

[BC546B](#) [BC546BG](#) [BC546BRL1](#) [BC546BRL1G](#) [BC546BZL1](#) [BC546BZL1G](#) [BC547ARL](#) [BC547ARL1](#)  
[BC547ARL1G](#) [BC547ARLG](#) [BC547AZL1](#) [BC547AZL1G](#) [BC547BG](#) [BC547BRL1](#) [BC547BRL1G](#) [BC547BZL1](#)  
[BC547BZL1G](#) [BC547C](#) [BC547CG](#) [BC547CZL1](#) [BC547CZL1G](#) [BC548B](#) [BC548BG](#) [BC548BRL1](#) [BC548BRL1G](#)  
[BC548BZL1](#) [BC548BZL1G](#) [BC548C](#) [BC548CG](#) [BC548CZL1](#) [BC548CZL1G](#) [BC547A](#)