

Schottky Barrier Diode

30 V, 2 A, Low IR, Single PCP

SB20-03P

Features

- Low Forward Voltage ($V_F max = 0.55 V$)
- Fast Reverse Recovery Time (t_{rr} max = 20 ns)
- Low Switching Noise
- Low Leakage Current and High Reliability due to Highly Reliable Planar Structure
- These Devices are Pb-Free and are RoHS Compliant

Applications

• High Frequency Rectification (Switching, Regulators, Converters, Choppers)

SPECIFICATIONS ABSOLUTE MAXIMUM RATINGS at $T_A = 25^{\circ}C$

Parameter	Symbol	Value	Unit
Repetitive Peak Reverse Voltage	V_{RRM}	30	V
Nonrepetitive Peak Reverse Voltage	V_{RSM}	35	V
Average Output Current	Io	2	Α
Surge Forward Current (Note 1)	I _{FSM}	20	Α
Junction Temperature	TJ	-55 to +125	۰C
Storage Temperature	T _{STG}	-55 to +125	°C

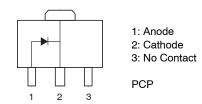
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Conditions: 50 Hz sine wave, 1 cycle

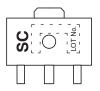


SOT-89 / PCP-1 CASE 419AU

ELECTRICAL CONNECTION



MARKING DIAGRAM



ORDERING INFORMATION

Device	Package	Shipping [†]
SB20-03P-TD-E	PCP (Pb-Free)	1000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

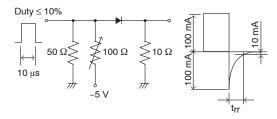
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ELECTRICAL CHARACTERISTICS at $T_A = 25$ °C

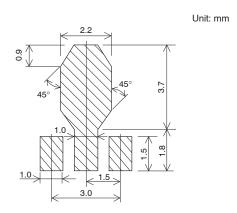
				Ratings		
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Reverse Voltage	V _R	I _R = 500 μA	30			V
Forward Voltage	V _F	I _F = 2 A			0.55	V
Reverse Current	I _R	V _R = 15 V			100	μΑ
Interterminal Capacitance	С	V _R = 10 V, f = 1 MHz		70		pF
Reverse Recovery Time	t _{rr}	I _F = I _R = 100 mA, See specified Test Circuit			20	ns
Thermal Resistance	Rth(j-a)1			300		°C/W
	Rth(j-a)2	When mounted on ceramic substrate (250 mm² x 0.8 mm)		110		°C/W

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

t_{rr} Test Circuit



Land Pattern Example



TYPICAL CHARACTERISTICS

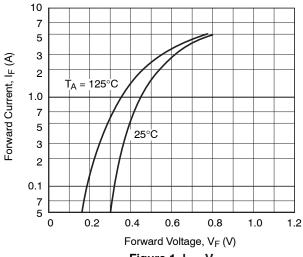


Figure 1. $I_F - V_F$

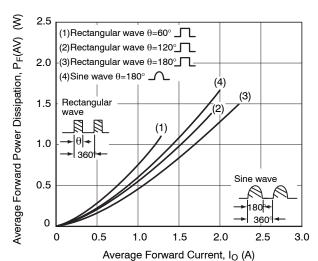


Figure 3. $P_F(AV) - I_O$

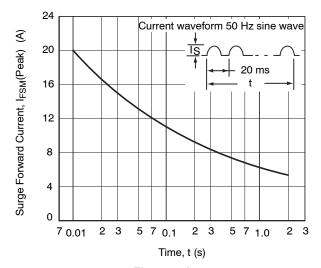


Figure 5. I_{FSM} – t

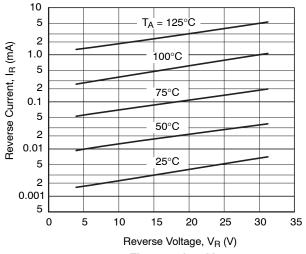


Figure 2. $I_R - V_R$

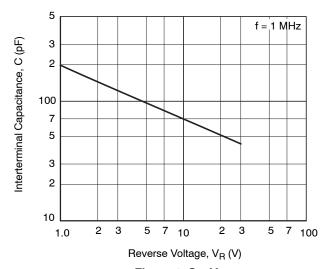
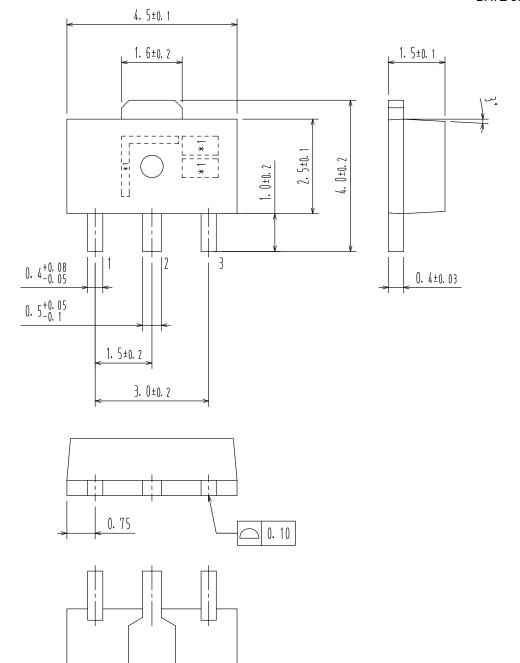


Figure 4. C - V_R



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