

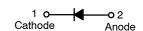
# **Rectifiers, Surface Mount**

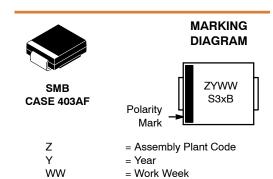
# 3 A, 50 V-1000 V

# S3AB-S3MB

### **Features**

- Glass Passivated Chip Junction
- High Surge Current Capacity
- Low Forward Voltage: 1.15 V Maximum
- UL Flammability 94V-0 Classification
- MSL 1 per J-STD-020
- RoHS Compliant / Green Molding Compound
- NRV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These are Pb-Free and Halide Free Devices





### **ORDERING INFORMATION**

= Specific Device Code

x = A, B, D, G, J, K, M

See detailed ordering and shipping information on page 3 of this data sheet.

S3xB

NOTE: Some of the devices on this data sheet have been **DISCONTINUED**. Please refer to the table on page 3.

# **ABSOLUTE MAXIMUM RATINGS** Values are at $T_A = 25$ °C unless otherwise noted.

|                    |  | Value      |      |      |      |      |      |      |      |
|--------------------|--|------------|------|------|------|------|------|------|------|
| Symbol             | Parameter  | S3AB       | S3BB | S3DB | S3GB | S3JB | S3KB | S3MB | Unit |
| $V_{RRM}$          | Repetitive Peak Reverse Voltage  | 50         | 100  | 200  | 400  | 600  | 800  | 1000 | V    |
| $V_{RMS}$          | RMS Reverse Voltage  | 35         | 70   | 140  | 280  | 420  | 560  | 700  | V    |
| $V_{R}$            | DC Blocking Voltage  | 50         | 100  | 200  | 400  | 600  | 800  | 1000 | Α    |
| I <sub>F(AV)</sub> | Average Forward Rectified Current  | 3          |      |      | Α    |      |      |      |      |
| I <sub>FSM</sub>   | Peak Forward Surge Current: 8.3 ms Single<br>Half Sine-Wave Superimposed on Rated Load | 80         |      |      | Α    |      |      |      |      |
| TJ                 | Operating Junction Temperature Range   | –55 to 150 |      |      | °C   |      |      |      |      |
| T <sub>STG</sub>   | Storage Temperature Range  | -55 to 150 |      |      | °C   |      |      |      |      |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

# **THERMAL CHARACTERISTICS** Values are at $T_A = 25^{\circ}$ C unless otherwise noted. (Note 1)

| Symbol          | Parameter   | Value | Unit |
|-----------------|---|-------|------|
| $R_{	heta JA}$  | Typical Thermal Resistance, Junction-to-Ambient   | 148   | °C/W |
| $\Psi_{\sf JL}$ | Typical Thermal Characteristics, Junction-to-Lead | 14    | °C/W |

<sup>1.</sup> Device mounted on FR-4 PCB, board size = 76.2 mm x 114.3 mm per JESD51-3.

#### **ELECTRICAL CHARACTERISTICS** Values are at T<sub>A</sub> = 25°C unless otherwise noted.

| Symbol          | Parameter                               | Test Conditions  | Min | Тур | Max  | Unit |
|-----------------|---|--|-----|-----|------|------|
| V <sub>F</sub>  | Instantaneous Forward Voltage (Note 2)  | I <sub>F</sub> = 3 A   | -   | _   | 1.15 | V    |
| I <sub>R</sub>  | Reverse Current at Rated V <sub>R</sub> | T <sub>J</sub> = 25°C  | _   | _   | 10   | μΑ   |
|                 |   | T <sub>J</sub> = 125°C   | -   | _   | 250  |      |
| t <sub>rr</sub> | Reverse Recovery Time                   | I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1 A, I <sub>rr</sub> = 0.25 A | _   | 1.5 | _    | μs   |
| CJ              | Junction Capacitance                    | V <sub>R</sub> = 4 V, f = 1 MHz  | _   | 40  | _    | pF   |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

2. Pulse test with PW = 300 μs, 1% duty cycle.

# TYPICAL PERFORMANCE CHARACTERISTICS

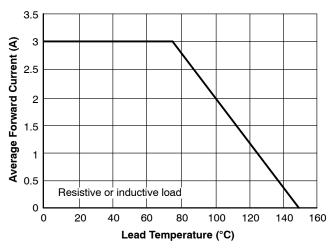


Figure 1. Forward Current Derating Curve

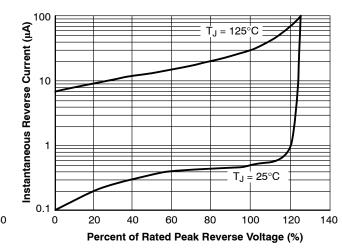


Figure 2. Typical Reverse Characteristics

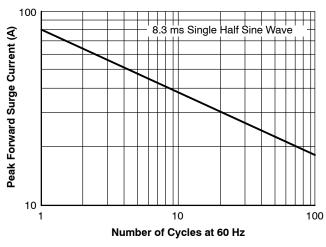


Figure 3. Maximum Non-Repetitive Forward Surge Current

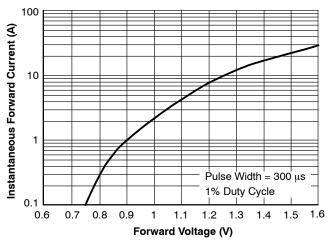


Figure 4. Typical Forward Characteristics

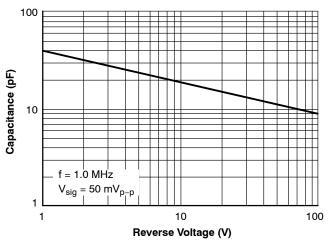


Figure 5. Typical Junction Capacitance

# S3AB-S3MB

# **TEST CIRCUIT DIAGRAM**

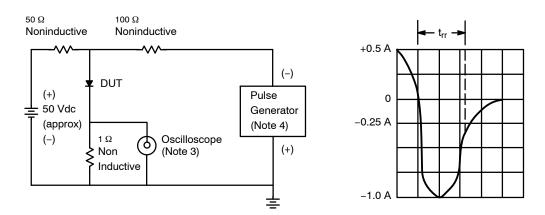


Figure 6. Reverse Recovery Time Characteristic and Test Circuit Diagram

#### NOTES:

- 3. Rise Time = 7 ns max. Input Impedance = 1 M $\Omega$ , 22 pF
- 4. Rise Time = 10 ns max. Source Impedance = 50  $\Omega$ , 22 pF

#### **ORDERING INFORMATION**

| Part Number    | Device Code Marking | Package                | Shipping <sup>†</sup> |  |  |
|----------------|---------------------|------------------------|-----------------------|--|--|
| NRVS3BB*       | S3BB                | SMB                    | 3000 / Tape & Reel    |  |  |
| S3DB           | S3DB                | (Pb-Free, Halide-Free) |                       |  |  |
| S3GB, NRVS3GB* | S3GB                | ]                      |                       |  |  |
| S3JB, NRVS3JB* | S3JB                |                        |                       |  |  |
| S3KB           | S3KB                | 1                      |                       |  |  |
| S3MB, NRVS3MB* | S3MB                | 1                      |                       |  |  |

### **DISCONTINUED** (Note 5)

| S3AB, NRVS3AB* | S3AB | SMB<br>(Pb-Free, Halide-Free) | 3000 / Tape & Reel |
|----------------|------|-------------------------------|--------------------|
| S3BB           | S3BB | (PD-Flee, Hallue-Flee)        |                    |
| NRVS3DB*       | S3DB |                               |                    |
| NRVS3KB*       | S3KB |                               |                    |

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, <a href="https://example.com/BRD8011/D">BRD8011/D</a>.

<sup>\*</sup>NRV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable

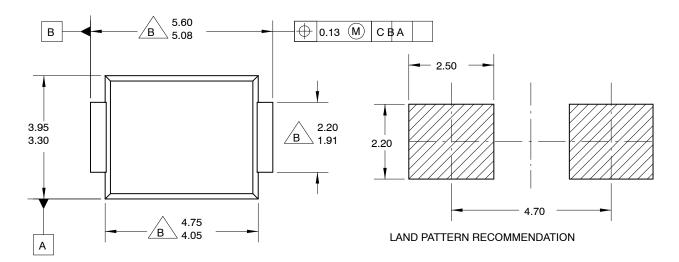
<sup>5.</sup> **DISCONTINUED:** These devices are not recommended for new design. Please contact your **onsemi** representative for information. The most current information on these devices may be available on <a href="https://www.onsemi.com">www.onsemi.com</a>.

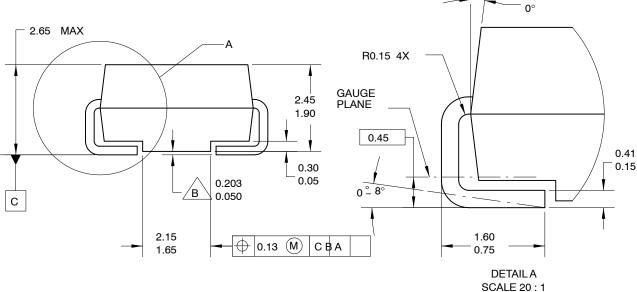
8°



SMB CASE 403AF ISSUE O

**DATE 31 AUG 2016** 





## NOTES:

- A. EXCEPT WHERE NOTED CONFORMS TO
- JEDEC DO214 VARIATION AA.
- $/\mathsf{B}\setminus\mathsf{DOES}$  NOT COMPLY JEDEC STD. VALUE.
- C. ALL DIMENSIONS ARE IN MILLIMETERS.
- D. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.
- E. DIMENSION AND TOLERANCE AS PER ASME
- Y14.5-1994.
- F. LAND PATTERN STD. DIOM5336X240M.

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| DESCRIPTION:     | SMB         |   | PAGE 1 OF 1 |  |  |  |

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