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Silicon Carbide (SiC) Module – EliteSiC Power Module for Traction Inverter, Single-Side Direct Cooling, 1.7 mohm, 900 V, 6-Pack

NVXR17S90M2SPC

Product Description

The NVXR17S90M2SPC is part of the EliteSiC power module for traction inverter, a revolutionary high mobility compound semiconductor product family that offers increased performance, better efficiency, and higher power density in similar and highly compatible packaging solutions.

The module integrates 900V SiC MOSFET in a 6-pack configuration. For assembly ease and reliability, a new generation of press-fit pins are integrated into the power module for signal terminals. In addition, it also integrates an optimized pin-fin heatsink in the baseplate.

To enhance reliability and thermal performance, sintering technology is applied for die attach. The module is designed to meet AQG324 automotive standard.

Features

- Direct Cooling w/ integrated Pin-fin Heatsink
- Silicon Nitride Isolator
- T_{vi.Max} = 175°C for Continuous Operation
- Automotive Grade SiC MOSFET Chip Technologies
- Sintered Die Technology for High Reliability Performance
- Easy to Integrate 6-pack Topology
- Automotive Module AQG324 Compliant
- These Devices are Pb-Free and are RoHS Compliant

Typical Applications

• Hybrid and Electric Vehicle Traction Inverter

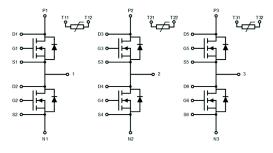


SSDC39 154.40x92.0 (SPC) CASE 183AL

MARKING DIAGRAM

XXXXX = Specific Device Code AT = Assembly & Test Site Code YYWW= Year and Work Week Code

PIN DESCRIPTION



ORDERING INFORMATION

Device	Package	Shipping
NVXR17S90M2SPC	SSDC39 (Pb-Free)	4 Units / Tray

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Pin Description

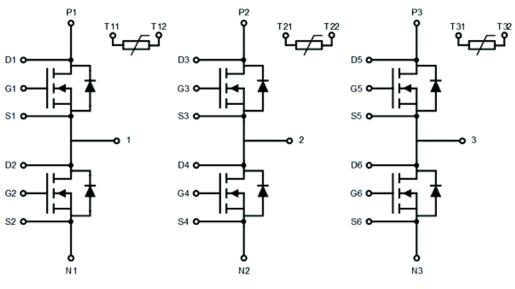


Figure 1. Pin Description

PIN FUNCTION DESCRIPTION

Pin #	Pin Function Description
P1, P2, P3	Positive Power Terminals
N1, N2, N3	Negative Power Terminals
1	Phase 1 Output
2	Phase 2 Output
3	Phase 3 Output
G1 – G6	SiC MOSFET Gate
S1 – S6	SiC MOSFET Source / Gate Return
D1 – D6	SiC MOSFET Drain Sense
T11, T12	Phase 1 Temperature Sensor Output
T21, T22	Phase 2 Temperature Sensor Output
T31, T32	Phase 3 Temperature Sensor Output

Materials

DBC Substrate: Si_3N_4 isolated substrate, basic isolation Terminals: Copper + Tin electro-plating Signal Leads: Copper + Tin plating Pin-fin Base plate: Copper + Ni plating

Flammability Information

The module frame meets UL94V-0 flammability rating

MODULE CHARACTERISTICS (T_{vj} = 25°C, Unless Otherwise Specified)

Symbol	Parameter	Rating	Unit
T _{vj}	Operating Junction Temperature	-40 to 175	°C
T _{STG}	Storage Temperature Range	-40 to 125	°C
V _{ISO}	Isolation Voltage (AC, 50 Hz, 5 s)	4200	V
Ls _{DS}	Stray Inductance	8.0	nH
R _{DD'+SS'}	Module Lead Resistance, Terminals-Chip	0.6	mΩ
G	Module Weight	700	g
CTI	Comparative Tracking Index	>200	-
d _{creep}	Creepage: Terminal to Heatsink Terminal to Terminal	9.0 9.0	mm
d _{clear}	Clearance: Terminal to Heatsink Terminal to Terminal	4.5 4.5	mm

Symbol	Parameters	Conditions	Min	Тур	Max	Unit
Δp	Pressure Drop in Cooling Circuit	10 L/min, 65°C, 50/50 EGW	-	95	-	mbar
P (Note 1)	Maximum Pressure in Cooling Loop (relative)	TBaseplate < 40°C TBaseplate > 40°C	-	-	2.5 2.0	bar

1. EPDM rubber 50 durometer 'O' ring used.

ABSOLUTE MAXIMUM RATINGS (T_{vj} = 25°C, Unless Otherwise Specified)

Symbol	Parameter	Rating	Unit
V _{DS}	Drain-to-Source Voltage	900	V
V _{GS}	Gate-to-Source Voltage	+22/-8	V
I _{DS}	Continuous DC Drain Current, V _{GS} = 18 V, T _{vj} = 175°C, T _F = 65°C @ 10LPM, R _{thj_F.max} (Note 2)	620	A
I _{DS.pulsed}	Pulsed Drain Current @ V_{GS} = 18 V, limited by $T_{vj.Max}$	1240	А
I _{SD.BD}	Continuous DC Body Diode Current, $V_{GS} = -5 \text{ V}$, $T_{vj} = 175^{\circ}\text{C}$, $T_F = 65^{\circ}\text{C}$ @ 10LPM, using Ref. Heatsink (Note 2)	264	A
I _{SD.pulsed}	Pulsed Body Diode Current, $V_{GS} = -5 V$, limited by $T_{vj.Max}$	1240	А
P _{tot}	Total Power Dissipation $T_{vj,Max}$ = 175°C, T_F = 65°C, Ref. Heatsink (typ)	1000	W

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 2. Verified by characterization/design, not by test.

Symbol	Parameters Drain – Source On Resistance	Conditions		Min	Тур.	Max	Unit
R _{DS(ON)}		V _{GS} = 18 V, I _D = 620 A	T _{vj} = 25°C T _{vj} = 175°C	-	1.70 2.30	2.10 -	mΩ
I _{DSS}	Zero Gate Voltage Drain Cur- rent	V_{GS} = 0 V, V_{DS} = 900 V	T _{vj} = 25°C T _{vj} = 175°C	_	_ 2	800 -	μA mA
I _{GSS}	Gate – Source Leakage Current	V _{GS} = 18 V, V _{DS} = 0 V		-	-	8	μA
V _{GS(TH)}	Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 200 \text{ mA}$		1.8	2.7	4.3	V
9 _{fs}	Forward Transconductance	V_{DS} = 20 V, I_{D} = 620 A			440		S
Q_{G}	Total Gate Charge	V_{GS} = –5/18 V, V_{DS} = 400 V, I_{D} :	= 620 A	-	2.4	-	μC
R _{g.int}	Internal gate resistance			-	0.7	-	Ω
C _{iss}	Input Capacitance	V _{DS} = 400V, V _{GS} = 0V, f = 100 kHz		-	45	_	nF
Coss	Output Capacitance			-	3.3	-	nF
C _{rss}	Reverse Transfer Capacitance		_	-	0.3	-	nF
T _{d.on}	Turn on delay, inductive load	$I_D = 620 \text{ A}, V_{DS} = 400 \text{ V},$ $V_{GS} = +18 / -5 \text{ V},$	T _{vj} = 25°C T _{vj} = 175°C	-	122 100	-	ns
T _r	Rise time, inductive load	R _{g.on} = 3.9 Ω	T _{vj} = 25°C T _{vj} = 175°C	-	110 90	-	ns
T _{d.off}	Turn off delay, inductive load	$I_D = 620 \text{ A}, V_{DS} = 400 \text{ V}, V_{GS} = +18 / -5 \text{ V},$	T _{vj} = 25°C T _{vj} = 175°C	-	327 388	-	ns
Т _f	Fall time, inductive load	$R_{g.off} = 1.8 \Omega$	T _{vj} = 25°C T _{vj} = 175°C	-	61 65	-	ns
E _{on}	Turn On Switching Loss (including diode reverse recov- ery loss)	$\begin{split} I_D &= 620 \text{ A}, \text{ V}_{DS} = 400 \text{ V}, \\ \text{V}_{GS} &= +18 \text{ / } -5 \text{ V}, \\ \text{L}_S &= 18 \text{ nH}, \text{ R}_{g.on} = 3.9 \Omega \end{split}$	$\label{eq:transform} \begin{array}{l} di/dt = 5.3 \text{ A/ns}, \\ T_{vj} = 25^\circ\text{C} \\ di/dt = 4.7 \text{ A/ns}, \\ T_{vj} = 175^\circ\text{C} \end{array}$	-	17 15	-	mJ
E _{off}	Turn Off Switching Loss	$\begin{split} I_D &= 620 \text{ A}, V_{DS} = 400 \text{ V}, \\ V_{GS} &= +18 \text{ / } -5 \text{ V}, \\ L_S &= 18 \text{ nH}, R_{g.off} = 1.8 \ \Omega \end{split}$	$\label{eq:transform} \begin{array}{l} dv/dt = 6.0 \text{ V/ns} \\ T_{vj} = 25^\circ\text{C} \\ dv/dt = 6.0 \text{ V/ns} \\ T_{vj} = 175^\circ\text{C} \end{array}$	-	18 19	-	mJ
E _{SC}	Short Circuit Energy withstand	V_{GS} = 18 V, V_{DD} = 400 V	T _{vj} = 25 °C T _{vj} = 175 °C	-	12 10	-	J

MOSFET CHARACTERISTICS (T_{vi} = 25°C, Unless Otherwise Specified)

BODY DIODE CHARACTERISTICS (T_{vj} = 25°C, Unless Otherwise Specified)

	Parameters	Condition	IS	Min	Тур	Max	Unit
V _{SD}	Diode Forward Voltage	V_{GS} = -5 V, I _{SD} = 264 A	T _{vj} = 25°C T _{vj} = 175°C	-	4.0 3.5	-	V
E _{rr}	Reverse Recovery Energy	$\begin{split} I_{SD} &= 620 \text{ A}, \\ V_{R} &= 400 \text{ V}, \\ V_{GS} &= -5 \text{ V}, \\ R_{g.on} &= 3.9 \Omega \end{split}$	$\begin{array}{l} \text{di/dt} = 5.3 \text{ A/ns,} \\ \text{T}_{\text{vj}} = 25^{\circ}\text{C} \\ \text{di/dt} = 4.7 \text{ A/ns,} \\ \text{T}_{\text{vj}} = 175^{\circ}\text{C} \end{array}$	-	0.4 1.3	-	mJ
Q _{rr}	Recovered Charge		T _{vj} = 25°C T _{vj} = 175°C	-	2.6 6.7	-	μC
l _{rr}	Peak Reverse Recovery Current		T _{vj} = 25°C T _{vj} = 175°C		115 215		A

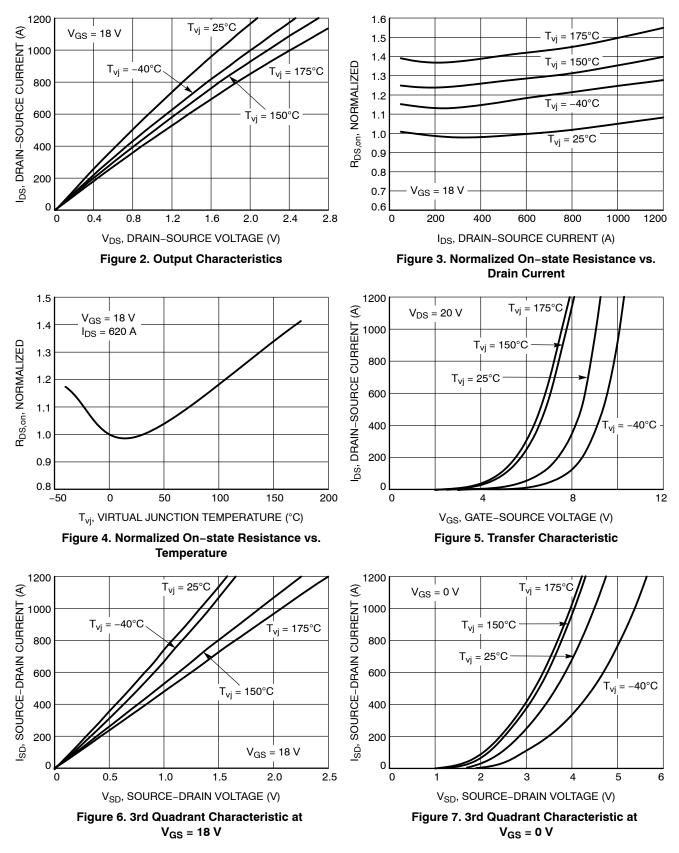
NTC SENSOR CHARACTERISTICS (T_{vj} = 25°C, Unless Otherwise Specified)

	Parameters	Conditions	Min	Тур	Max	Unit
R ₂₅	Rated Resistance	$T_c = 25^{\circ}C$	-	5	-	kΩ
$\Delta R/R$	Deviation of R25	$T_c = 25^{\circ}C, R25 = 5 k\Omega$	-5	-	5	%
P ₂₅	Power Dissipation	$T_c = 25^{\circ}C$	-	-	25	mW
B _{25/50}	B-Value	$R = R_{25} \exp [B_{25/50} (1/T - 1/298)]$	-	3375	-	К
B _{25/80}	B-Value	$R = R_{25} \exp [B_{25/80} (1/T - 1/298)]$	-	3411	-	К
B _{25/120}	B-Value	$R = R_{25} \exp \left[B_{25/120} \left(1/T - 1/298 \right) \right]$	1	3433	-	К

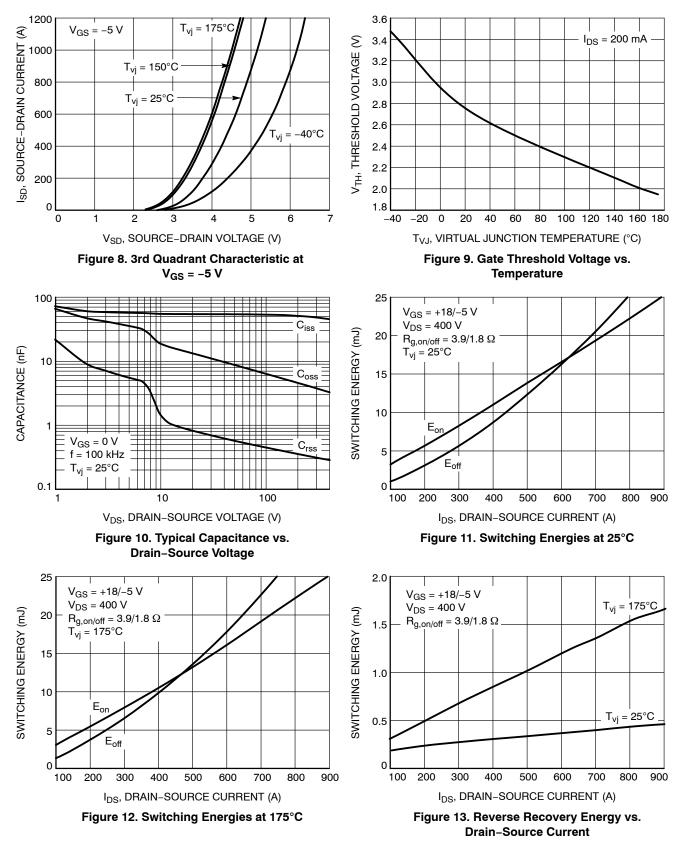
THERMAL CHARACTERISTICS

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
$R_{th,J-F}$	FET Junction to Fluid	$R_{\mbox{\tiny th}},$ Junction to Fluid, 10 L/min, 65°C, 50/50 EGW	-	0.10	0.11	°C/W

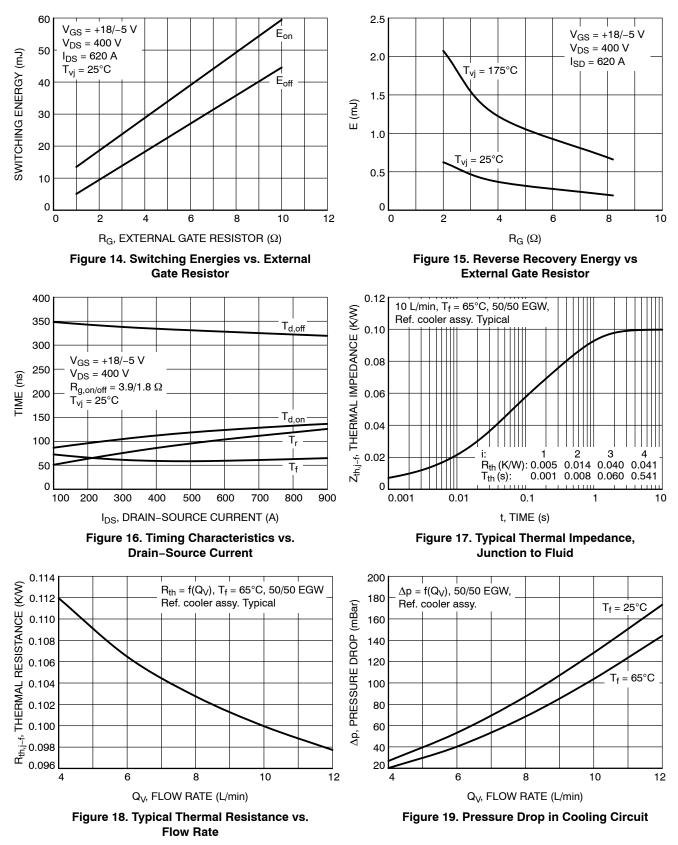
TYPICAL CHARACTERISTICS



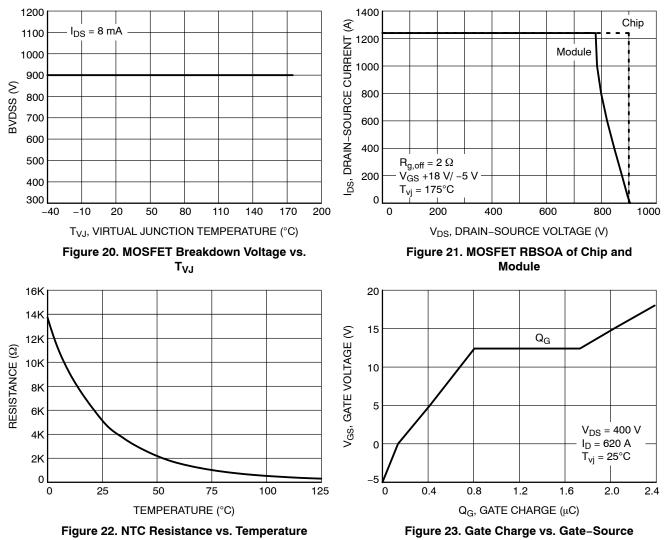
TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS



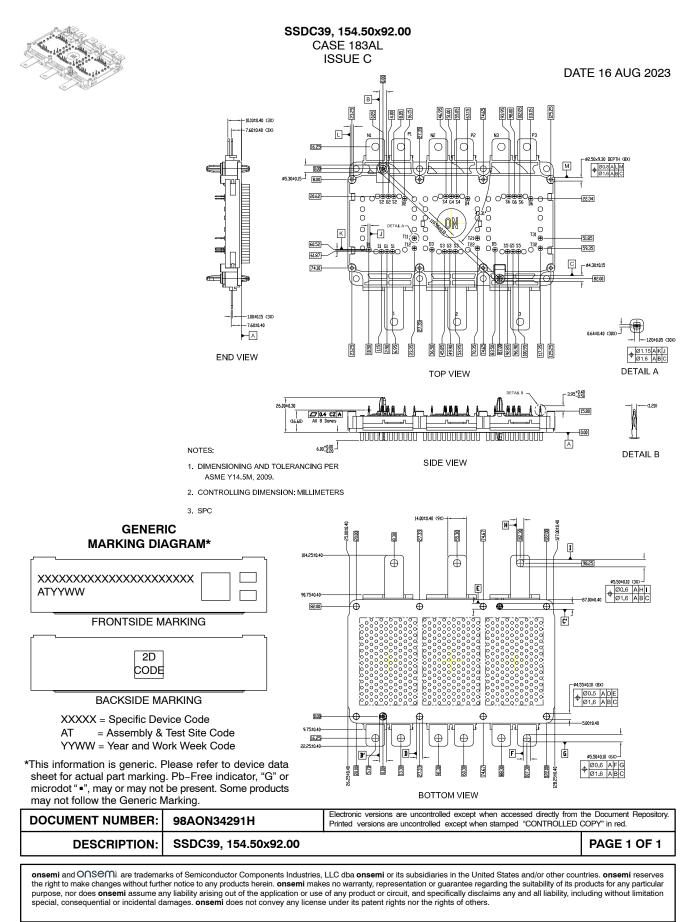




Voltage

MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

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