

MOSFET - SiC Power, Single N-Channel, TO247-3L 650 V, 44 mΩ, 47 A

NVHL060N065SC1

Features

- Typ. $R_{DS(on)} = 44 \text{ m}\Omega$ @ $V_{GS} = 18 \text{ V}$ Typ. $R_{DS(on)} = 60 \text{ m}\Omega$ @ $V_{GS} = 15 \text{ V}$
- Ultra Low Gate Charge $(Q_{G(tot)} = 74 \text{ nC})$
- High Speed Switching with Low Capacitance (Coss = 133 pF)
- 100% Avalanche Tested
- AEC-Q101 Qualified and PPAP Capable
- This Device is Pb-Free and is RoHS Compliant

Typical Applications

- Automotive On Board Charger
- Automotive DC/DC Converter for EV/HEV

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

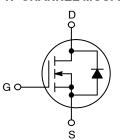
Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V_{DSS}	650	V
Gate-to-Source Voltage	Gate-to-Source Voltage			-8/+22	V
	Recommended Operation Values of Gate-to-Source Voltage		V_{GSop}	-5/+18	٧
Continuous Drain Current (Note 1)	Steady State	, 0		47	Α
Power Dissipation (Note 1)			P _D	176	W
Continuous Drain Current (Note 1)	Steady State	T _C = 100°C	I _D	33	Α
Power Dissipation (Note 1)			P _D	88	W
Pulsed Drain Current (Note 2)	T _C = 25°C		I _{DM}	143	Α
Operating Junction and Storage Temperature Range			T _J , T _{stg}	-55 to +175	°C
Source Current (Body Diode)			I _S	47	Α
Single Pulse Drain-to-Source Avalanche Energy (I _{L(pk)} = 10.1 A, L = 1 mH) (Note 3)			E _{AS}	51	mJ
Maximum Lead Temperature for Soldering (1/8" from case for 5 s)			TL	260	°C

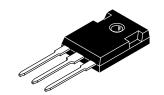
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 2. Repetitive rating, limited by max junction temperature.
- 3. EAS of 51 mJ is based on starting $T_J = 25^{\circ}C$; L = 1 mH, $I_{AS} = 10.1$ A, $V_{DD} = 50$ V, $V_{GS} = 18$ V.

V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX	
650 V	70 mΩ @ 18 V	47 A	

N-CHANNEL MOSFET





TO-247-3LD CASE 340CX

MARKING DIAGRAM



HL060N065SC1 = Specific Device Code

A = Assembly Location

Y = Year

WW = Work Week
ZZ = Lot Traceability

ORDERING INFORMATION

Device	Package	Shipping		
NVHL060N065SC1	TO247-3L	30 Units / Tube		

THERMAL RESISTANCE MAXIMUM RATINGS

Parameter	Symbol	Max	Unit
Junction-to-Case - Steady State (Note 1)	$R_{\theta JC}$	0.85	°C/W
Junction-to-Ambient - Steady State (Note 1)	$R_{\theta JA}$	40	

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS							
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = 1 mA		650	_	-	٧
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	I _D = 20 mA, referenced to 25°C		-	0.15	-	V/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V,	T _J = 25°C	-	_	10	μΑ
		V _{DS} = 650 V	T _J = 175°C	-	_	1	mA
Gate-to-Source Leakage Current	I _{GSS}	$V_{GS} = +18/-5 \text{ V}, \text{ V}$	V _{DS} = 0 V	-	_	250	nA
ON CHARACTERISTICS (Note 2)							
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_D =$	6.5 mA	1.8	2.8	4.3	V
Recommended Gate Voltage	V_{GOP}			-5	_	+18	V
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 15 V, I _D = 20 A	A, T _J = 25°C	-	60	-	mΩ
		V _{GS} = 18 V, I _D = 20 A	A, T _J = 25°C	-	44	70	
		V _{GS} = 18 V, I _D = 20 A	A, T _J = 175°C	-	49	-	
Forward Transconductance	9FS	V _{DS} = 10 V, I _D	= 20 A	-	12	_	S
CHARGES, CAPACITANCES & GATE RES	SISTANCE				•		
Input Capacitance	C _{ISS}	V _{GS} = 0 V, f = 1 MHz, V _{DS} = 325 V		-	1473	-	pF
Output Capacitance	C _{OSS}			-	133	-	
Reverse Transfer Capacitance	C _{RSS}			-	13	_	
Total Gate Charge	Q _{G(TOT)}	$V_{GS} = -5/18 \text{ V}, V_{DS} = 520 \text{ V},$ $I_{D} = 20 \text{ A}$		1	74	_	nC
Gate-to-Source Charge	Q _{GS}			1	20	_	
Gate-to-Drain Charge	Q_{GD}			1	23	_	
Gate-Resistance	R _G	f = 1 MHz		-	3.9	_	Ω
SWITCHING CHARACTERISTICS	•					•	
Turn-On Delay Time	t _{d(ON)}	$V_{GS} = -5/18$	8 V,	-	12	_	ns
Rise Time	t _r	V _{DS} = 400 I _D = 20 A		-	32	_	
Turn-Off Delay Time	t _{d(OFF)}	R _G = 2.2 s inductive lo		-	23	_	
Fall Time	t _f	madon o lo	, au	1	8	_	
Turn-On Switching Loss	E _{ON}			_	181	_	μJ
Turn-Off Switching Loss	E _{OFF}			_	25	_	
Total Switching Loss	E _{tot}			_	206	_	
DRAIN-SOURCE DIODE CHARACTERIST	ics				-		
Continuous Drain-Source Diode Forward Current	I _{SD}	$V_{GS} = -5 \text{ V}, T_J$	= 25°C	-	-	47	Α
Pulsed Drain-Source Diode Forward Current (Note 2)	I _{SDM}	1		-	-	143	
Forward Diode Voltage	V_{SD}	V _{GS} = -5 V, I _{SD} = 20 A, T _J = 25°C		-	4.3	-	V

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise specified) (continued)

`	· ·	1 / 1					
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit	
DRAIN-SOURCE DIODE CHARACTERISTICS							
Reverse Recovery Time	t _{RR}	$V_{GS} = -5/18 \text{ V}, I_{SD} = 20 \text{ A},$ $dI_{S}/dt = 1000 \text{ A}/\mu\text{s}$	-	18	-	ns	
Reverse Recovery Charge	Q_{RR}		-	85	-	nC	
Reverse Recovery Energy	E _{REC}		-	11	-	μJ	
Peak Reverse Recovery Current	I _{RRM}		-	10	-	Α	
Charge time	Ta		-	10	-	ns	
Discharge time	Tb	1	-	7.6	-	ns	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS

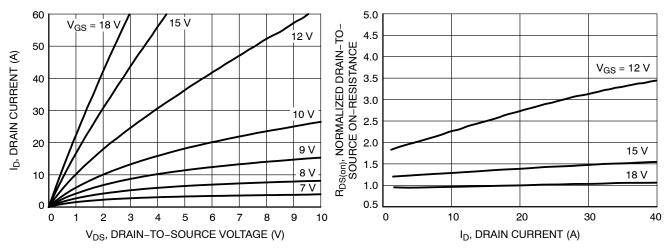


Figure 1. On-Region Characteristics

Figure 2. Normalized On–Resistance vs. Drain Current and Gate Voltage

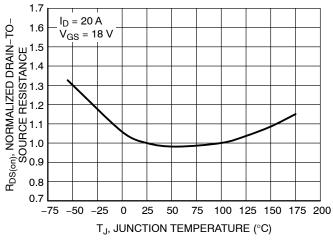


Figure 3. On–Resistance Variation with Temperature

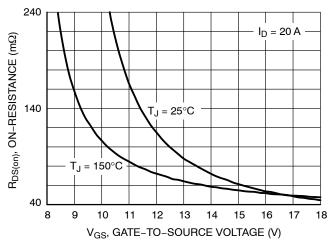


Figure 4. On-Resistance vs. Gate-to-Source Voltage

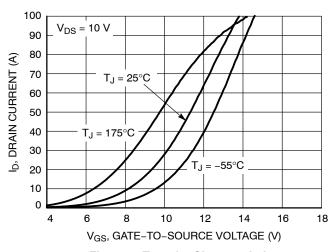


Figure 5. Transfer Characteristics

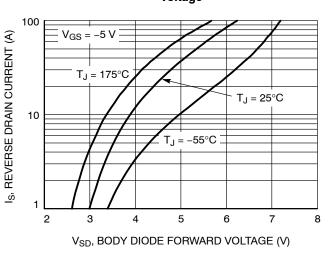


Figure 6. Diode Forward Voltage vs. Current

TYPICAL CHARACTERISTICS

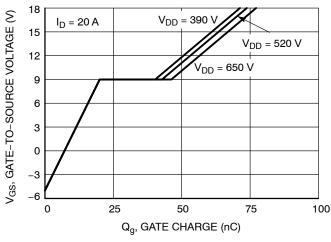


Figure 7. Gate-to-Source Voltage vs. Total Charge

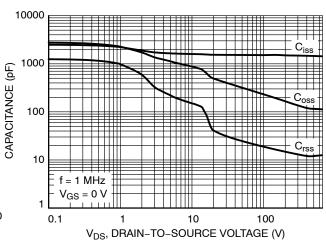


Figure 8. Capacitance vs. Drain-to-Source Voltage

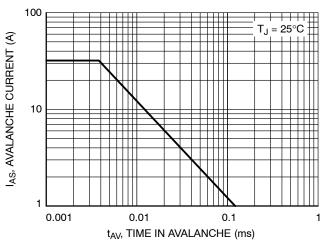


Figure 9. Unclamped Inductive Switching Capability

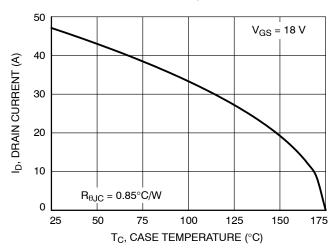


Figure 10. Maximum Continuous Drain **Current vs. Case Temperature**

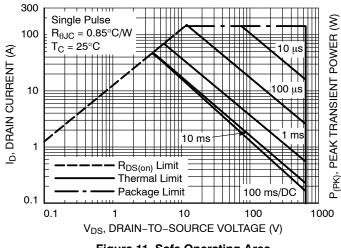


Figure 11. Safe Operating Area

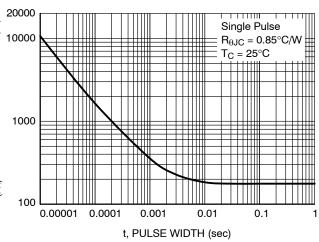


Figure 12. Single Pulse Maximum Power Dissipation

TYPICAL CHARACTERISTICS

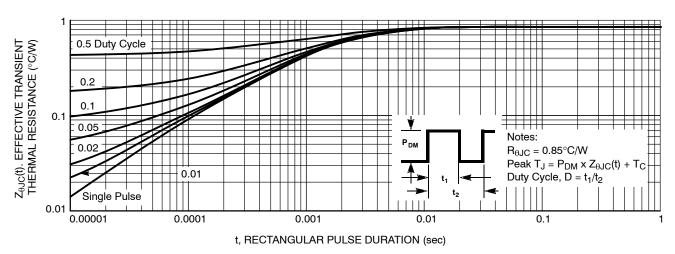
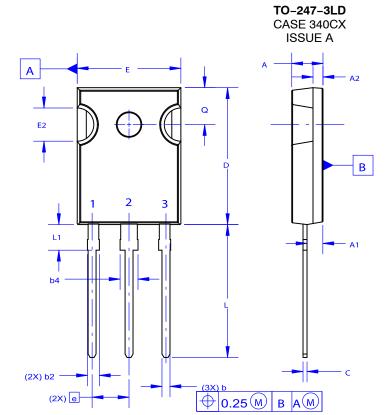


Figure 13. Junction-to-Case Thermal Response

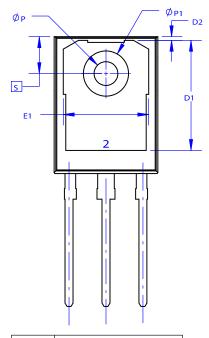
PACKAGE DIMENSIONS



NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.

 B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.



DIM	MILLIMETERS					
DIM	MIN	NOM	MAX			
Α	4.58	4.70	4.82			
A1	2.20	2.40	2.60			
A2	1.40	1.50	1.60			
D	20.32	20.57	20.82			
Е	15.37	15.62	15.87			
E2	4.96	5.08	5.20			
е	~	5.56	~			
L	19.75	20.00	20.25			
L1	3.69	3.81	3.93			
ØР	3.51	3.58	3.65			
Q	5.34	5.46	5.58			
S	5.34	5.46	5.58			
b	1.17	1.26	1.35			
b2	1.53	1.65	1.77			
b4	2.42	2.54	2.66			
С	0.51	0.61	0.71			
D1	13.08	~	~			
D2	0.51	0.93	1.35			
E1	12.81	~	~			
ØP1	6.60	6.80	7.00			

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