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# **MOSFET** - SiC Power, Single **N-Channel**

# 900 V, 60 mΩ, 46 A

## NTH4L060N090SC1

#### **Features**

- Typ.  $R_{DS(on)} = 60 \text{ m}\Omega @ V_{GS} = 15 \text{ V}$ Typ.  $R_{DS(on)} = 43 \text{ m}\Omega$  @  $V_{GS} = 18 \text{ V}$
- Ultra Low Gate Charge (typ.  $Q_{G(tot)} = 87 \text{ nC}$ )
- Low Effective Output Capacitance (typ. C<sub>oss</sub> = 113 pF)
- 100% UIL Tested
- These Devices are RoHS Compliant

## **Typical Applications**

- UPS
- DC/DC Converter
- Boost Inverter

## MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise noted)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V <sub>DSS</sub>	900	V
Gate-to-Source Voltage			V <sub>GS</sub>	+22/-8	V
Recommended Operation Values of Gate-to-Source Voltage	T <sub>C</sub> < 175°C		$V_{GSop}$	-5/+15	V
Continuous Drain Current $R_{\theta JC}$	Steady State T <sub>C</sub> = 25°0		I <sub>D</sub>	46	Α
Power Dissipation $R_{\theta JC}$	State		$P_{D}$	221	W
Continuous Drain Current $R_{\theta JC}$	Steady State	T <sub>C</sub> = 100°C	I <sub>D</sub>	32	Α
Power Dissipation $R_{\theta JC}$	Olaic		$P_{D}$	110	W
Pulsed Drain Current (Note 2)	T <sub>A</sub>	= 25°C	I <sub>DM</sub>	211	Α
Operating Junction and Storage Temperature Range			T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C
Source Current (Body Diode)			Is	22	Α
Single Pulse Drain-to-Source Avalanche Energy (I <sub>L(pk)</sub> = 18 A, L = 1 mH) (Note 3)			E <sub>AS</sub>	162	mJ

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### THERMAL RESISTANCE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Junction-to-Case (Note 1)	$R_{\theta JC}$	0.68	°C/W
Junction-to-Ambient (Note 1)	$R_{\theta JA}$	40	°C/W

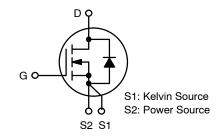
- 1. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 2. Repetitive rating, limited by max junction temperature. 3.  $E_{AS}$  of 162 mJ is based on starting  $T_J$  = 25°C; L = 1 mH,  $I_{AS}$  = 18 A,  $V_{DD}$  =  $100 \text{ V}, \text{ V}_{GS} = 15 \text{ V}.$



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V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub> MAX	I <sub>D</sub> MAX	
900 V	84 mΩ @ 15 V	46 A	



#### **N-CHANNEL MOSFET**



#### MARKING DIAGRAM



H4L060090SC1 = Specific Device Code = Assembly Site Υ = Year of Production WW =Work Week Number ZZ = Assembly Lot Number

#### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 2 of this data sheet.

## **ELECTRICAL CHARACTERISTICS**

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 1 mA	900			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /T <sub>J</sub>	I <sub>D</sub> = 1 mA, referenced to 25°C		574		mV/°C
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 900 V, T <sub>J</sub> = 25°C			100	μА
		V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 900 V, T <sub>J</sub> = 175°C			250	
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = +22/-8 V, V <sub>DS</sub> = 0 V			±1	μΑ
ON CHARACTERISTICS	•			•	•	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{GS} = V_{DS}$ , $I_D = 5 \text{ mA}$	1.8	2.7	4.3	V
Recommended Gate Voltage	$V_{GOP}$		-5		+15	V
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	$V_{GS} = 15 \text{ V}, I_D = 20 \text{ A}, T_J = 25^{\circ}\text{C}$		60	84	mΩ
		V <sub>GS</sub> = 18 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 25°C		43		
		V <sub>GS</sub> = 15 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 175°C		76		
Forward Transconductance	9FS	V <sub>DS</sub> = 20 V, I <sub>D</sub> = 20 A		17		S
CHARGES, CAPACITANCES & GATE	RESISTANCE			1		
Input Capacitance	C <sub>ISS</sub>			1770		pF
Output Capacitance	C <sub>OSS</sub>	V <sub>GS</sub> = 0 V, f = 1 MHz, V <sub>DS</sub> = 450 V		113		1 1
Reverse Transfer Capacitance	C <sub>RSS</sub>	1 -		11		
Total Gate Charge	Q <sub>G(tot)</sub>			87		nC
Threshold Gate Charge	Q <sub>G(th)</sub>	1		17		
Gate-to-Source Charge	Q <sub>GS</sub>	$V_{GS} = -5/15 \text{ V}, V_{DS} = 720 \text{ V}, I_D = 10 \text{ A}$		27		
Gate-to-Drain Charge	$Q_{GD}$	1		26		
Gate Resistance	R <sub>G</sub>	f = 1 MHz		3.0		Ω
SWITCHING CHARACTERISTICS	1			L	I	
Turn-On Delay Time	t <sub>d(on)</sub>			17	31	ns
Rise Time	t <sub>r</sub>	1		15	27	
Turn-Off Delay Time	t <sub>d(off)</sub>	1		29	47	
Fall Time	t <sub>f</sub>	$V_{GS}$ = -5/15 V, $V_{DS}$ = 720 V, $I_{D}$ = 20 A, $R_{G}$ = 2.5 Ω,		11	20	
Turn-On Switching Loss	E <sub>ON</sub>	Inductive Load		183		μJ
Turn-Off Switching Loss	E <sub>OFF</sub>	1		52		1
Total Switching Loss	E <sub>TOT</sub>	1		235		
DRAIN-SOURCE DIODE CHARACTE	1			I	I	
Continuous Drain-to-Source Diode Forward Current	I <sub>SD</sub>	V <sub>GS</sub> = -5 V, T <sub>J</sub> = 25°C			22	А
Pulsed Drain-to-Source Diode Forward Current (Note 2)	I <sub>SDM</sub>	V <sub>GS</sub> = -5 V, T <sub>J</sub> = 25°C			184	Α
Forward Diode Voltage	V <sub>SD</sub>	V <sub>GS</sub> = -5 V, I <sub>SD</sub> = 10 A, T <sub>J</sub> = 25°C		3.9		V
Reverse Recovery Time	t <sub>RR</sub>			18		ns
Reverse Recovery Charge	Q <sub>RR</sub>	1		84		nC
Reverse Recovery Energy	E <sub>REC</sub>	Vog = -5/15 V log = 30 Δ		1.0		μJ
Peak Reverse Recovery Current	I <sub>RRM</sub>	$V_{GS} = -5/15 \text{ V}, I_{SD} = 30 \text{ A},$ $dI_S/dt = 1000 \text{ A}/\mu\text{s}, V_{DS} = 720 \text{ V}$		9.0		A
Charge Time	t <sub>a</sub>	†		10		ns
Discharge Time	t <sub>b</sub>	†		8.0		ns
ŭ				1	L	L

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

### **TYPICAL CHARACTERISTICS**

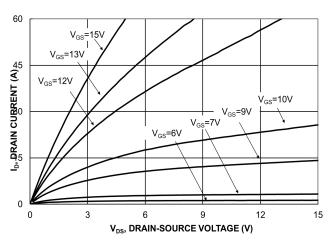


Figure 1. On-Region Characteristics

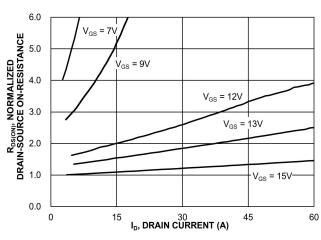


Figure 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

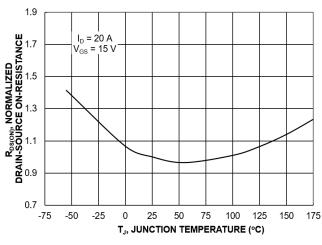


Figure 3. On–Resistance Variation with Temperature

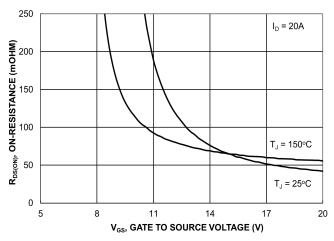


Figure 4. On-Resistance vs. Gate-to-Source Voltage

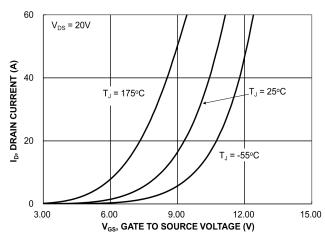


Figure 5. Transfer Characteristics

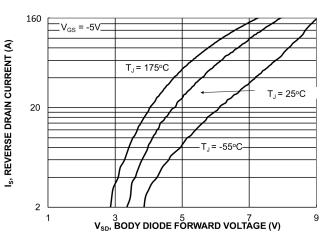


Figure 6. Diode Forward Voltage vs. Current

### **TYPICAL CHARACTERISTICS**

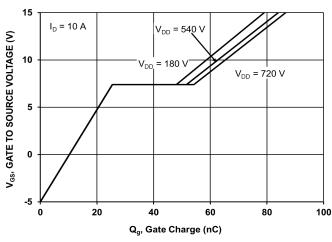


Figure 7. Gate-to-Source Voltage vs. Total Charge

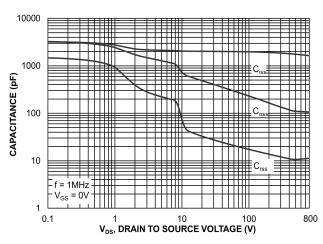


Figure 8. Capacitance vs. Drain-to-Source Voltage

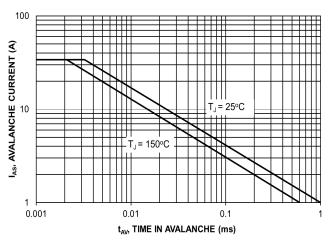


Figure 9. Unclamped Inductive Switching Capability

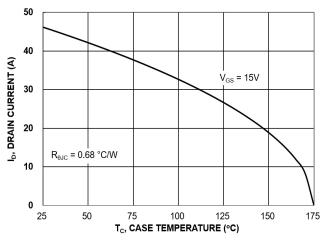


Figure 10. Maximum Continuous Drain Current vs. Case Temperature

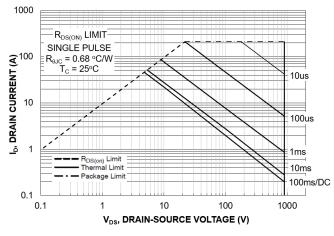


Figure 11. Safe Operating Area

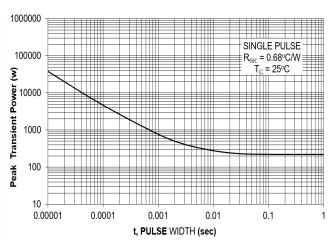


Figure 12. Single Pulse Maximum Power Dissipation

## **TYPICAL CHARACTERISTICS**

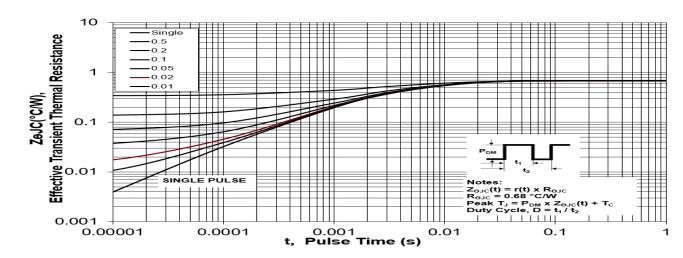


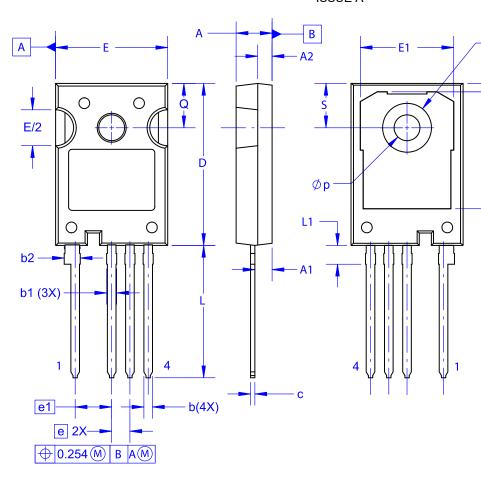
Figure 13. Junction-to-Ambient Thermal Response

## PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Packing Method	Reel Size	Tape Width	Quantity
NTH4L060N090SC1	H4L060 090SC1	TO247-4L	Tube	N/A	N/A	30 Units

## PACKAGE DIMENSIONS

### TO-247-4LD CASE 340CJ ISSUE A



#### NOTES:

- A. NO INDUSTRY STANDARD APPLIES TO THIS PACKAGE.
  B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD
  FLASH, AND TIE BAR EXTRUSIONS.
  C. ALL DIMENSIONS ARE IN MILLIMETERS.
  D. DRAWING CONFORMS TO ASME Y14.5-2009.

DIM	MILLIMETERS				
DIM	MIN	NOM	MAX		
Α	4.80	5.00	5.20		
A1	2.10	2.40	2.70		
A2	1.80	2.00	2.20		
b	1.07	1.20	1.33		
b1	1.20	1.40	1.60		
b2	2.02	2.22	2.42		
С	0.50	0.60	0.70		
D	22.34	22.54	22.74		
D1	16.00	16.25	16.50		
D2	0.97	1.17	1.37		
е	2.54 BSC				
e1	5.08 BSC				
E	15.40	15.60	15.80		
E1	12.80	13.00	13.20		
E/2	4.80	5.00	5.20		
L	18.22	18.42	18.62		
L1	2.42 2.62		2.82		
р	3.40 3.60		3.80		
p1	6.60	6.80	7.00		
Q	5.97	6.17	6.37		
S	5.97 6.17		6.37		

Øp1

D1

D2

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