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## NC7SVL32 TinyLogic<sup>®</sup> Low-I<sub>CCT</sub> Two-Input OR Gate

## Features

**FAIRCHILD** 

- 0.9V to 3.6V V<sub>CC</sub> Supply Operation
- 3.6V Over-Voltage Tolerant I/Os at Vcc from 0.9V to 3.6V
- Power-Off High-Impedance Inputs and Outputs
- Proprietary Quiet Series<sup>™</sup> Noise / EMI Reduction Circuitry
- Ultra-Small MicroPak<sup>™</sup> Packages
- Ultra-Low Dynamic Power

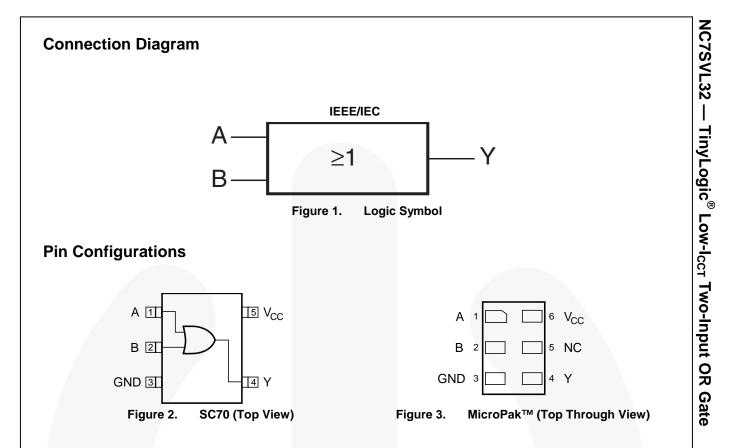
Description

The NC7SVL32 is a single two-input OR gate with a Low-I<sub>CCT</sub> input design from Fairchild's Ultra-Low Power (ULP-A) series of TinyLogic<sup>®</sup>. The NC7SVL32 features very low quiescent current, even when the input voltage is lower than the V<sub>CC</sub> supply. This feature services mobile handset applications very well, allowing for direct interface with baseband processor general-purpose I/Os. Since mobile devices rely on a battery supply, the NC7SVL32 facilitates lower power consumption in mixed-voltage rail environments.

This product is designed on an advanced CMOS technology for a wide low-voltage operating range (0.9V to 3.6V V<sub>CC</sub>), high drive needs (up to 24mA), and speed (maximum propagation delay of 3.5ns, V<sub>CC</sub>=3.3V). It achieves this performance while maintaining low CMOS power dissipation.

## **Ordering Information**

Part Number	Top Mark	Package	Packing Method
NC7SVL32P5X	L32	5-Lead SC70, EIAJ SC-88a, 1.25mm Wide	3000 Units on Tape & Reel
NC7SVL32L6X	CF	6-Lead MicroPak™, 1.00mm Wide	5000 Units on Tape & Reel
NC7SVL32FHX	CF	6-Lead, MicroPak2 <sup>™</sup> , 1x1mm Body, .35mm Pitch	5000 Units on Tape & Reel



## **Pin Definitions**

Pin # SC70	Pin # MicroPak™	Name	Description
1	1	A	Input
2	2	В	Input
3	3	GND	Ground
4	4	Y	Output
	5	5 NC No Cor	
5	6	Vcc	Supply Voltage

## **Function Table**

Y = A+B

Inp	outs	Output
A	В	Y
L	L	L
L	Н	Н
Н	L	Н
Н	Н	Н

H = HIGH Logic Level

L = LOW Logic Level

## Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Para	ameter	Min.	Max.	Unit
V <sub>CC</sub>	Supply Voltage		-0.5	4.6	V
V <sub>IN</sub>	DC Input Voltage		-0.5	4.6	V
14		HIGH or LOW State <sup>(1)</sup>	-0.5	V <sub>CC</sub> + 0.5	N/
Vout	DC Output Voltage	$V_{CC} = 0V$	-0.5	4.6	V
I <sub>IK</sub>	DC Input Diode Current	V <sub>IN</sub> < 0V		-50	mA
1	DC Output Diada Ourrant	V <sub>OUT</sub> < 0V		-50	
loκ	DC Output Diode Current	$V_{OUT} > V_{CC}$		+50	mA
$I_{OH}  /  I_{OL}$	DC Output Source/Sink Curren	t		±50	mA
I <sub>CC</sub> or I <sub>GND</sub>	DC V <sub>CC</sub> or Ground Current per	Supply Pin		±50	mA
T <sub>STG</sub>	Storage Temperature Range		-65	+150	°C
TJ	Junction Temperature Under B	ias		+150	°C
TL	Junction Lead Temperature, So	oldering 10 Seconds		+260	°C
		SC70-5		150	
PD	Power Dissipation at +85°C	MicroPak <sup>™</sup> -6		130	mW
		MicroPak2 <sup>™</sup> -6		120	
FOD	Human Body Model, JEDEC:JE	Human Body Model, JEDEC:JESD22-A114			V
ESD	Charge Device Model, JEDEC:	JESD22-C101		2000	V

### Note:

1. Io absolute maximum ratings must be observed.

## **Recommended Operating Conditions**

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Conditions	Min.	Max.	Unit	
V <sub>CC</sub>	Supply Voltage		0.9	3.6	V	
Vin	Input Voltage		0	3.6	V	
V		V <sub>CC</sub> =0V	0	3.6	V	
Vout	Output Voltage	HIGH or LOW State	0	V <sub>cc</sub>	v	
		V <sub>CC</sub> =3.0V to 3.6V		±24.0		
		V <sub>CC</sub> =2.3V to 2.7V		±18.0		
1 /1		V <sub>CC</sub> =1.65V to 1.95V		±6.0	mA	
I <sub>OH</sub> /I <sub>OL</sub>	Output Current in I <sub>OH</sub> /I <sub>OL</sub>	V <sub>CC</sub> =1.4V to 1.6V		±4.0		
		V <sub>CC</sub> =1.1V to 1.3V		±2.0		
		V <sub>CC</sub> =0.9V		±0.1		
T <sub>A</sub>	Operating Temperature, Free Air		-40	+85	°C	
$\Delta t / \Delta V$	Minimum Input Edge Rate	$V_{IN}$ =0.8V to 2.0, $V_{CC}$ =3.0V		10	ns/V	
		SC70-5		425		
$\theta_{JA}$	Thermal Resistance	MicroPak™-6		500	°C/W	
		MicroPak2 <sup>™</sup> -6		560	1	

### Note:

2. Unused inputs must be held HIGH or LOW. They may not float.

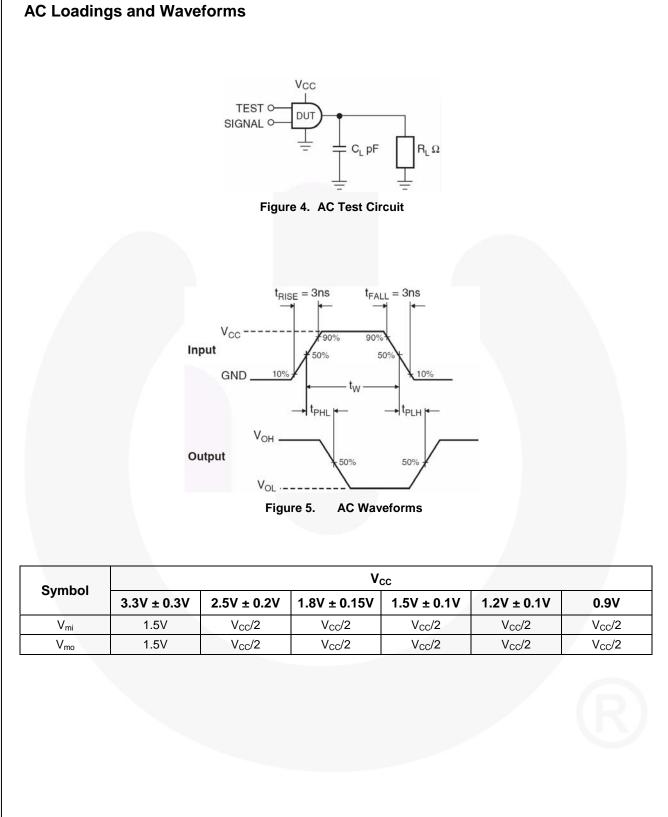
	Symbol Parameter		N N		T <sub>A</sub> =25°C		T <sub>A</sub> =-40 to +85°C		
Symbol		V <sub>cc</sub>	Conditions	Min.	Max.	Min.	Max.	Units	
		0.90		.65 x V <sub>CC</sub>		$.65 \times V_{CC}$			
		$1.10 \leq V_{CC} \leq 1.30$		.65 x V <sub>CC</sub>		$.65 \times V_{CC}$			
VIH	HIGH Level Input	$1.40 \leq V_{CC} \leq 1.60$		.65 x V <sub>CC</sub>		$.65 \ x \ V_{CC}$		v	
VIН	Voltage	$1.65 \leq V_{CC} \leq 1.95$		0.90		0.90		v	
		$2.30 \leq V_{CC} \leq 2.70$		1.50		1.50			
		$2.70 \leq V_{CC} \leq 3.60$		1.50		1.50			
		0.90			$.25 \text{ x V}_{CC}$		.25 x $V_{CC}$		
		$1.10 \leq V_{CC} \leq 1.30$			$.25 \text{ x V}_{CC}$		$.25 \text{ x V}_{CC}$		
V <sub>IL</sub>	LOW Level Input Voltage	$1.40 \leq V_{CC} \leq 1.60$			.25 x V <sub>CC</sub>		$.25 \text{ x V}_{CC}$	v	
		$1.65 \leq V_{CC} \leq 1.95$			$.25 \times V_{CC}$		.25 x $V_{CC}$	v	
		$2.30 \leq V_{CC} \leq 2.70$			0.70		0.70		
		$2.70 \leq V_{CC} \leq 3.60$			0.80		0.80		
		0.90		V <sub>cc</sub> -0.10		V <sub>cc</sub> -0.10		-	
		$1.10 \leq V_{CC} \leq 1.30$		V <sub>CC</sub> -0.10		V <sub>CC</sub> -0.10			
		$1.40 \leq V_{CC} \leq 1.60$	L	V <sub>CC</sub> -0.20		V <sub>CC</sub> -0.20			
		$1.65 \leq V_{CC} \leq 1.95$	I <sub>ОН</sub> =-100µА	V <sub>CC</sub> -0.20		V <sub>CC</sub> -0.20			
		$2.30 \leq V_{CC} \leq 2.70$		V <sub>CC</sub> -0.20		V <sub>CC</sub> -0.20			
		$2.70 \leq V_{CC} \leq 3.60$		V <sub>CC</sub> -0.20		V <sub>cc</sub> -0.20			
		$1.10 \leq V_{CC} \leq 1.30$	I <sub>OH</sub> =-2mA	.75 x V <sub>CC</sub>		$.75 \times V_{CC}$			
V <sub>OH</sub>	HIGH Level Output Voltage	$1.40 \leq V_{CC} \leq 1.60$	I <sub>OH</sub> =-4mA	.75 x V <sub>CC</sub>		.75 x V <sub>CC</sub>		V	
	Vollago	$1.65 \leq V_{CC} \leq 1.95$	I <sub>OH</sub> =-6mA	1.25		1.25		-	
		$2.30 \leq V_{CC} \leq 2.70$		2.00		2.00			
		$2.30 \leq V_{CC} \leq 2.70$	)	1.80		1.80			
		$2.70 \leq V_{CC} \leq 3.60$	I <sub>OH</sub> =-12mA	2.20		2.20			
		$2.30 \leq V_{CC} \leq 2.70$	10m A	1.70		1.70			
		$2.70 \leq V_{CC} \leq 3.60$	I <sub>OH</sub> =-18mA	2.40		2.40			
		$2.70 \leq V_{CC} \leq 3.60$	I <sub>OH</sub> =-24mA	2.20	7	2.20			

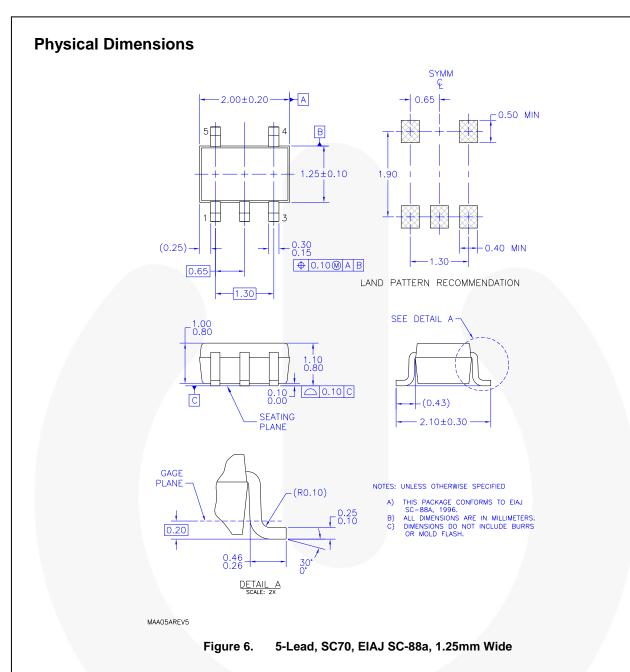
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DC Ele	ctrical Cha	racteristics (0	Continued)					
				T <sub>A</sub> =	25°C	T <sub>A</sub> =-40	to +85°C	
Symbol	Parameter	V <sub>cc</sub>	Conditions	Min.	Max.	Min.	Max.	Units
		0.90			0.1		0.1	
		$1.10 \leq V_{CC} \leq 1.30$			0.1		0.1	
		$1.40 \leq V_{CC} \leq 1.60$	Ι <sub>ΟL</sub> =100μΑ		0.2		0.2	
		$1.65 \leq V_{CC} \leq 1.95$		2	0.2		0.2	
		$2.30 \leq V_{CC} \leq 2.70$			0.2		0.2	
		$2.70 \leq V_{CC} \leq 3.60$			0.2		0.2	
N	LOW Level	$1.10 \leq V_{CC} \leq 1.30$	I <sub>OL</sub> =2mA		$0.25 \text{ x V}_{CC}$		$0.25 \text{ x V}_{CC}$	V
V <sub>OL</sub>	Output Voltage	$1.40 \leq V_{CC} \leq 1.60$	I <sub>OL</sub> =4mA		$0.25 \text{ x V}_{CC}$		$0.25 \text{ x V}_{CC}$	
		$1.65 \leq V_{CC} \leq 1.95$	$I_{OL}=6mA$ $I_{OL}=12mA$		0.30		0.3	
		$2.30 \leq V_{CC} \leq 2.70$			0.40		0.40	
		$2.70 \leq V_{CC} \leq 3.60$			0.40		0.40	
		$2.30 \leq V_{CC} \leq 2.70$	loL=18mA		0.60		0.60	
		$2.70 \leq V_{CC} \leq 3.60$			0.40		0.40	
		$2.70 \leq V_{CC} \leq 3.60$	I <sub>OL</sub> =24mA		0.55		0.55	
l <sub>iN</sub>	Input Leakage Current	0.90 to 3.60	$0 \leq V_{IN} \leq 3.60V$		±0.1		±0.5	μA
I <sub>OFF</sub>	Power Off Leakage Current	0	$0 \leq (V_{IN,}  V_O) \leq 3.60 V$		0.5		0.5	μA
	Quiescent	0.00 to 0.00	V <sub>IN</sub> =V <sub>CC</sub> , or GND		0.9		0.9	
Icc	Supply Current	0.90 to 3.60	$V_{CC} \leq V_{IN} \leq 3.60 V$			±0.9		μA
Ісст	Increase in I <sub>CC</sub>	1.95	V <sub>IN</sub> =0.9V		6		8	
ICCT	per Input	3.60	V <sub>IN</sub> =1.5V		6		8	μA

## **AC Electrical Characteristics**

Symphol	Parameter	N	V <sub>cc</sub> Conditions		T <sub>A</sub> =25°	С	T <sub>A</sub> =-40	to 85°C	Unito	Figure
Symbol	Parameter	V <sub>cc</sub>	Conditions	Min.	Тур.	Max.	Min.	Max.	Units	Figure
		0.90	$C_L$ =15pF, $R_L$ =1M $\Omega$		42.0					
		$1.10 \leq V_{CC} \leq 1.30$		3.5	8.2	17.0	3.0	30.5		
	Propagation	$1.40 \leq V_{CC} \leq 1.60$	$C_L=15pF, R_L=2k\Omega$	1.5	4.0	7.0	1.5	7.5		Figure 4
t <sub>PHL</sub> , t <sub>PLH</sub>	Delay	$1.65 \leq V_{CC} \leq 1.95$		1.1	3.2	5.5	1.0	6.0	- ns	Figure 5
		$2.30 \leq V_{CC} \leq 2.70$	$C_L=30pF, R_L=500\Omega$	0.6	2.3	4.0	0.6	4.5		
		$2.70 \leq V_{CC} \leq 3.60$	0	0.5	1.9	3.5	0.5	4.0		
C <sub>IN</sub>	Input Capacitance	0			3				pF	K
C <sub>PD</sub>	Power Dissipation Capacitance	0.90 to 3.60	V <sub>IN</sub> =0V or V <sub>CC</sub> , f=10MHz		5				pF	





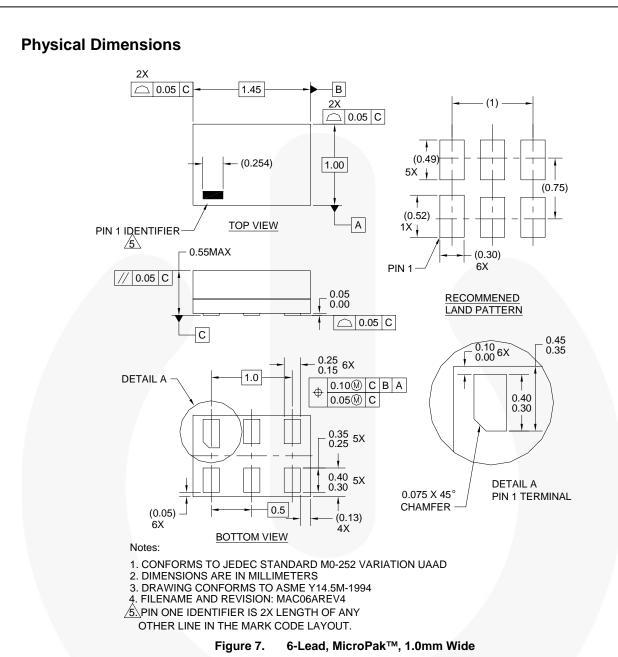
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Package Designator	Tape Section	Cavity Number	Cavity Status	Cover Type Status
	Leader (Start End)	125 (Typical)	Empty	Sealed
P5X	Carrier	3000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed



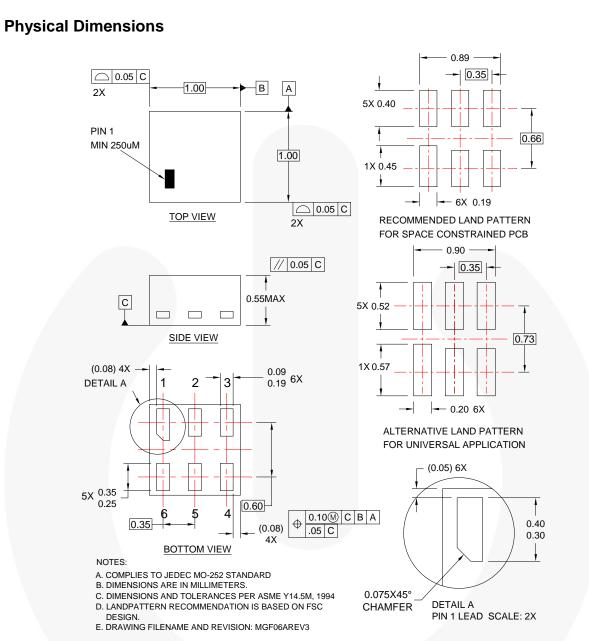
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Package Designator	Tape Section	Tape Section Cavity Number		Cover Type Status
	Leader (Start End)	125 (Typical)	Empty	Sealed
L6X	Carrier	5000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed



### Figure 8. 6-Lead, MicroPak2<sup>™</sup>, 1x1mm Body, .35mm Pitch

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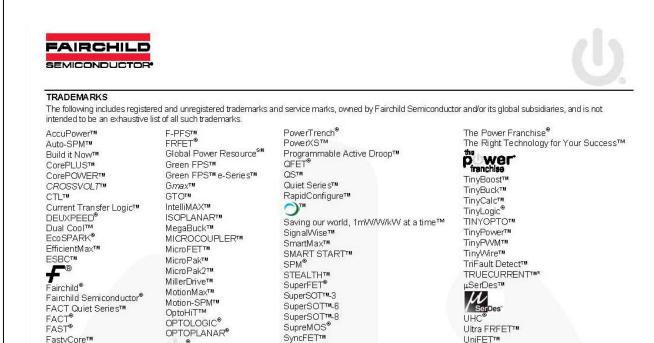
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Package Designator	Tape Section	Tape Section Cavity Number		Cover Type Status
	Leader (Start End)	125 (Typical)	Empty	Sealed
FHX	Carrier	5000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed

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