## **ON Semiconductor**

## Is Now



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# NPN - MPS8099; PNP - MPS8599

Preferred Device

## **Amplifier Transistors**

# **Voltage and Current are Negative for PNP Transistors**

### **Features**

• Pb-Free Packages are Available\*

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector - Emitter Voltage	V <sub>CEO</sub>	80	Vdc
Collector - Base Voltage	V <sub>CBO</sub>	80	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	4.0	Vdc
Collector Current – Continuous	I <sub>C</sub>	500	mAdc
Total Device Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	625 5.0	mW mW/°C
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

## THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction–to–Ambient (Note 1)	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W

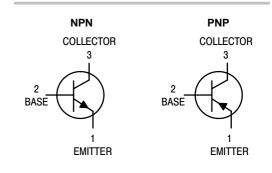
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

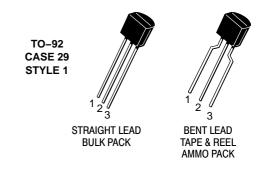
1.  $R_{\theta JA}$  is measured with the device soldered into a typical printed circuit board.



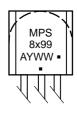
## ON Semiconductor®

http://onsemi.com





## **MARKING DIAGRAM**



x = 0 or 5

A = Assembly Location

Y = Year

WW = Work Week

= Pb-Free Package

(Note: Microdot may be in either location)

## **ORDERING INFORMATION**

See detailed ordering and shipping information in the package dimensions section on page 3 of this data sheet.

**Preferred** devices are recommended choices for future use and best overall value.

<sup>\*</sup>For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

## **NPN - MPS8099; PNP - MPS8599**

## **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS	<u>.</u>			
Collector – Emitter Breakdown Voltage (Note 2) (I <sub>C</sub> = 10 mAdc, I <sub>B</sub> = 0)	V <sub>(BR)</sub> CEO	80	_	Vdc
Collector – Base Breakdown Voltage ( $I_C = 100 \mu Adc$ , $I_E = 0$ )	V <sub>(BR)</sub> CBO	80	-	Vdc
Emitter – Base Breakdown Voltage ( $I_E = 10 \mu Adc, I_C = 0$ )	V <sub>(BR)EBO</sub>	5.0	-	Vdc
Collector Cutoff Current (V <sub>CE</sub> = 60 Vdc, I <sub>B</sub> = 0)	I <sub>CES</sub>	_	0.1	μAdc
Collector Cutoff Current (V <sub>CB</sub> = 80 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>	_	0.1	μAdc
Emitter Cutoff Current $(V_{EB} = 4.0 \text{ Vdc}, I_C = 0)$	I <sub>EBO</sub>	_	0.1	μAdc
ON CHARACTERISTICS (Note 2)	•	•	•	•
DC Current Gain ( $I_{C} = 1.0 \text{ mAdc}$ , $V_{CE} = 5.0 \text{ Vdc}$ ) ( $I_{C} = 10 \text{ mAdc}$ , $V_{CE} = 5.0 \text{ Vdc}$ ) ( $I_{C} = 100 \text{ mAdc}$ , $V_{CE} = 5.0 \text{ Vdc}$ )	h <sub>FE</sub>	100 100 75	300 - -	-
Collector – Emitter Saturation Voltage ( $I_C = 100 \text{ mAdc}$ , $I_B = 5.0 \text{ mAdc}$ ) ( $I_C = 100 \text{ mAdc}$ , $I_B = 10 \text{ mAdc}$ )	V <sub>CE(sat)</sub>	- -	0.4 0.3	Vdc
Base–Emitter On Voltage (I <sub>C</sub> = 10 mAdc, V <sub>CE</sub> = 5.0 Vdc)	V <sub>BE(on)</sub>	0.6	0.8	Vdc
SMALL-SIGNAL CHARACTERISTICS				
Current – Gain – Bandwidth Product (I <sub>C</sub> = 10 mAdc, V <sub>CE</sub> = 5.0 Vdc, f = 100 MHz)	f <sub>T</sub>	150	_	MHz
Output Capacitance ( $V_{CB} = 5.0 \text{ Vdc}$ , $I_{E} = 0$ , $f = 1.0 \text{ MHz}$ )	C <sub>obo</sub>	_	8.0	pF
Input Capacitance (V <sub>EB</sub> = 0.5 Vdc, I <sub>C</sub> = 0, f = 1.0 MHz)	C <sub>ibo</sub>	_	30	pF

<sup>2.</sup> Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle = 2.0%.

### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
MPS8099	TO-92	5000 Units / Bulk
MPS8099G	TO-92 (Pb-Free)	5000 Units / Bulk
MPS8099RLRA	TO-92	2000 / Tape & Reel
MPS8099RLRAG	TO-92 (Pb-Free)	2000 / Tape & Reel
MPS8099RLRP	TO-92	2000 / Ammo Pack
MPS8099RLRPG	TO-92 (Pb-Free)	2000 / Ammo Pack
MPS8599RLRA	TO-92	2000 / Tape & Reel
MPS8599RLRAG	TO-92 (Pb-Free)	2000 / Tape & Reel
MPS8599RLRMG	TO-92 (Pb-Free)	2000 / Ammo Pack

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

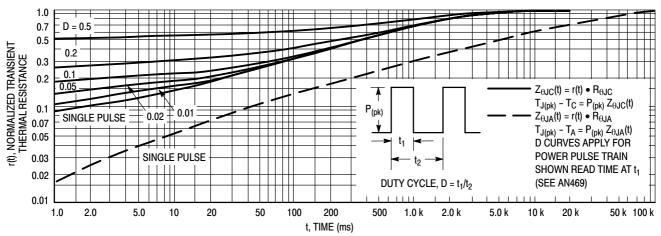
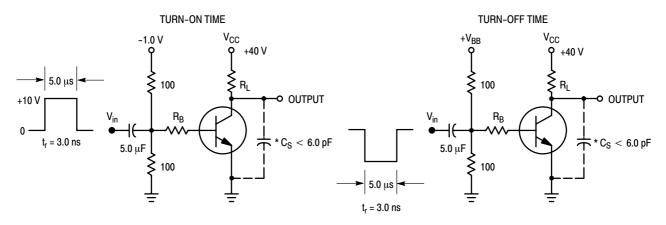


Figure 1. Thermal Response



\*Total Shunt Capacitance of Test Jig and Connectors For PNP Test Circuits, Reverse All Voltage Polarities

**Figure 2. Switching Time Test Circuits** 

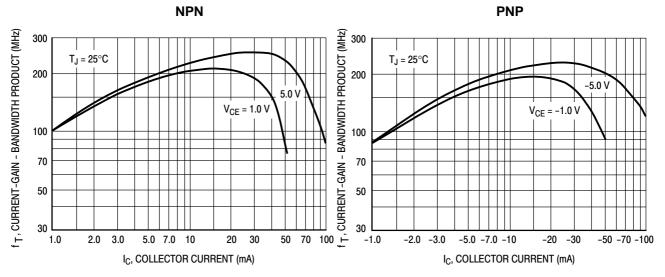


Figure 3. Current-Gain - Bandwidth Product

Figure 4. Current-Gain - Bandwidth Product

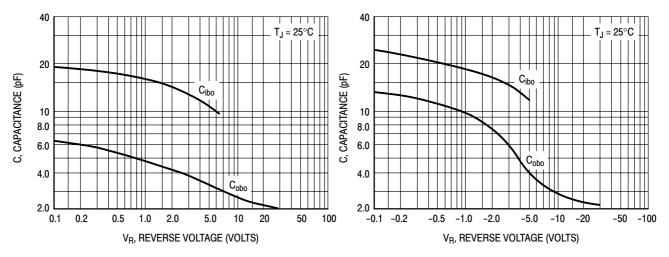


Figure 5. Capacitance

Figure 6. Capacitance

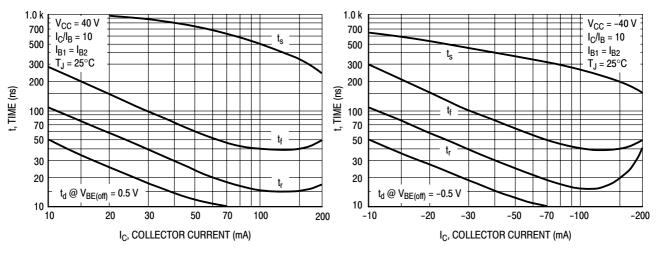


Figure 7. Switching Times

Figure 8. Switching Times

NPN PNP

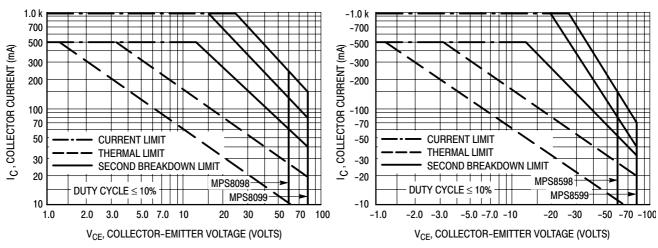


Figure 9. Active-Region Safe Operating Area

Figure 10. Active-Region Safe Operating Area

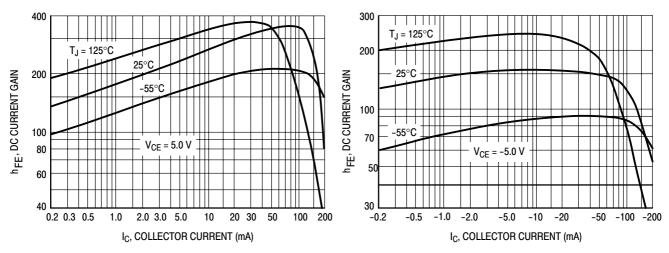


Figure 11. DC Current Gain

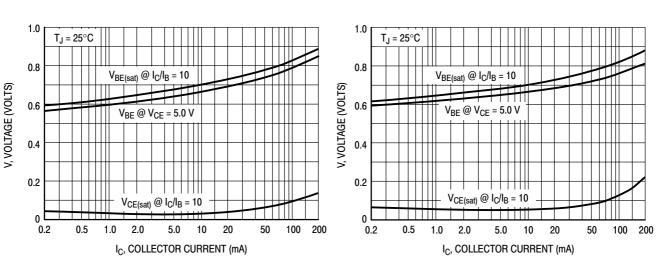


Figure 13. "ON" Voltages

Figure 14. "ON" Voltages

Figure 12. DC Current Gain

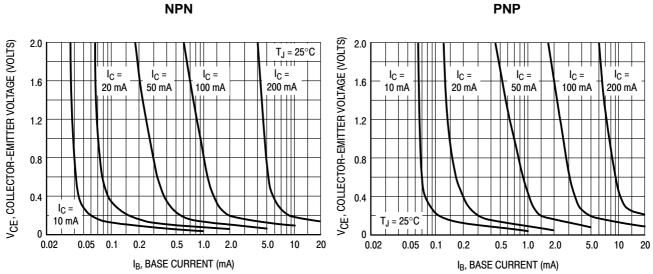


Figure 15. Collector Saturation Region

Figure 16. Collector Saturation Region

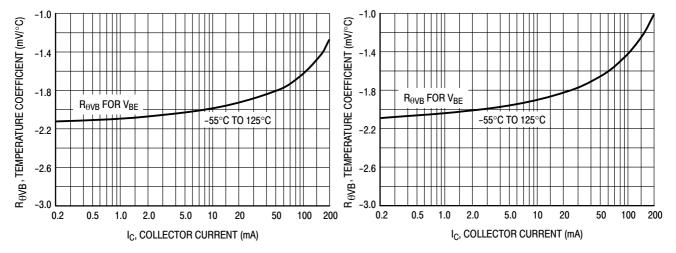


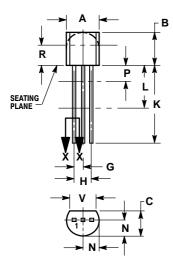
Figure 17. Base-Emitter Temperature Coefficient

Figure 18. Base-Emitter Temperature Coefficient

## NPN - MPS8099; PNP - MPS8599

### PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 **ISSUE AM** 



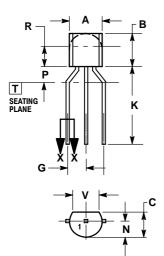
STRAIGHT LEAD **BULK PACK** 



#### NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED
- LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
P		0.100		2.54
R	0.115		2.93	
٧	0.135		3 43	



**BENT LEAD** TAPE & REEL AMMO PACK



#### NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- CONTROLLING DIMENSION: MILLIMETERS.
  CONTOUR OF PACKAGE BEYOND
- DIMENSION R IS UNCONTROLLED
- LEAD DIMENSION IS UNCONTROLLED IN PAND BEYOND DIMENSION K MINIMUM.

	MILLIMETERS		
DIM	MIN MAX		
Α	4.45	5.20	
В	4.32	5.33	
С	3.18	4.19	
D	0.40	0.54	
G	2.40	2.80	
J	0.39	0.50	
K	12.70		
N	2.04	2.66	
Р	1.50	4.00	
R	2.93		
٧	3.43		

STYLE 1:

PIN 1 FMITTER

BASE

COLLECTOR

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