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Dual Series High Voltage Switching Diode

Product Preview

MMBD1401ALT1G, MMBD1403ALT1G

Features

- Moisture Sensitivity Level: 1
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Continuous Reverse Voltage	V_R	250	Vdc
Repetitive Peak Reverse Voltage	V_{RRM}	250	Vdc
Peak Forward Current	I_F	225	mA dc
Peak Forward Surge Current	$I_{FM(surge)}$	625	mA dc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1) $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	225	mW
		1.8	mW/ $^\circ\text{C}$
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	556	$^\circ\text{C/W}$
Total Device Dissipation Alumina Substrate, (Note 2) $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	300	mW
		2.4	mW/ $^\circ\text{C}$
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	417	$^\circ\text{C/W}$
Junction and Storage Temperature Range	T_J, T_{stg}	-55 to +150	$^\circ\text{C}$

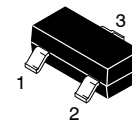
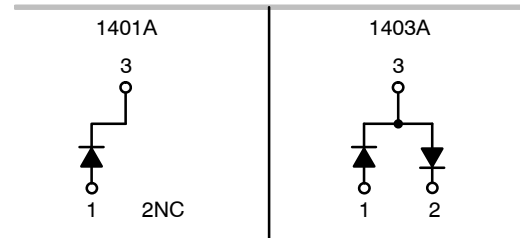
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- FR-5 = $1.0 \times 0.75 \times 0.062$ in.
- Alumina = $0.4 \times 0.3 \times 0.024$ in. 99.5% alumina.



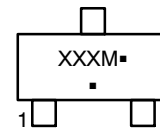
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SOT-23
CASE 318

MARKING DIAGRAM



XXX = Specific Device Code

MMBD1401: A29

MMBD1403: A32

M = Date Code*

▪ = Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping†
MMBD1401ALT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel
MMBD1403ALT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

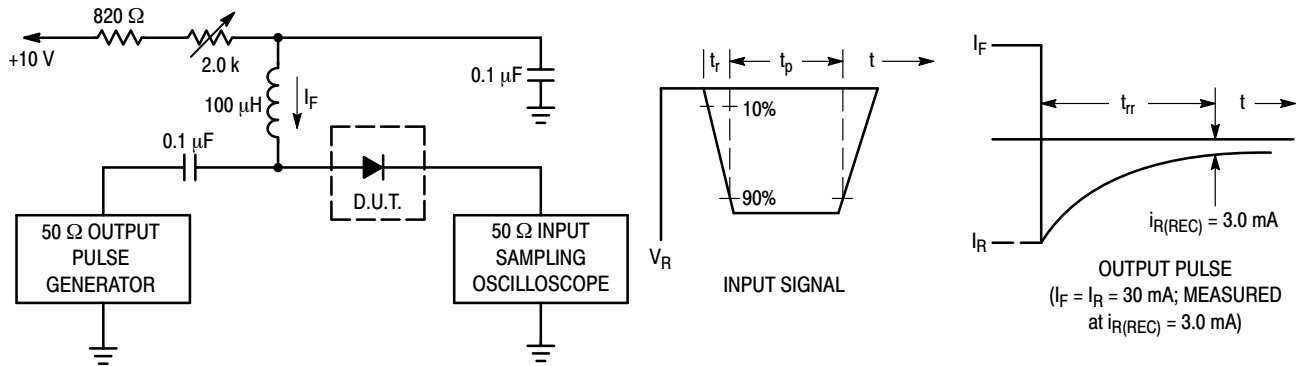
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MMBD1401ALT1G, MMBD1403ALT1G

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Reverse Voltage Leakage Current ($V_R = 200\text{ Vdc}$) ($V_R = 175\text{ Vdc}$)	I_R	– –	40 100	nA
Reverse Breakdown Voltage ($I_{BR} = 100\text{ }\mu\text{Adc}$)	$V_{(BR)}$	250	–	V
Forward Voltage ($I_F = 10\text{ mA}$) ($I_F = 50\text{ mA}$) ($I_F = 200\text{ mA}$) ($I_F = 300\text{ mA}$)	V_F	– 760 – –	800 920 1.1 1.25	mV mV V V
Diode Capacitance ($V_R = 0$, $f = 1.0\text{ MHz}$)	C_D	–	5.0	pF
Reverse Recovery Time ($I_F = I_R = 30\text{ mA}$, $I_{RR} = 1.0\text{ mA}$, $R_L = 100\text{ }\Omega$)	t_{rr}	–	50	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



- Notes: 1. A 2.0 k Ω variable resistor adjusted for a Forward Current (I_F) of 30 mA.
 2. Input pulse is adjusted so $I_{R(\text{peak})}$ is equal to 30 mA.
 3. $t_p \gg t_{rr}$

Figure 1. Recovery Time Equivalent Test Circuit

MMBD1401ALT1G, MMBD1403ALT1G

TYPICAL CHARACTERISTICS

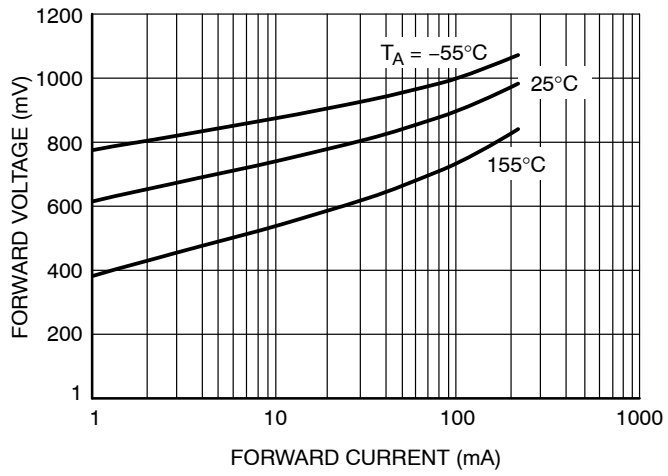


Figure 2. Forward Voltage

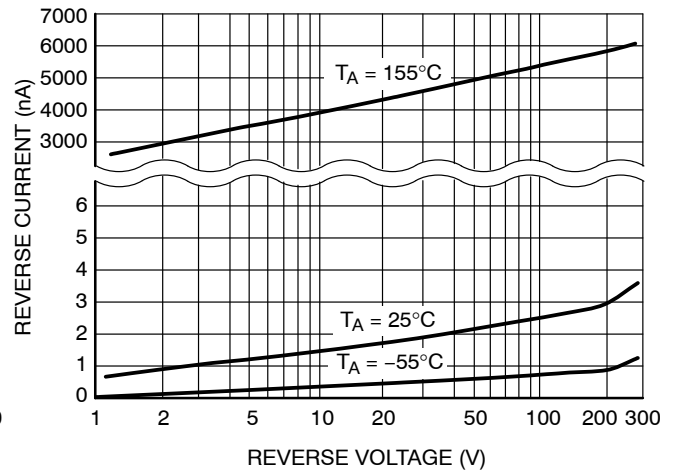


Figure 3. Reverse Leakage

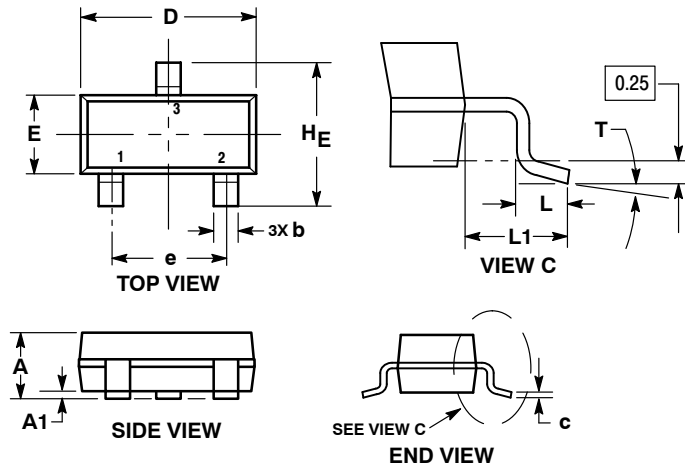
MMBD1401ALT1G, MMBD1403ALT1G

PACKAGE DIMENSIONS

SOT-23 (TO-236)

CASE 318-08

ISSUE AS

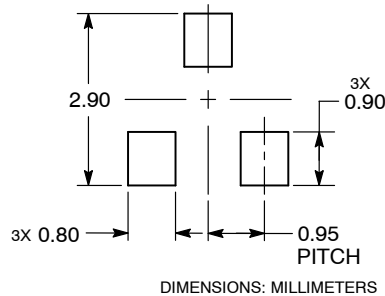


NOTES:


1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.89	1.00	1.11	0.035	0.039	0.044
A1	0.01	0.06	0.10	0.000	0.002	0.004
b	0.37	0.44	0.50	0.015	0.017	0.020
c	0.08	0.14	0.20	0.003	0.006	0.008
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
e	1.78	1.90	2.04	0.070	0.075	0.080
L	0.30	0.43	0.55	0.012	0.017	0.022
L1	0.35	0.54	0.69	0.014	0.021	0.027
HE	2.10	2.40	2.64	0.083	0.094	0.104
T	0°	---	10°	0°	---	10°

RECOMMENDED SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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