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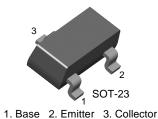
# KST4403

# FAIRCHILD

SEMICONDUCTOR®

### KST4403

#### **Switching Transistor**



## PNP Epitaxial Silicon Transistor

#### Absolute Maximum Ratings $T_a=25$ °C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CBO</sub>	Collector-Base Voltage	-40	V
V <sub>CEO</sub>	Collector-Emitter Voltage	-40	V
V <sub>EBO</sub>	Emitter-Base Voltage	-5	V
c	Collector Current	-600	mA
P <sub>C</sub>	Collector Power Dissipation	350	mW
T <sub>STG</sub>	Storage Temperature	150	°C

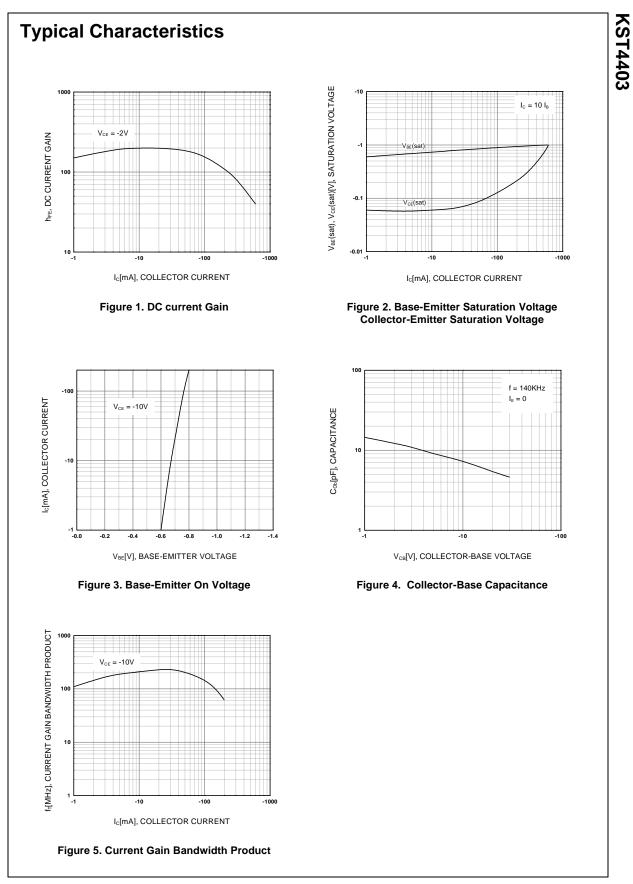
#### Electrical Characteristics T<sub>a</sub>=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = -0.1mA, I <sub>E</sub> =0	-40		V
BV <sub>CEO</sub>	* Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -1.0mA, I <sub>B</sub> =0	-40		V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = -0.1mA, I <sub>C</sub> =0	-5		V
I <sub>BEV</sub>	Base Cut-off Current	V <sub>CE</sub> = -35V, V <sub>BE</sub> = -0.4V		-0.1	μA
I <sub>CEX</sub>	Collector Cut-off Current	V <sub>CE</sub> = -35V, V <sub>BE</sub> = -0.4V		-0.1	μA
h <sub>FE</sub>	DC Current Gain	$V_{CE}$ = -1V, I <sub>C</sub> = -0.1mA $V_{CE}$ = -1V, I <sub>C</sub> = -1.0mA $V_{CE}$ = -1V, I <sub>C</sub> = -10mA $^{*}V_{CE}$ = -2V, IC= -150mA $^{*}V_{CE}$ = -2V, I <sub>C</sub> = -500mA	30 60 100 100 20	300	
V <sub>CE</sub> (sat)	* Collector-Emitter Saturation Voltage	I <sub>C</sub> = -150mA, I <sub>B</sub> = -15mA I <sub>C</sub> = -500mA, I <sub>B</sub> = -50mA		-0.4 -0.75	V V
V <sub>BE</sub> (sat)	* Base-Emitter Saturation Voltage	I <sub>C</sub> = -150mA, I <sub>B</sub> = -15mA I <sub>C</sub> = -500mA, I <sub>B</sub> = -50mA	-0.75	-0.95 -1.3	V V
f <sub>T</sub>	Current Gain Bandwidth Product	I <sub>C</sub> = -20mA, V <sub>CE</sub> = -10V f=100MHz	200		MHz
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> = -10V, I <sub>E</sub> =0 f=140KHz		8.5	pF
t <sub>ON</sub>	Turn On Time	V <sub>CC</sub> = -30V, V <sub>BE</sub> = -2V I <sub>C</sub> = -150mA, I <sub>B1</sub> = -15mA		35	ns
t <sub>OFF</sub>	Turn Off Time	$V_{CC}$ = -30V, I <sub>C</sub> = -150mA I <sub>B1</sub> =I <sub>B2</sub> = -15mA		255	ns

\* Pulse Test: Pulse Width ${\leq}300\mu s,$  Duty Cycle ${\leq}2\%$ 

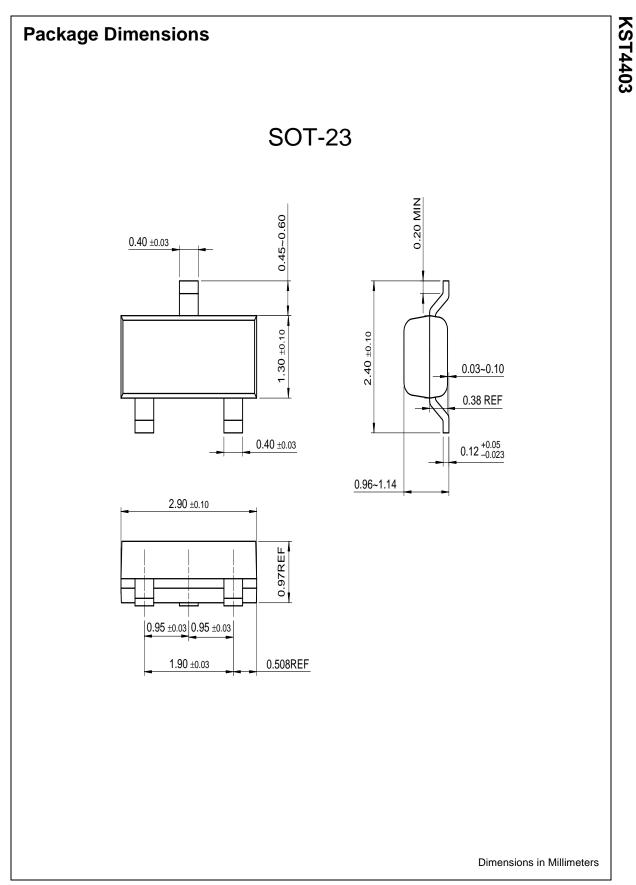






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