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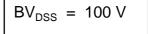
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Advanced Power MOSFET

IRFP150A

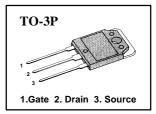
FEATURES

- Avalanche Rugged Technology
- Rugged Gate Oxide Technology
- Lower Input Capacitance
- Improved Gate Charge
- Extended Safe Operating Area
- 175°C Operating Temperature
- Lower Leakage Current : 10 µA (Max.) @ V_{DS} = 100V
- Lower $R_{DS(ON)}$: 0.032 Ω (Typ.)



 $R_{DS(on)} = 0.04 \ \Omega$

 $I_D = 43 A$



Absolute Maximum Ratings

Symbol	Characteristic	Value	Units		
V _{DSS}	Drain-to-Source Voltage		100	V	
	Continuous Drain Current (T _c =25°C)		43	4	
۱ _D	Continuous Drain Current (T _c =100 °C)	30.4	A		
I _{DM}	Drain Current-Pulsed	1	170	А	
V _{GS}	Gate-to-Source Voltage		± 20	V	
E _{AS}	Single Pulsed Avalanche Energy	2	740	mJ	
I _{AR}	Avalanche Current	1	43	А	
E _{AR}	Repetitive Avalanche Energy	Ð	19.3	mJ	
dv/dt	Peak Diode Recovery dv/dt	3	6.5	V/ns	
P	Total Power Dissipation (T _c =25° _C)		193	W	
P _D	Linear Derating Factor		1.28	W/°C	
	Operating Junction and				
T_J , T_STG	Storage Temperature Range		- 55 to +175		
- T	Maximum Lead Temp. for Soldering		200	°C	
TL	Purposes, 1/8" from case for 5-seconds		300		

Thermal Resistance

Symbol	Characteristic	Тур.	Max.	Units
R _{θJC}	Junction-to-Case		0.78	
R _{θCS}	Case-to-Sink	0.24		°C/W
R _{θJA}	Junction-to-Ambient		40	



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Symbol	Characteristic	Min.	Тур.	Max.	Units	Test Condition
BV _{DSS}	Drain-Source Breakdown Voltage	100			V	V _{GS} =0V,I _D =250
$\Delta BV/\Delta T_J$	Breakdown Voltage Temp. Coeff.		0.11		V/°C	I _D =250μA See Fig 7
V _{GS(th)}	Gate Threshold Voltage	2.0		4.0	V	V _{DS} =5V,I _D =250μA
I _{GSS}	Gate-Source Leakage, Forward			100	nA	V _{GS} =20V
GSS	Gate-Source Leakage, Reverse			-100		V _{GS} =-20V
	Droin to Source Laskage Current			10		V _{DS} =100V
I _{DSS}	Drain-to-Source Leakage Current			100	μA	V _{DS} =80V,T _C =150°C
R _{DS(on)}	Static Drain-Source On-State Resistance			0.04	Ω	V _{GS} =10V,I _D =21.5A ④
9 _{fs}	Forward Transconductance		28.34		Ω	V _{DS} =40V,I _D =21.5A ④
C _{iss}	Input Capacitance		1750	2270		
C _{oss}	Output Capacitance		420	485	pF	$V_{GS}=0V, V_{DS}=25V, f=1MHz$
C _{rss}	Reverse Transfer Capacitance		185	215		See Fig 5
t _{d(on)}	Turn-On Delay Time		17	50		V _{DD} =50V,I _D =40A,
t _r	Rise Time		20	50	20	$R_{G} = 6.2\Omega$
t _{d(off)}	Turn-Off Delay Time		80	160	ns	See Fig 13 46
t _f	Fall Time		45	100		
Q _g	Total Gate Charge		75	97		V _{DS} =80V,V _{GS} =10V,
Q _{gs}	Gate-Source Charge		13.2		nC	I _D =40A
Q_{gd}	Gate-Drain("Miller") Charge		34.8			See Fig 6 & Fig 12 46

Electrical Characteristics (T_C=25 $^{\circ}$ C unless otherwise specified)

Source-Drain Diode Ratings and Characteristics

Symbol	Characteristic	Min.	Тур.	Max.	Units	Test Condition
ا _s	Continuous Source Current			43	^	Integral reverse pn-diode
I _{SM}	Pulsed-Source Current ()			170	А	in the MOSFET
V _{SD}	Diode Forward Voltage			1.6	V	T _J =25°C,I _S =43A,V _{GS} =0V
t _{rr}	Reverse Recovery Time		135		ns	T _J =25°C,I _F =40A
Q _{rr}	Reverse Recovery Charge		0.65		μC	di _F /dt=100A/µs ④

Notes;

- O Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature
- 2 L=0.6mH, I_{AS}=43A, V_{DD}=25V, R_G=27 Ω , Starting T_J=25°C

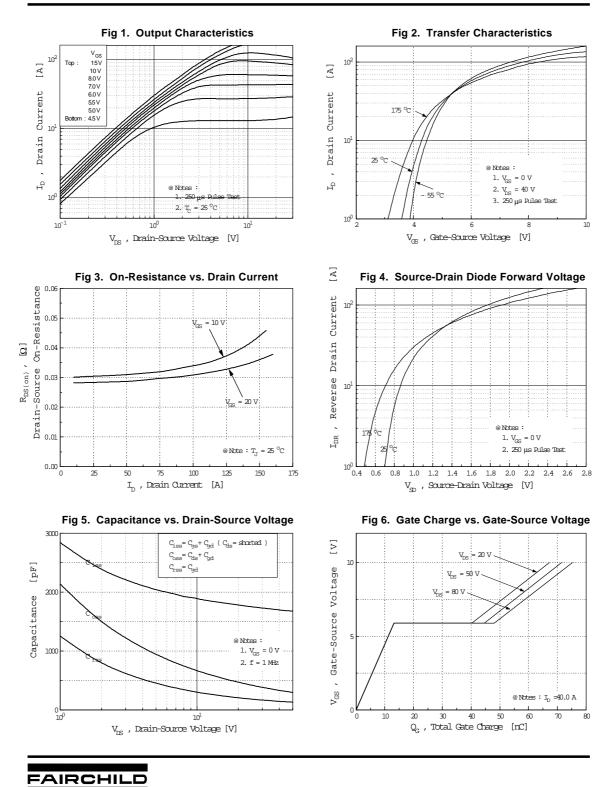
- 5 Essentially Independent of Operating Temperature

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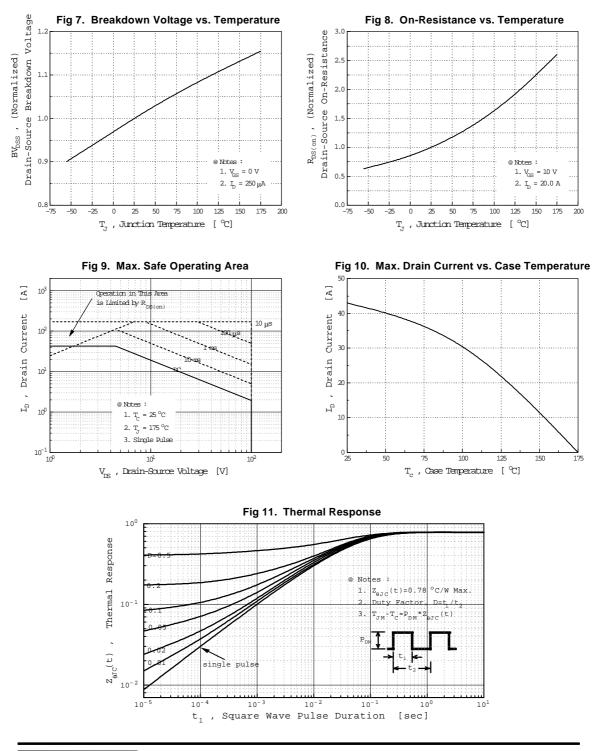
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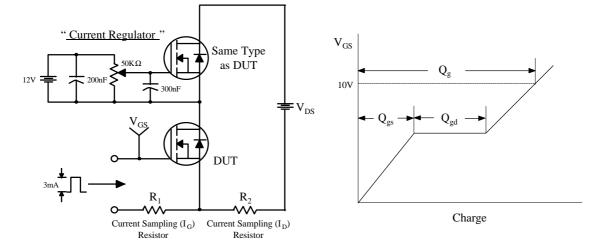
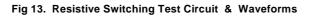


Fig 12. Gate Charge Test Circuit & Waveform



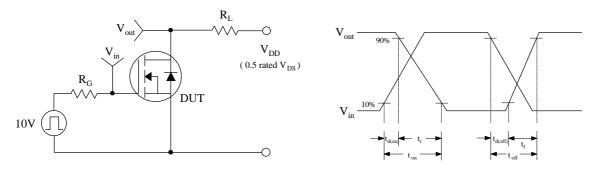
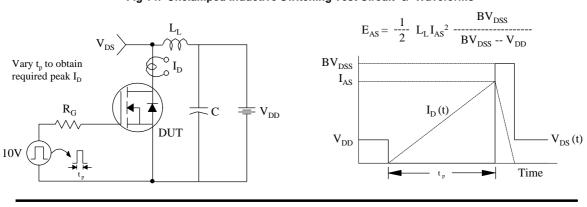


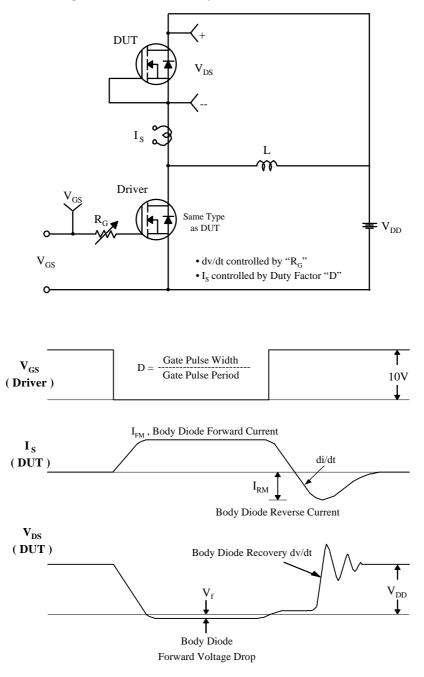
Fig 14. Unclamped Inductive Switching Test Circuit & Waveforms

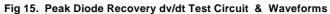




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