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FSUSB31 Low-Power, Single-Port, High-Speed USB 2.0 (480Mbps) Switch

Features

- Low On Capacitance: 3.7pF (Typical)
- Low On Resistance: 6.5Ω (Typical)
- Low Power Consumption: 1µA (Maximum)
 - 10µA Maximum I_{CCT} Over an Expanded Control Voltage Range: V_{IN}=2.6V, V_{CC}=4.3V
- Wide -3dB Bandwidth: > 720MHz
- 8kV I/O to GND ESD Protection
- Power-off Protection When V_{CC} = 0V, D+/D- Pins Can Tolerate up to 5.5V
- Packaged in:
 - 8-lead MicroPak[™] (1.6 x 1.6mm)
 - 8-lead US8
 - 8-lead Ultrathin MLP (1.2 x 1.4mm)

Applications

- Cell Phone, PDA, Digital Camera, and Notebook
- LCD Monitor, TV, and Set-top Box

Related Resources

AN-6022 Using the FSUSB30/31 to Comply with USB 2.0 Fault Condition Requirements

Description

The FSUSB31 is a low-power, single-port, high-speed USB 2.0 switch. This part is configured as a double-pole, single-throw switch and is optimized for switching or isolating a high-speed (480Mbps) source or a high-speed and full-speed (12Mbps) source. The FSUSB31 is compatible with the requirements of USB2.0 and features an extremely low on capacitance (C_{ON}) of 3.7pF. The wide bandwidth of this device (>720MHz) exceeds the bandwidth needed to pass the third harmonic, resulting in signals with minimum edge and phase distortion. Superior channel-to-channel crosstalk minimizes interference.

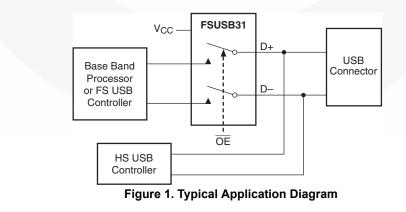
The FSUSB31 contains special circuitry on the D+/Dpins that allows the device to withstand an over-voltage condition. This device is also designed to minimize current consumption even when the control voltage applied to the \overrightarrow{OE} pin is lower than the supply voltage (V_{CC}). This feature is especially valuable for mobile applications, such as cell phones, allowing direct interface with the general-purpose I/Os of the baseband processor. Other applications include port isolation and switching in portable cell phones, PDAs, digital cameras, printers, and notebook computers.

Ordering I	nformation
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Part Number	Package	Eco Status	Package Description
FSUSB31K8X	MAB08A	Green	8-Lead US8, JEDEC MO-187, Variation CA 3.1mm Wide
FSUSB31L8X	MAC08A	RoHS	8-Lead MicroPak, 1.6mm Wide
FSUSB31UMX	UMLP08A	Green	8-Lead, Ultrathin Molded Leadless Package (UMLP), 1.2 x 1.4mm

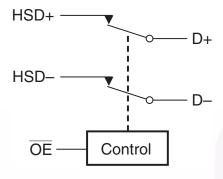
W For Fairchild's definition of "green" Eco Status, please visit: <u>http://www.fairchildsemi.com/company/green/rohs_green.html</u>.

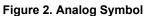
Application Diagram



MicroPak™ is a trademark of Fairchild Semiconductor Corporation.

Analog Symbol





Connection Diagrams

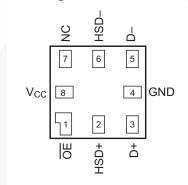


Figure 3. Pin Assignments for MicroPak

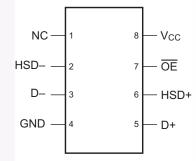
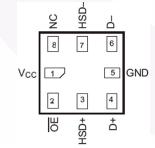


Figure 4. Pin Assignments for US8





Pin Descriptions

Pin Name	Description
OE	Bus Switch Enable
D+, D–, HSD+, HSD–	Data Ports
NC	No Connect

Truth Table

OE	Function
HIGH	Disconnect
LOW	D+, D- = HSD+, HSD-

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Para	meter	Minimum	Maximum	Unit
V _{CC}	Supply Voltage		-0.5	5.5	V
V _S	DC Input Voltage ⁽¹⁾		-0.5	V _{CC}	V
V	DC Switch Voltage ⁽¹⁾	HSD	-0.5	V _{CC}	V
V _{IN}	DC Switch Voltage	D+, D-	-0.5	V _{CC}	V
I _{IK}	DC Input Diode Current		-50		mA
I _{OUT}	DC Output Current			50	mA
T _{STG}	Storage Temperature		-65	+150	°C
ESD	Human Body Model:	All Pins		7.5	kV
LOD	JESD22-A114	I/O to GND		8	kV

Note:

1. The input and output negative voltage ratings may be exceeded if the input and output diode current ratings are observed.

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to absolute maximum ratings.

Symbol	Parameter	Minimum	Maximum	Unit
V _{CC}	Supply Voltage	3.0	4.3	V
V _{IN}	Control Input Voltage ⁽²⁾	0	V _{CC}	V
	Switch Input Voltage	0	V _{CC}	V
T _A	Operating Temperature	-40	+85	°C

Note:

2. Control input must be held HIGH or LOW and it must not float.

DC Electrical Characteristics

All typical values are at 25°C unless otherwise specified.

Symbol	Boromotor	Parameter Conditions V _{CC}	V _{CC} (V)	T _A = -40°C to +85°C			Unit
Symbol	Parameter	Conditions	VCC(V)	Min.	Тур.	Max.	Unit
V _{IK}	Clamp Diode Voltage	I _{IN} = -18mA	3.0			-1.2	V
V			3.0 to 3.6	1.3			V
V _{IH}	Input Voltage HIGH		4.3	1.7			
V			3.0 to 3.6			0.5	V
VIL	Input Voltage LOW		4.3			0.7	
I _{IN}	Control Input Leakage	$V_{IN} = 0V$ to V_{CC}	0 to V _{CC}	-1.0		1.0	μA
I _{OZ}	OFF State Leakage	$0 \leq HSD \leq V_{CC}$	4.3	-2.0		2.0	μA
I _{OFF}	Power OFF Leakage Current (D+, D–)	$V_{IN} = 0.0V$ to 4.3V, $V_{CC} = 0V$	0	-2.0		2.0	μA
R _{ON}	Switch On Resistance ⁽³⁾	V _{IN} = 0.4V, I _{ON} = -8mA	3.0		6.5	10.0	Ω
ΔR_{ON}	Delta R _{ON} ⁽⁴⁾	V _{IN} = 0.4V, I _{ON} = -8mA	3.0		0.35		Ω
R _{ON} Flatness	R _{ON} Flatness ⁽³⁾	V _{IN} = 0.0V –1.0V, I _{ON} = –8mA	3.0		2.0		Ω
Icc	Quiescent Supply Current	$V_{IN} = 0.0V \text{ or } V_{CC},$ $I_{OUT} = 0$	4.3			1.0	μA
I _{CCT}	Increase in I_{CC} Current per Control Voltage and V_{CC} Levels	V _{IN} = 2.6V, V _{CC} = 4.3V	4.3			10.0	μA

Notes:

3. Measured by the voltage drop between Dn, HSD, and Dn pins at the indicated current through the switch.

On resistance is determined by the lower of the voltage on the two ports.

4. Guaranteed by characterization.

AC Electrical Characteristics

All typical values are for V_{CC} = 3.3V are at 25°C unless otherwise specified.

Symbol	Parameter Conditions V _{CC} (V)		$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$			Unit	Figure	
Symbol	Falameter	conditions	• 66 (•)	Min.	Тур.	Max.	onit	Number
t _{ON}	Turn-On Time, OE to Output	V_{IN} = 0.8V, R_L = 50 Ω , C_L = 5pF	3.0 to 3.6		15.0	30.0	ns	Figure 13
t _{OFF}	Turn-Off Time, OE to Output	V_{IN} = 0.8V, R_L = 50 Ω , C_L = 5pF	3.0 to 3.6		12.0	25.0	ns	Figure 13
t _{PD}	Propagation Delay ⁽⁵⁾	R _L = 50Ω, C _L = 5pF	3.3		0.25		ns	Figure 11 Figure 12
t _{BBM}	Break-Before-Make	$ \begin{array}{l} R_L = 50\Omega, C_L = 5pF, \\ V_IN = 0.8V \end{array} $	3.0 to 3.6	2.0		6.5	ns	Figure 14
O _{IRR}	Off Isolation (Non-Adjacent)	R _T = 50Ω, f = 240MHz	3.0 to 3.6		-35.0		dB	Figure 17
Xtalk	Non-Adjacent Channel Crosstalk	R _T = 50Ω, f = 240MHz	3.0 to 3.6		-55.0		dB	Figure 18
BW	-3dB Bandwidth	$R_T = 50\Omega, C_L = 0pF$	3.0 to 3.6		720		MHz	Figure 16
		R_T = 50 Ω , C_L = 5pF	0.0 10 0.0		550		101112	i igule to

Note:

5. Guaranteed by characterization.

USB Hi-	Speed Related Parameter	AC Electrical Charac	cteristics V _{cc} (V)		= –40°0 +85°C		Unit	Figure Number
				Min.	Тур.	Max.		Number
t _{SK(O)}	Channel-to-Chan- nel Skew ⁽⁶⁾	C _L = 5pF	3.0 to 3.6		50.0		ps	Figure 11 Figure 15
t _{SK(P)}	Skew of Opposite Transitions of the Same Output ⁽⁶⁾	C _L = 5pF	3.0 to 3.6		20.0		ps	Figure 11 Figure 15
tJ	Total Jitter ⁽⁶⁾	$R_L = 50\Omega$, $C_L = 5pF$, $t_R = t_F = 500ps at 480 Mbps$ (PRBS = $2^{15} - 1$)	3.0 to 3.6		200		ps	

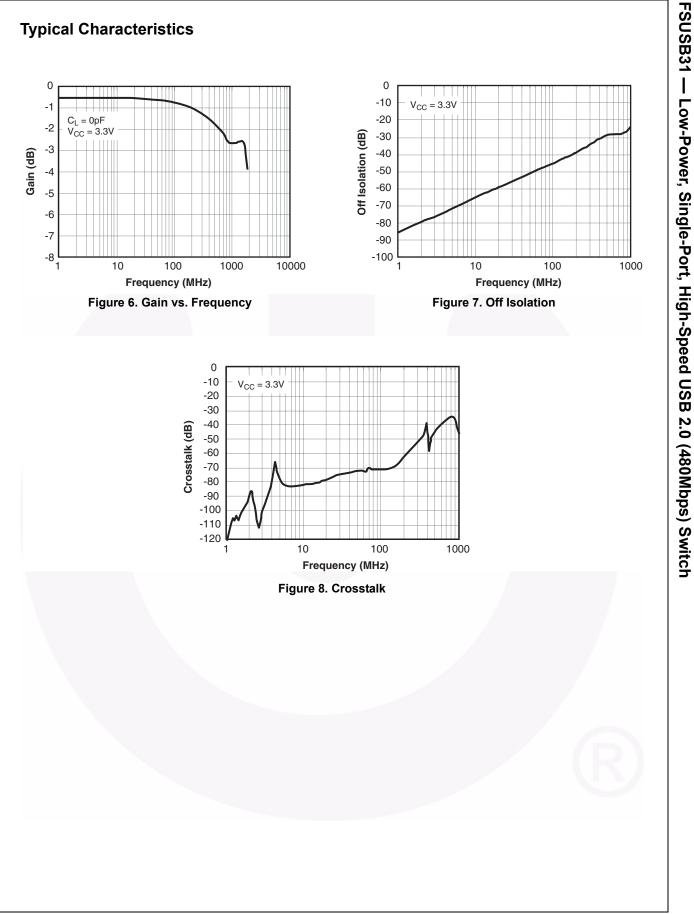
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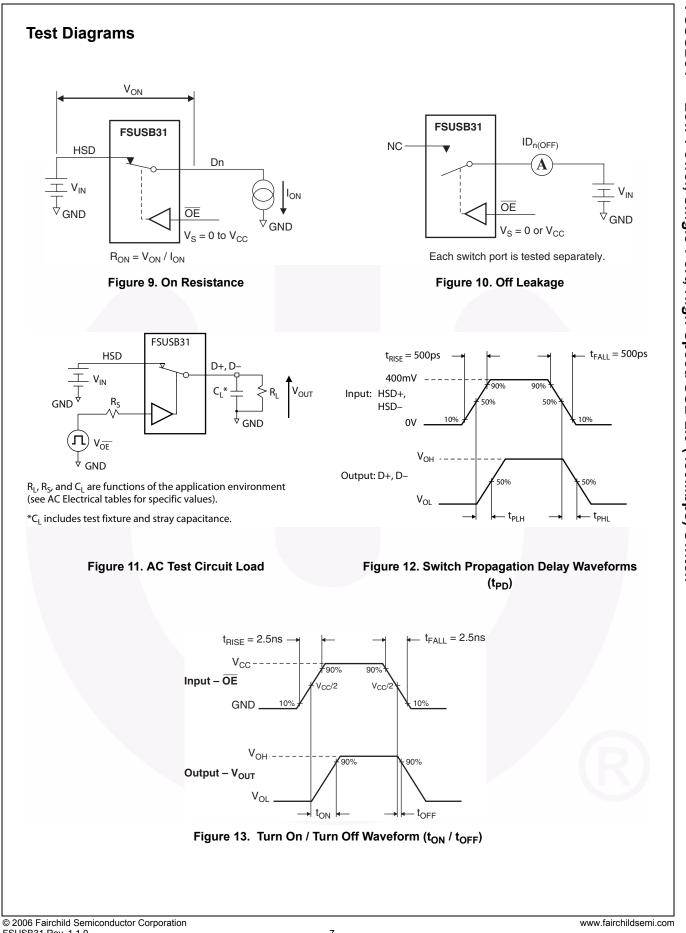
6. Guaranteed by design.

Capacitance

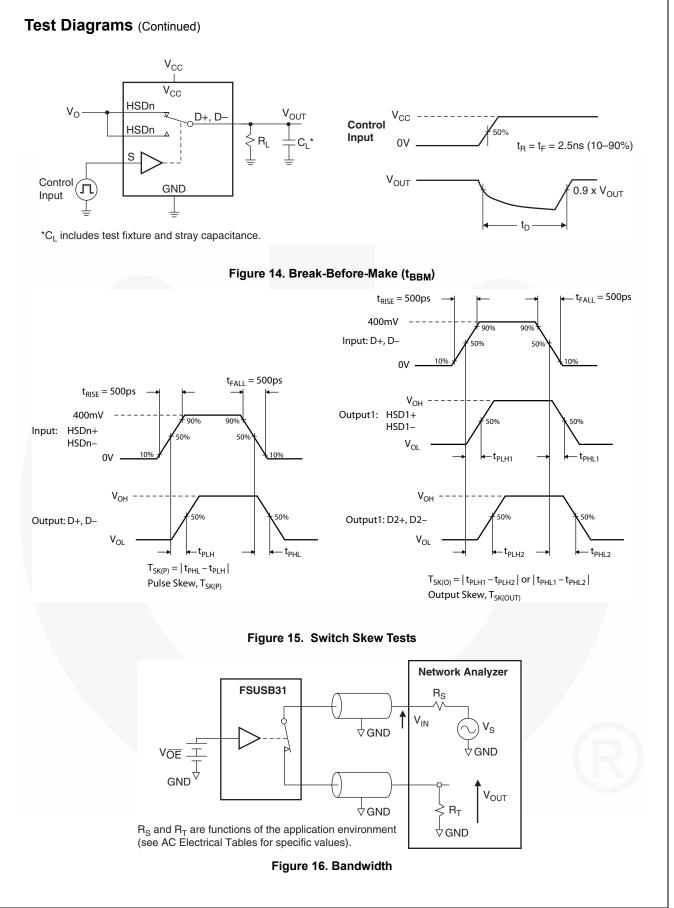
Symbol	Parameter Conditions		T _A = -	40°C to	Unit	Figure	
Symbol	Tarameter	Conditions	Min.	Тур.	Max.	onit	Number
C _{IN}	Control Pin Input Capacitance	V _{CC} = 0V		1.0		pF	Figure 20
C _{ON}	On Capacitance	$V_{CC} = 3.3V, \overline{OE} = 0V$		3.7		pF	Figure 19
C _{OFF}	Off Capacitance	V_{CC} and \overline{OE} = 3.3V		1.7		pF	Figure 20

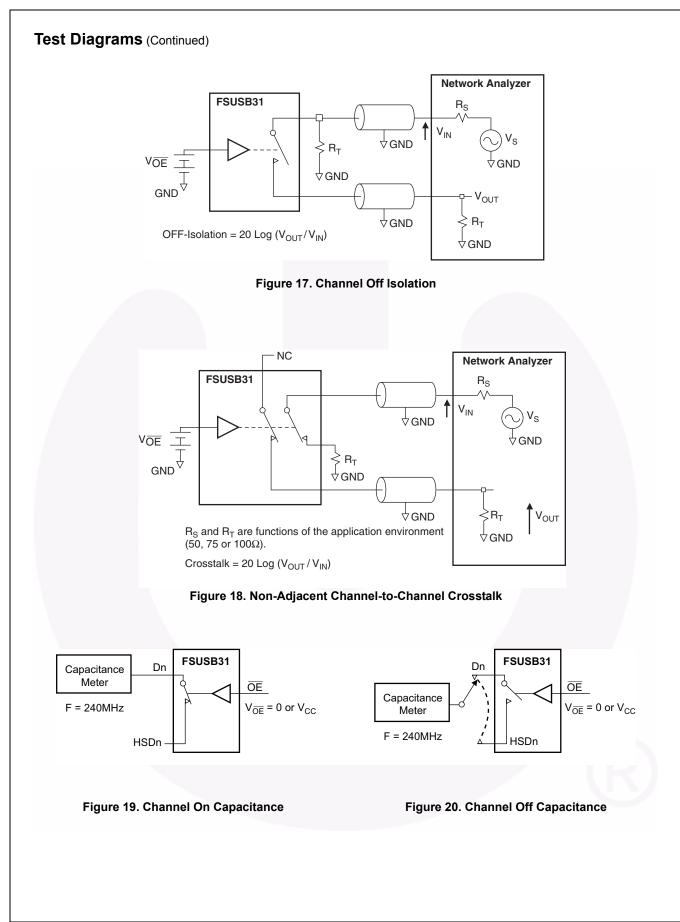
FSUSB31 — Low-Power, Single-Port, High-Speed USB 2.0 (480Mbps) Switch





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FSUSB31 — Low-Power, Single-Port, High-Speed USB 2.0 (480Mbps) Switch

Application Guidance: Meeting USB 2.0 Vbus Short Requirements

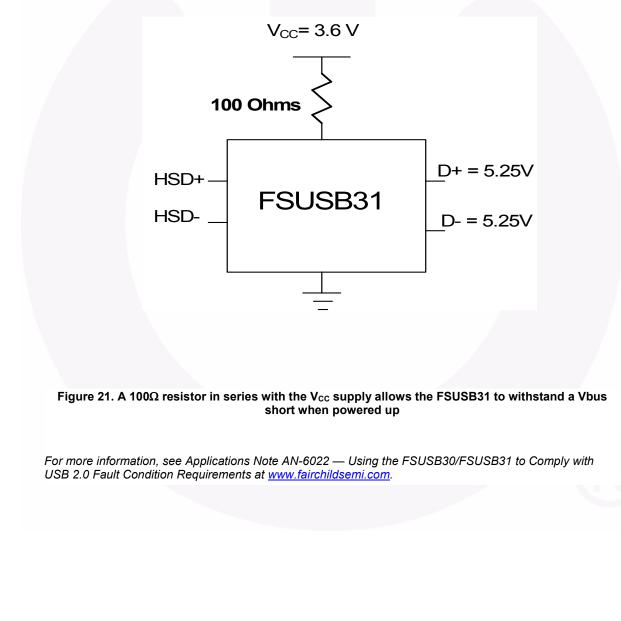
In section 7.1.1 of the USB 2.0 specification, it notes that USB devices must be able to withstand a Vbus short to D+ or D- when the USB devices is either powered off or powered on. The FSUSB31 can be successfully configured to meet both these requirements.

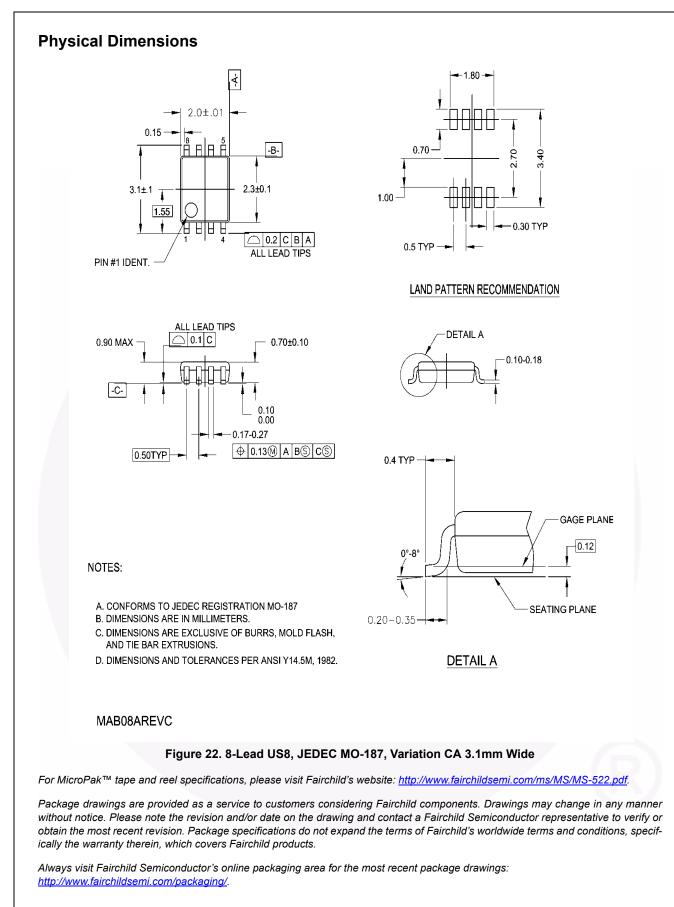
Power-Off Protection

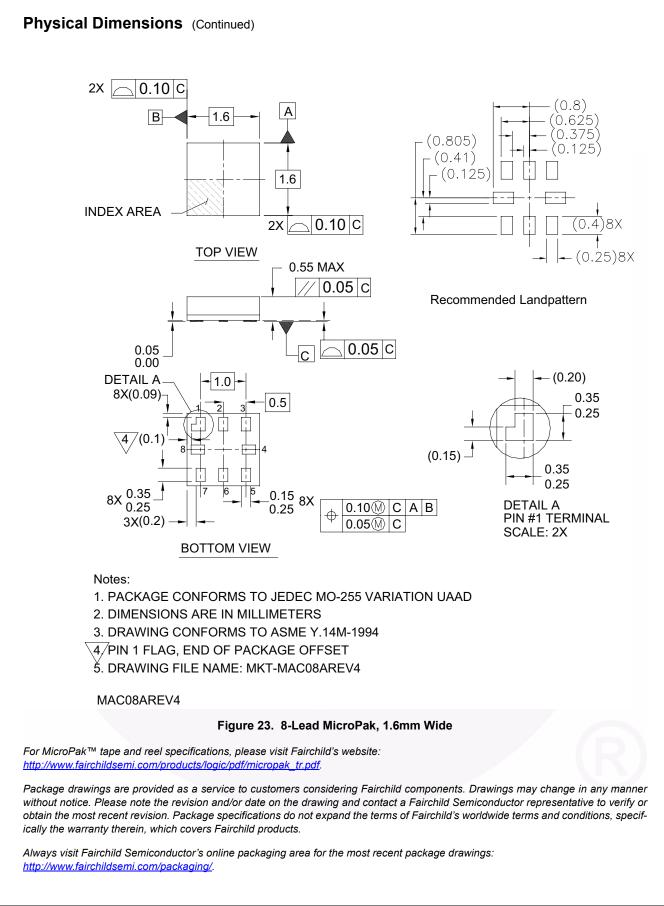
For a Vbus short circuit, the switch is expected to withstand such a condition for at least 24 hours. The FSUSB31 has specially designed circuitry which prevents unintended signal bleed through as well as guaranteed system reliability during a power-down, overvoltage condition. The protection has been added to the common pins (D+, D-).

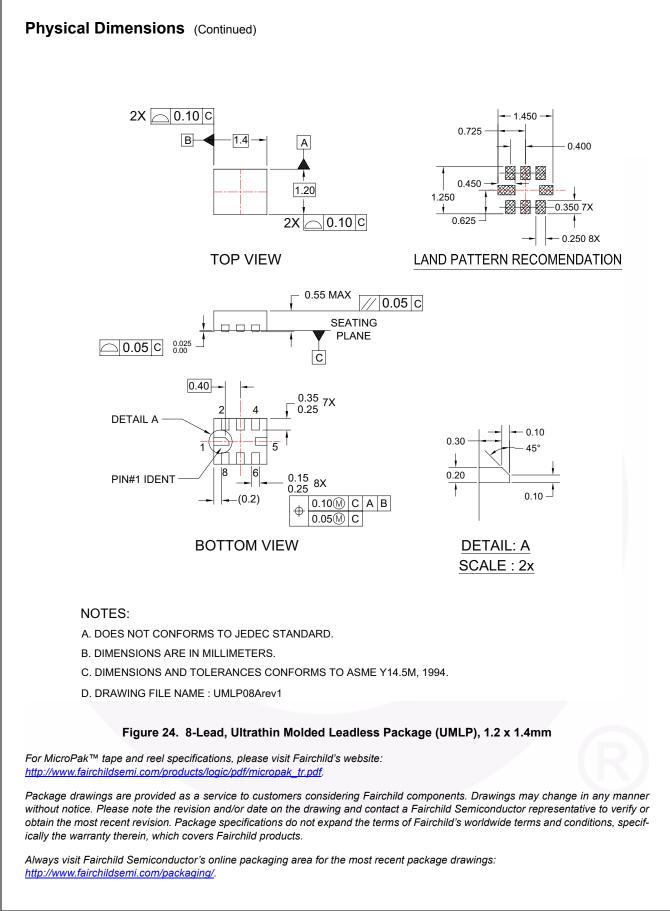
Power-On Protection

The USB 2.0 specification also notes that the USB device should be capable of withstanding a Vbus short during transmission of data. Fairchild recommends adding a 100Ω series resister between the switch VCC pin and supply rail to protect against this case. This modification works by limiting current flow back into the VCC rail during the over-voltage event so current remains within the safe operating range. In this application, the switch passes the full 5.25V input signal through to the selected output, while maintaining specified off isolation on the un-selected pins.

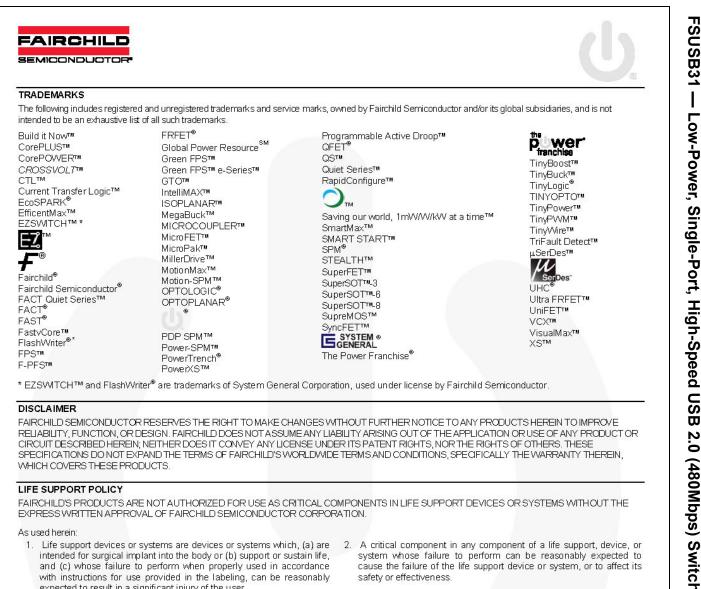








FSUSB31 — Low-Power, Single-Port, High-Speed USB 2.0 (480Mbps) Switch



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