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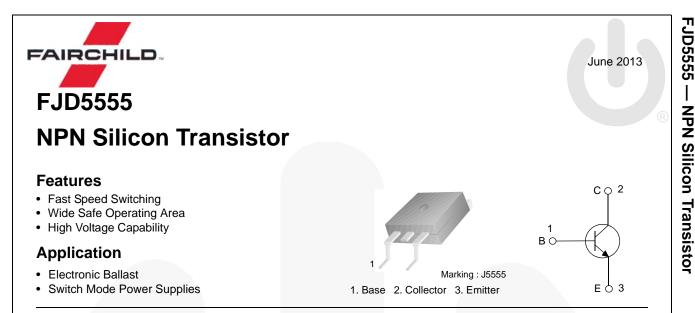


ON Semiconductor®

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Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (_), the underscore (_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.onsemi.com. Please email any questions regarding the system integration to Fairchild_questions@onsemi.com.

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Ordering Information

Part Number	Marking	Package	Packing Method
FJD5555TM	J5555	D-PAK	Tape & Reel

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Units
BV _{CBO}	Collector-Base Voltage	1050	V
BV _{CEO}	Collector-Emitter Voltage	400	V
BV _{EBO}	Emitter-Base Voltage	14	V
۱ _C	Collector Current (DC)	5	A
I _{CP}	Collector Current (Pulse)	10	A
I _B	Base Current (DC)	2	Α
I _{BP}	Base Current (Pulse)	4	A
TJ	Junction Temperature	150	°C
T _{STG}	Storage Junction Temperature Range	- 55 to +150	°C

Thermal Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter		Value	Units
P _D	Total Device Dissipation	T _A = 25°C	1.34	W
		$T_{C} = 25^{\circ}C$	100	W
$R_{ heta ja}{}^{(1)}$	Thermal Resistance, Junction to Ambient		95	°C/W
$R_{\theta jc}^{(2)}$	Thermal Resistance, Junction to Case		1.25	°C/W

Notes:

1. $R_{\theta ia}$ test board and fixture under natural convection; JESD51-3 recommended thermal test board.

2. $R_{\theta jc}$ test fixture under infinite cooling condition.

FJD5555 — NPN Silicon Transistor

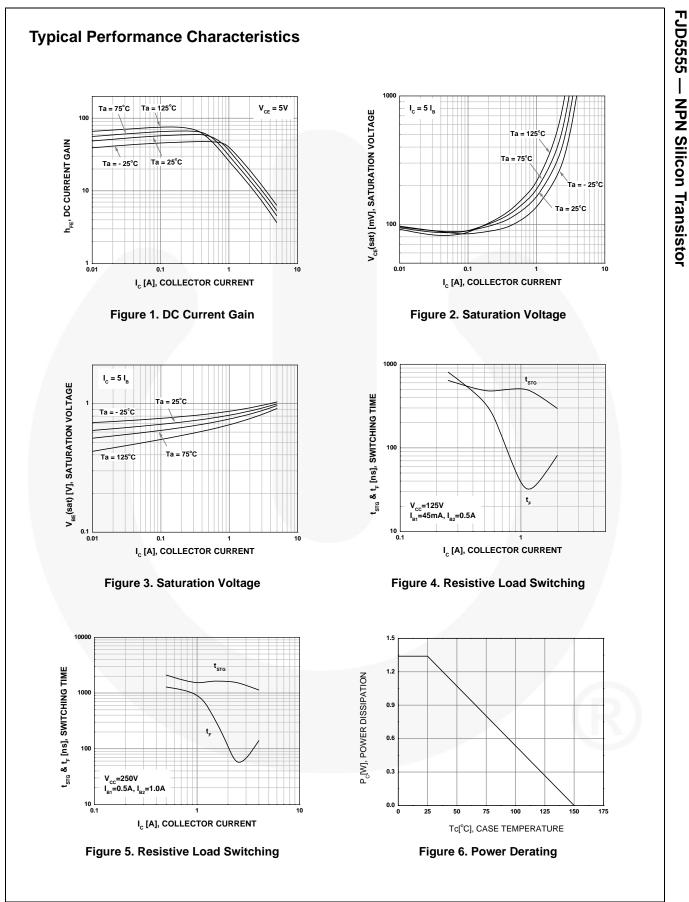
Electrical Characteristics⁽³⁾

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

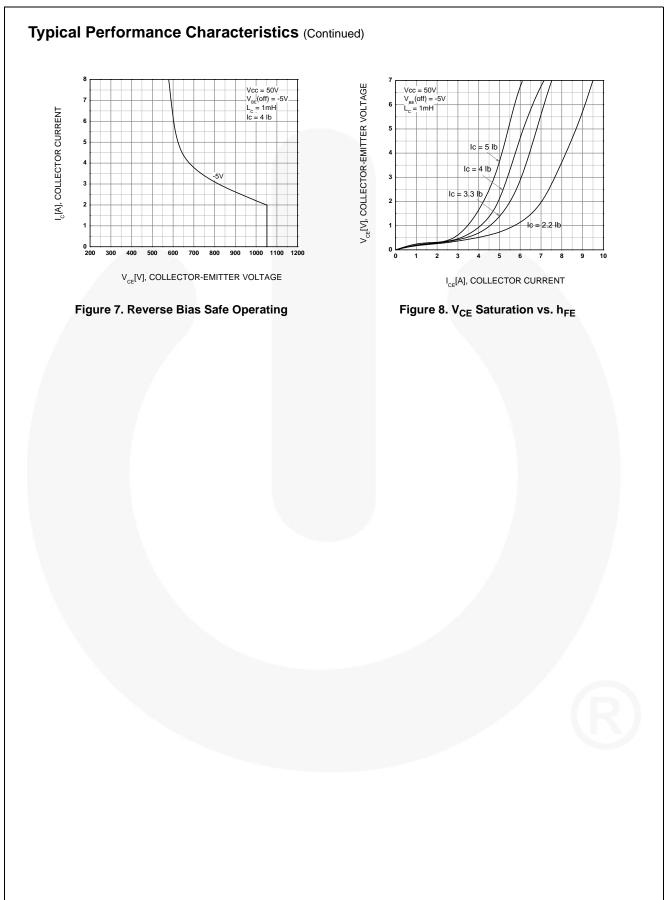
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	I _C = 500 μA, I _E = 0	1050			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_{\rm C} = 5 \text{ mA}, I_{\rm B} = 0$	400			V
BV_{EBO}	Emitter-Base Breakdown Voltage	$I_{\rm E} = 500 \ \mu {\rm A}, \ I_{\rm C} = 0$	14			V
h _{FE}	DC Current Gain	$V_{CE} = 5 \text{ V}, I_{C} = 10 \text{ mA}$	10			
		$V_{CE} = 3 \text{ V}, \text{ I}_{C} = 0.8 \text{ A}$	20		40	
V (aat)	Collector Emitter Seturation Voltage	I _C = 1 A, I _B = 0.2 A		0.17	0.50	V
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = 3.5 A, I _B = 1.0 A		1.5	V	
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = 3.5 A, I _B = 1.0 A			1.2	V
C _{ob}	Output Capacitance	V _{CB} = 10 V, f = 1 MHz		45		pF
t _{ON}	Turn-On Time	$V_{CC} = 125 \text{ V}, I_{C} = 0.5 \text{ A},$			1.0	μs
t _{STG}	Storage Time	$I_{B1} = 45 \text{ mA}, I_{B2} = -0.5 \text{ A}, R_{L} = 250 \Omega$			1.2	μs
t _F	Fall Time			0.3		μs
t _{ON}	Turn-On Time	V _{CC} = 250 V, I _C = 2.5 A,			2.0	μs
t _{STG}	Storage Time	$I_{B1} = 0.5 \text{ A}, I_{B2} = -1.0 \text{ A}, R_{L} = 100 \Omega$			2.5	μs
t _F	Fall Time				0.3	μs
EAS	Avalanche Energy	L = 2 mH	6			mJ

Note:

3. Pulse test: pulse width \leq 300 $\mu s,$ duty cycle \leq 2%.



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