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## FGA30N120FTD 1200 V, 30 A Field Stop Trench IGBT

## **Features**

- Field Stop Trench Technology
- High Speed Switching
- Low Saturation Voltage: V<sub>CE(sat)</sub> = 1.6 V @ I<sub>C</sub> = 30 A
- · High Input Impedance

### April 2013

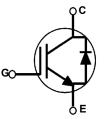
## **General Description**

Using advanced field stop trench technology, Fairchild®'s 1200V trench IGBTs offer superior conduction and switching performances for soft switching applications. The device can operate in parallel configuration with exceptional avalanche ruggedness. This device is designed for induction heating and microwave oven.

## **Applications**

· Solar Inverter, UPS, Welder, PFC





## **Absolute Maximum Ratings**

Symbol	Description		Ratings	Unit
V <sub>CES</sub>	Collector to Emitter Voltage		1200	V
V <sub>GES</sub>	Gate to Emitter Voltage		± 25	V
I <sub>C</sub>	Collector Current	@ T <sub>C</sub> = 25°C	60	А
·C	Collector Current	@ T <sub>C</sub> = 100°C	30	А
I <sub>CM (1)</sub>	Pulsed Collector Current	@ T <sub>C</sub> = 25°C	90	A
I <sub>F</sub>	Diode Continuous Forward Current	@ T <sub>C</sub> = 100 <sup>o</sup> C	30	А
P <sub>D</sub>	Maximum Power Dissipation	@ T <sub>C</sub> = 25°C	339	W
	Maximum Power Dissipation	@ T <sub>C</sub> = 100°C	132	W
TJ	Operating Junction Temperature		-55 to +150	°C
T <sub>stg</sub>	Storage Temperature Range		-55 to +150	°C
TL	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds	5	300	°C

Notes: 1: Repetitive rating: Pulse width limited by max. junction temperature

## **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JC}(IGBT)$	Thermal Resistance, Junction to Case	-	0.38	°C/W
$R_{\theta JC}(Diode)$	Thermal Resistance, Junction to Case	-	1.2	°C/W

$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to An		mbient		-	40		°C/W	
Packag tion	e Marking	and Ordering I	nforma- 🧭						
🥖 Dev	rice Marking	Marking Device		Eco S	Status	Packaging Type Tube		Qty per Tu 30ea	
FG	FGA30N120FTD FGA30N120FTDTU		TO-3PN	RoHS	5				
Electric	al Charact	reen" Eco Status, pleas eristics of the l	<b>GBT</b> $T_{C} = 25^{\circ}C$ unles	ss otherwise noted	1		1		
Symbol	Pa	arameter	Test Cond	itions Mir		Тур.	Max	. Unit	
Off Chara	cteristics								
BV <sub>CES</sub>		tter Breakdown Voltage	V <sub>GE</sub> = 0V, I <sub>C</sub> = 250	μA	1200	-	-	V	
I <sub>CES</sub>	Collector Cut-O	-		$V_{CE} = V_{CES}, V_{GE} = 0V$		-	1	mA	
I <sub>GES</sub>	G-E Leakage C	urrent	$V_{GE} = V_{GES}, V_{CE} =$		-	-	±250	) nA	
					1	]	1		
	G-E Threshold	Voltage	$l_0 = 30 \text{ mA}        $	/05	3.5	6	7.5	V	
V <sub>GE(th)</sub>		volidge	$I_{\rm C} = 30 \text{A}, V_{\rm CE} = 15$	$I_{C} = 30$ mA, $V_{CE} = V_{GE}$		1.6	2	V	
V <sub>CE(sat)</sub>	CE(sat) Collector to Emitter Saturation Voltage		$I_{\rm C} = 30$ A, $V_{\rm GE} = 15$ $I_{\rm C} = 30$ A, $V_{\rm GE} = 15$			-	2		
. /			$T_{\rm C} = 125^{\circ}{\rm C}$		-	2.0	-	V	
Dynamic (	Characteristics								
C <sub>ies</sub>	Input Capacitar	ice			-	5140	-	pF	
C <sub>oes</sub>	Output Capacita	ance	V <sub>CE</sub> = 30V, V <sub>GE</sub> = 0V, f = 1MHz		-	150	-	pF	
C <sub>res</sub>	Reverse Transf	er Capacitance			-	95	-	pF	
Switching	Characteristics								
t <sub>d(on)</sub>	Turn-On Delay	Time			-	31	-	ns	
t <sub>r</sub>	Rise Time		1		-	101	-	ns	
t <sub>d(off)</sub>	Turn-Off Delay	Time	V <sub>CC</sub> = 600V, I <sub>C</sub> = 3	0A,	-	198	-	ns	
t <sub>f</sub>	Fall Time		R <sub>G</sub> = 10Ω, V <sub>GE</sub> = 1	5V,	-	259	-	ns	
Eon	Turn-On Switch	ing Loss	Resistive Load, T <sub>C</sub>	Resistive Load, T <sub>C</sub> = 25 <sup>o</sup> C		0.54	-	mJ	
E <sub>off</sub>	Turn-Off Switch	ing Loss				1.16	1.51	mJ	
E <sub>ts</sub>	Total Switching	Loss			-	1.70	-	mJ	
t <sub>d(on)</sub>	Turn-On Delay	Time				40	-	ns	
t <sub>r</sub>	Rise Time				-	127	-	ns	
t <sub>d(off)</sub>	Turn-Off Delay	Time	V <sub>CC</sub> = 600V, I <sub>C</sub> = 3	0A,	-	211	-	ns	
t <sub>f</sub>	Fall Time		$R_G = 10\Omega$ , V <sub>GE</sub> = 1 Resistive Load, T <sub>C</sub>	5V, = 125°C	-	364	-	ns	
E <sub>on</sub>	Turn-On Switch	ing Loss		- 12510	-	0.74	-	mJ	
E <sub>off</sub>	Turn-Off Switch	ing Loss			-	1.63	-	mJ	
E <sub>ts</sub>	Total Switching	Loss			-	2.37	-	mJ	
Qg	Total Gate Char	ge			-	208	-	nC	
Q <sub>ge</sub>	Gate to Emitter	Charge	V <sub>CE</sub> = 600V, I <sub>C</sub> = 3 V <sub>GE</sub> = 15V	0A,	-	41	-	nC	
			V <sub>GE</sub> = 15V						

Symbol	Parameter	Test Conditions		Min.	Тур.	Мах	Unit
Ven	V <sub>FM</sub> Diode Forward Voltage	I <sub>F</sub> = 30A	T <sub>C</sub> = 25°C	-	1.3	1.7	V
* FIM			T <sub>C</sub> = 125 <sup>o</sup> C	-	1.3	-	
t <sub>rr</sub>	Diode Reverse Recovery Time	- I <sub>F</sub> =30A, di/dt = 200A/μs	T <sub>C</sub> = 25 <sup>o</sup> C	-	730	-	ns A
·rr			T <sub>C</sub> = 125°C	-	775	-	
Irr	Diode Peak Reverse Recovery Current		T <sub>C</sub> = 25 <sup>o</sup> C	-	43	-	
			T <sub>C</sub> = 125 <sup>o</sup> C	-	47	-	
Q <sub>rr</sub>	Diode Reverse Recovery Charge		T <sub>C</sub> = 25°C	-	5.9	-	μC
∽II.			$T_{\rm C} = 125^{\rm o}{\rm C}$	-	18.2	-	μΟ

### **Typical Performance Characteristics** Figure 1. Typical Output Characteristics 180 180 T<sub>C</sub> = 25<sup>o</sup>C 20V 150 17V 15V Collector Current, Ic [A] Collector Current, Ic [A] 120 12V 90 60 10V 9V 30 V<sub>GE</sub> = 8V 0 2 4 6 Collector-Emitter Voltage, V<sub>CE</sub> [V] 0 8 **Figure 3. Typical Saturation Voltage** Characteristics 120 Common Emitter V<sub>GE</sub> = 15V 100 $T_{\rm C} = 25^{\rm o} {\rm C}$ — Collector Current, I<sub>c</sub> [A] Collector Current, Ic [A] T<sub>C</sub> = 125<sup>o</sup>C .... 80 60 40 20 0 0 2 4 6 Collector-Emitter Voltage, V<sub>CE</sub> [V] Figure 5. Saturation Voltage vs. Case **Temperature at Variant Current Level** 3.0 Common Emitter V<sub>GE</sub> = 15V Collector-Emitter Voltage, V<sub>CE</sub> [V] Collector-Emitter Voltage, V<sub>CE</sub> [V] 60A 2.5 2.0 30A 1.5 $I_{\rm C} = 10A$ 1.0 125 25 50 75 100 Collector-EmitterCase Temperature, T<sub>c</sub> [°C]

### Figure 2. Typical Output Characteristics

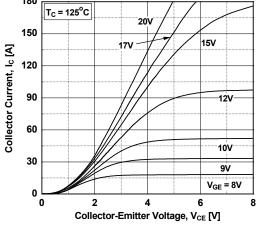
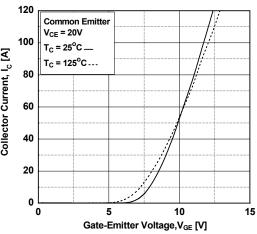
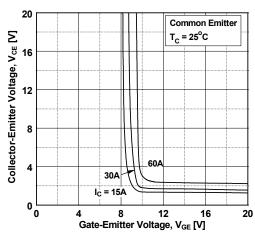


Figure 4. Transfer Characteristics







## **Typical Performance Characteristics**



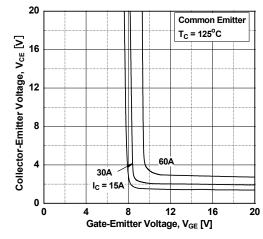
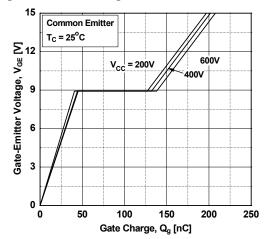


Figure 9. Gate charge Characteristics





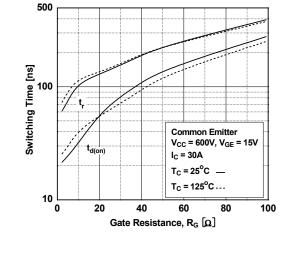


Figure 8. Capacitance Characteristics

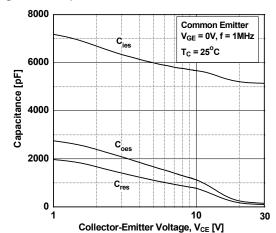


Figure 10. SOA Characteristics

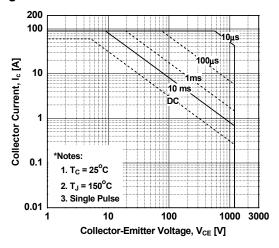
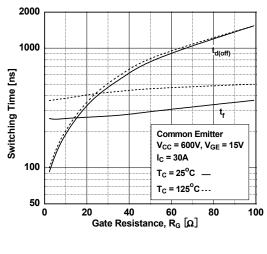
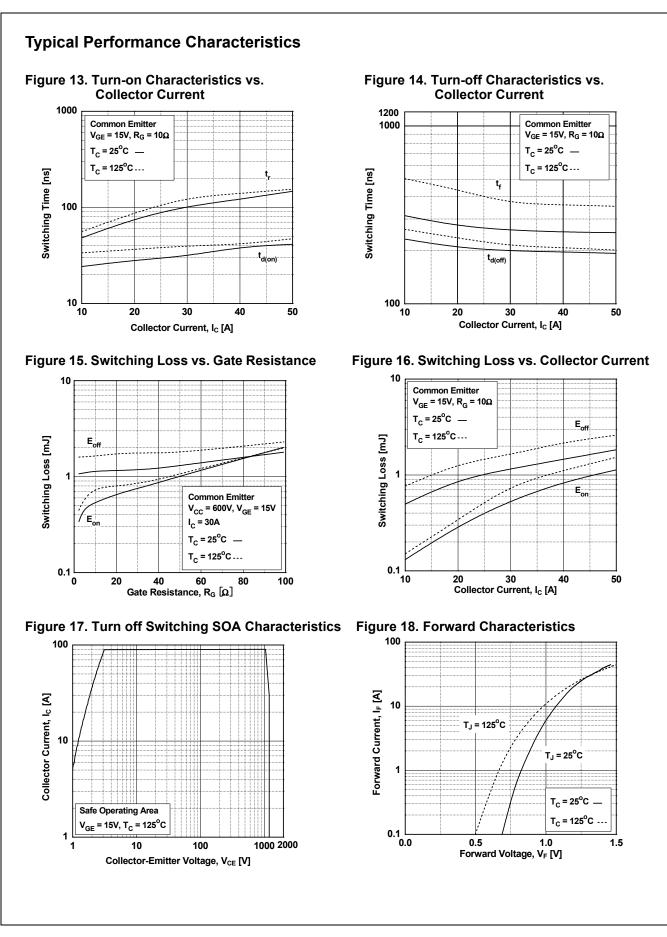


Figure 12. Turn-off Characteristics vs. Gate Resistance





# **Typical Performance Characteristics** Figure 19. Reverse Current 50 Reverse Recovery Currnet, In [A] 200A/µs 40

di/dt = 100A/µs

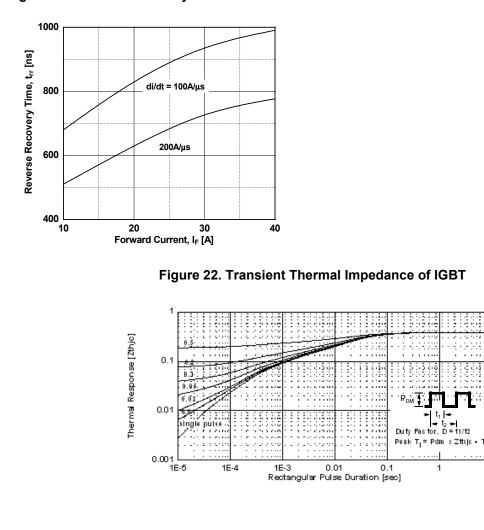
20 30 Forward Current, I<sub>F</sub> [A]



30

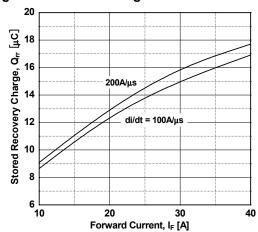
20

10

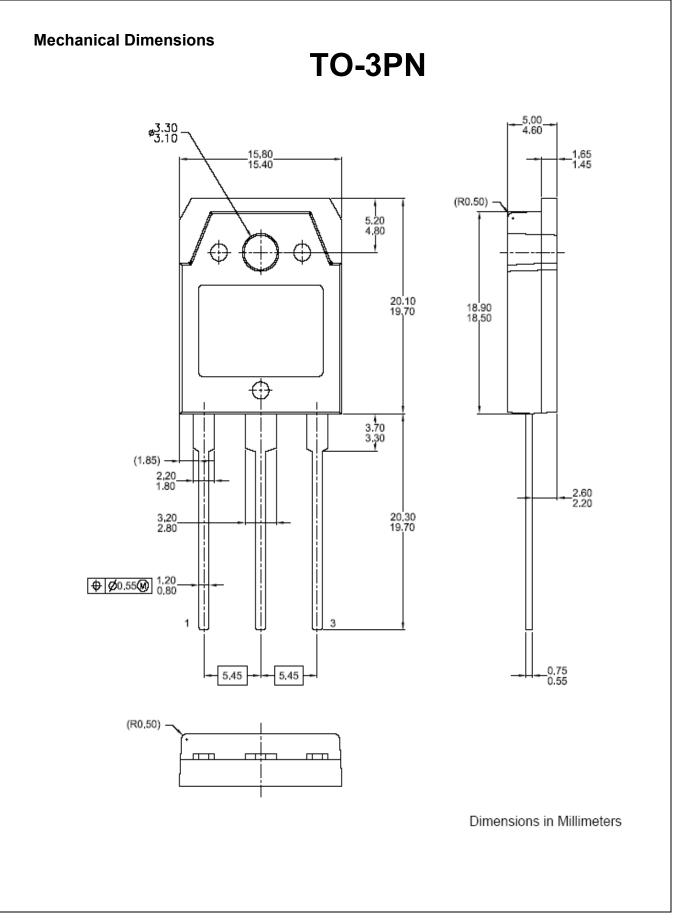


40

### Figure 20. Stored Charge



10



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