ON Semiconductor

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Silicon Carbide Schottky Diode

650 V, 6 A

FFSM0665B

Silicon Carbide (SiC) Schottky Diodes use a completely new technology that provides superior switching performance and higher reliability compared to Silicon. No reverse recovery current, temperature independent switching characteristics, and excellent thermal performance sets Silicon Carbide as the next generation of power semiconductor. System benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size and cost.

Features

- Max Junction Temperature 175°C
- Avalanche Rated 24.5 mJ
- High Surge Current Capacity
- Positive Temperature Coefficient
- Ease of Paralleling
- No Reverse Recovery / No Forward Recovery
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

- General Purpose
- SMPS, Solar Inverter, UPS
- Power Switching Circuits

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter		Symbol	Value	Unit
Peak Repetitive Reverse Voltage		V_{RRM}	650	V
Single Pulse Avalanche Energy ($T_J = 25^{\circ}C$, $I_{L(pk)} = 9.9$ A, L = 0.5 mH, V = 50 V)		E _{AS}	24.5	mJ
Continuous Rectified Forward	T _C < 154	l _F	6.0	Α
Current	T _C < 135		9.1	
Non-Repetitive Peak Forward Surge Current	T _C = 25°C, t _P = 10 μs	I _{FM}	292	Α
	$T_{C} = 150^{\circ}C,$ $t_{P} = 10 \ \mu s$		262	
Non-Repetitive Forward Surge Current (Half-Sine Pulse)	$T_C = 25$ °C $t_P = 8.3$ ms	I _{FSM}	28	Α
Power Dissipation	T _C = 25°C	P _{tot}	75	W
	T _C = 150°C		12.5	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +175	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL RESISTANCE

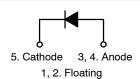
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	2.0	°C/W



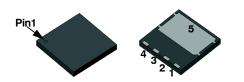
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V _{RRM}	I _F
650 V	6.0 A



Schottky Diode



PQFN 8×8, 2P CASE 483AP

MARKING DIAGRAM

\$Y FFSM 0665B &Z&K&3

\$Y = ON Semiconductor Logo FFSM0665B = Specific Device Code &Z = Assembly Plant Code &3 = Numeric Date Code &K = Lot Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
N CHARAC	TERISTICS	•		•	•	•
V _F Forward Volta	Forward Voltage	I _F = 6.0 A, T _J = 25°C		1.43	1.7	V
		I _F = 6.0 A, T _J = 125°C		1.62		1
		I _F = 6.0 A, T _J = 175°C		1.69		
I _R Reverse Current	Reverse Current	V _R = 650 V, T _J = 25°C		0.5	40	μΑ
		V _R = 650 V, T _J = 125°C		1.0	80	
		V _R = 650 V, T _J = 175°C		2.0	160	
HARGES, C	APACITANCES & GATE RES	ISTANCE				
Q_C	Total Capacitive Charge	V _C = 400 V		16		nC
C _{tot}		V _R = 1 V, f = 100 kHz		259		pF
		V _R = 200 V, f = 100 kHz		29		1
		V _R = 400 V, f = 100 kHz		22		1

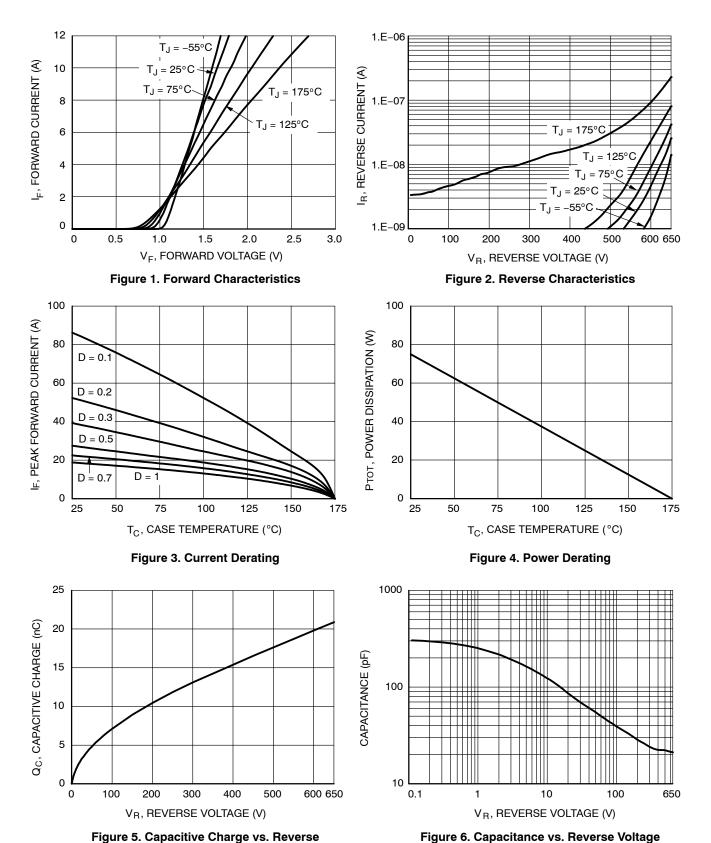
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

PART MARKING AND ORDERING INFORMATION

Part Number	Top Mark	Package	Packing Method [†]	Quantity
FFSM0665B	FFSM0665B	PQFN 8X8, 2P (Halogen Free)	Tape & Reel	3000 units

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

TYPICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)



vs. neverse rigure 6. Capacitance vs. neverse voltage

Voltage

TYPICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise noted)

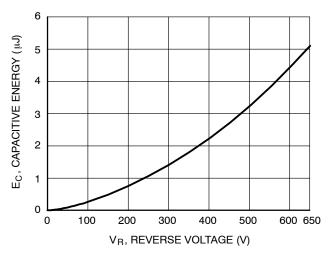


Figure 7. Capacitance Stored Energy

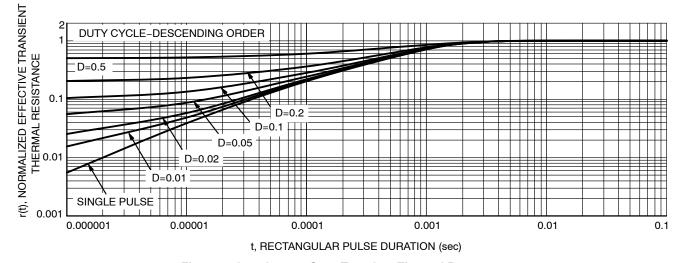
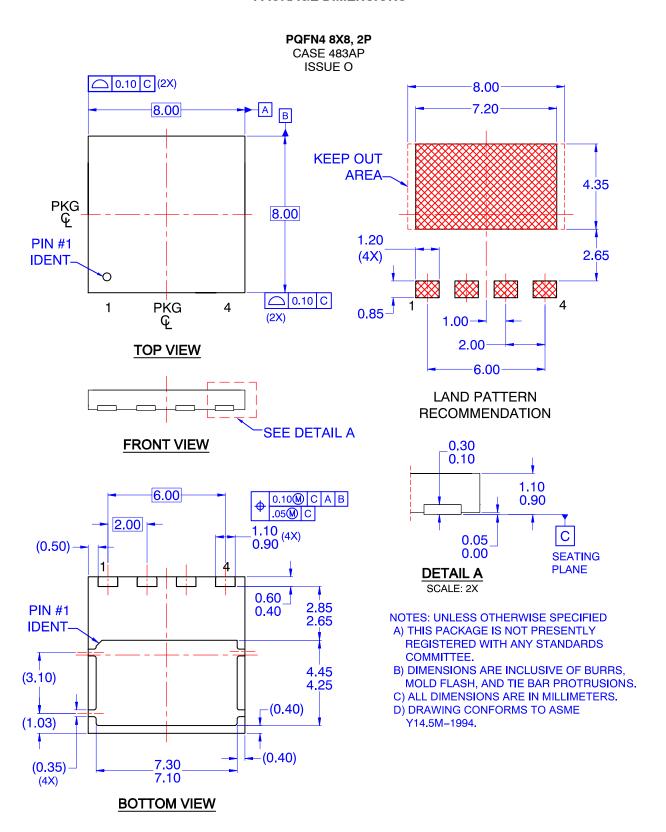


Figure 8. Junction-to-Case Transient Thermal Response

PACKAGE DIMENSIONS



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