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## FDI150N10 N-Channel PowerTrench<sup>®</sup> MOSFET 100 V, 57 A, 16 m $\Omega$

### Features

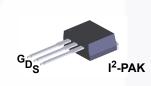
- $R_{DS(on)}$  = 12 m $\Omega$  (Typ.) @  $V_{GS}$  = 10 V,  $I_D$  = 49 A
- · Fast Switching Speed
- Low Gate Charge
- High Performance Trench Technology for Extremely Low  $R_{\text{DS}(\text{on})}$
- High Power and Current Handling Capability
- RoHS Compliant

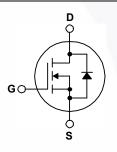
## Description

This N-Channel MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench<sup>®</sup> process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

## Applications

- Synchronous Rectification for ATX / Server / Telecom PSU
- Battery Protection Circuit
- Motor Drives and Uninterruptible Power Supplies
- Micor Solar Inverter





## MOSFET Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted.

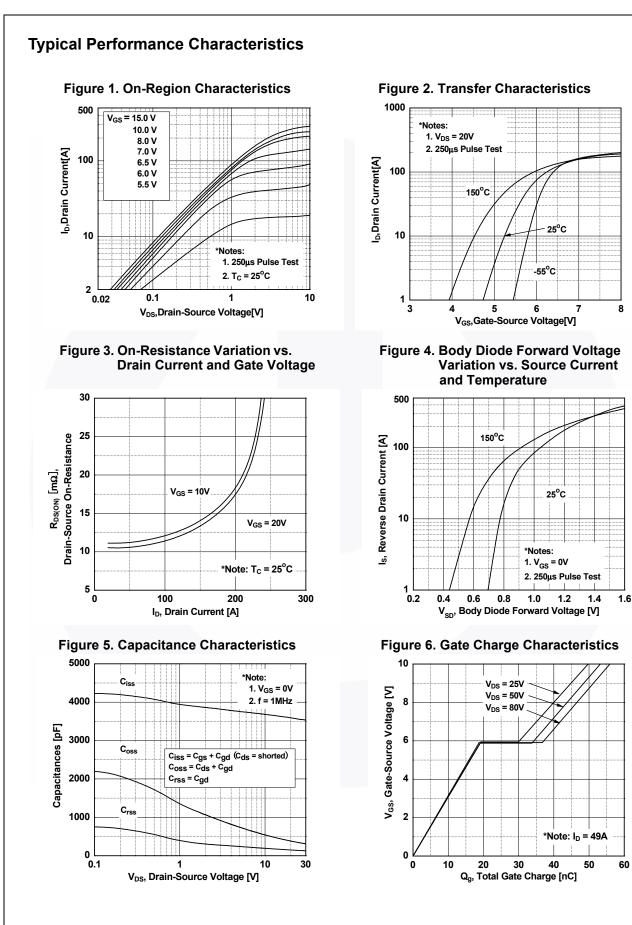
Symbol		Parameter		FDI150N10	Unit
V <sub>DSS</sub>	Drain to Source Voltage			100	V
V <sub>GSS</sub>	Gate to Source Voltage			±20	V
I <sub>D</sub>	Drain Current	- Continuous (T <sub>C</sub> = 25 <sup>o</sup> C)		57	Α
	Drain Current	- Continuous (T <sub>C</sub> = 100 <sup>o</sup> C)		40	Α
I <sub>DM</sub>	Drain Current	- Pulsed (1	Note 1)	228	Α
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)		Note 2)	132	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		Note 3)	7.5	V/ns
P <sub>D</sub>	Dewer Dissingtion	$(T_{\rm C} = 25^{\rm o}{\rm C})$		110	W
	Power Dissipation	- Derate Above 25°C		0.88	W/ºC
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range			-55 to +150	°C
TL	Maximum Lead Tempera	ature for Soldering, 1/8" from Case for 5 Secon	lds	300	°C

## **Thermal Characteristics**

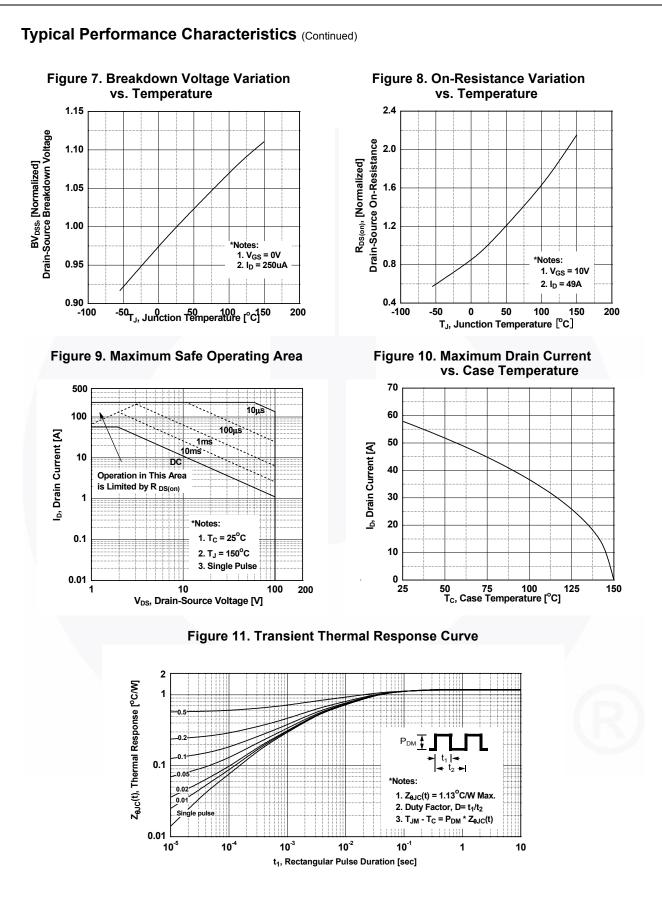
Symbol	Parameter FDI150N1		Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	1.13	°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	0/00

Part Nu	Part Number Top Mark Pag		Package	Packing Method	Reel Size	Тар	e Width	Qua	ntity
•		I <sup>2</sup> -PAK	•		N/A		50 units		
Electrica	l Char	acteristics T <sub>C</sub> = 25%	C unless of	therwise noted.					
Symbol		Parameter		Test Conditio	ns	Min.	Тур.	Max.	Unit
Off Charao	cteristic						.,,,,		
BV <sub>DSS</sub>		Source Breakdown Voltage	<u>ا</u> د	$= 250 \mu A V_{co} = 0 V$	To= 25°C	100	-	-	V
ΔBV <sub>DSS</sub> /ΔTJ	Breakdo	own Voltage Temperature		$I_D = 250 \ \mu\text{A}, \ V_{GS} = 0 \ V, \ T_C = 25^{\circ}\text{C}$ $I_D = 250 \ \mu\text{A}, \ \text{Referenced to } 25^{\circ}\text{C}$		-	0.1	_	V/ºC
7 Δ1 J	Cocilica	Coefficient		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V		_	-	1	
I <sub>DSS</sub>	Zero Ga	Zero Gate Voltage Drain Current		$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{C} = 150^{\circ}\text{C}$		-	-	500	μA
I <sub>GSS</sub>	Gate to	Body Leakage Current		$V_{GS} = \pm 20 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$		-	-	±100	nA
On Charac		· · ·					1	I	1
V <sub>GS(th)</sub>		reshold Voltage	N	/ <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> = 250 μA		2.5	_	4.5	V
R <sub>DS(on)</sub>		rain to Source On Resistan		$I_{GS} = 10 \text{ V}, I_D = 49 \text{ A}$		-	12	16	mΩ
9FS		d Transconductance		$I_{\rm DS} = 20$ V, $I_{\rm D} = 49$ A		_	156	-	S
C <sub>iss</sub> C <sub>oss</sub> C <sub>rss</sub>	Output (	apacitance Capacitance e Transfer Capacitance		V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz		-	3580 340 140	4760 450 210	pF pF pF
Switching									
t <sub>d(on)</sub>		Delay Time				-	47	104	ns
t <sub>r</sub>		Rise Time	V	V <sub>DD</sub> = 50 V, I <sub>D</sub> = 49 A,		-	164	338	ns
t <sub>d(off)</sub>		Delay Time	V	$V_{\rm GS}$ = 10 V, $R_{\rm G}$ = 25 $\Omega$	-	-	86	182	ns
t <sub>f</sub>		Fall Time		(Note 4)		-	83	176	ns
Q <sub>g(tot)</sub>	Total Ga	te Charge at 10V	V	/ <sub>DS</sub> = 80 V, I <sub>D</sub> = 49 A,		-	53	69	nC
5(10.1)	Gate to	Source Gate Charge		V <sub>GS</sub> = 10 V (Note 4)		-	19	-	nC
	Gate to	Drain "Miller" Charge				-	15	-	nC
Q <sub>gs</sub> Q <sub>gd</sub>									
Q <sub>gs</sub> Q <sub>gd</sub>	rce Dioc	le Characteristics							
Q <sub>gs</sub> Q <sub>gd</sub>		e Characteristics	ce Diode F	Forward Current		-	-	57	Α
Q <sub>gs</sub> Q <sub>gd</sub> Drain-Sou	Maximu					-	-	57 228	A A
Q <sub>gs</sub> Q <sub>gd</sub> Drain-Sou I <sub>S</sub> I <sub>SM</sub>	Maximu Maximu	m Continuous Drain to Sour	iode Forwa				-		
Q <sub>gs</sub> Q <sub>gd</sub> Drain-Sou I <sub>S</sub>	Maximu Maximu Drain to	m Continuous Drain to Sour m Pulsed Drain to Source D	iode Forwa age V	ard Current			- - - 41	228	Α

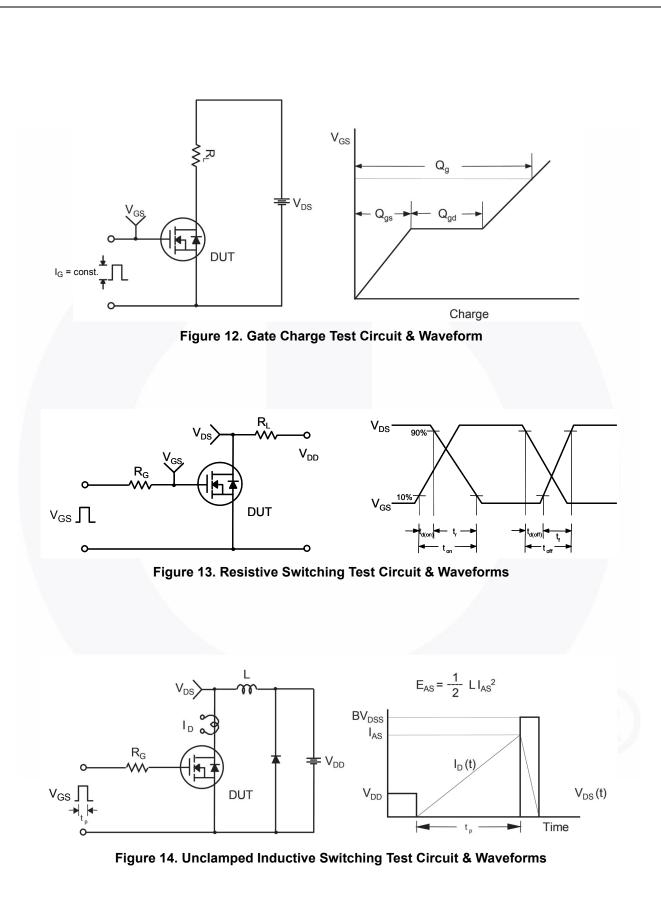
4: Essentially independent of operating temperature typical characteristics.

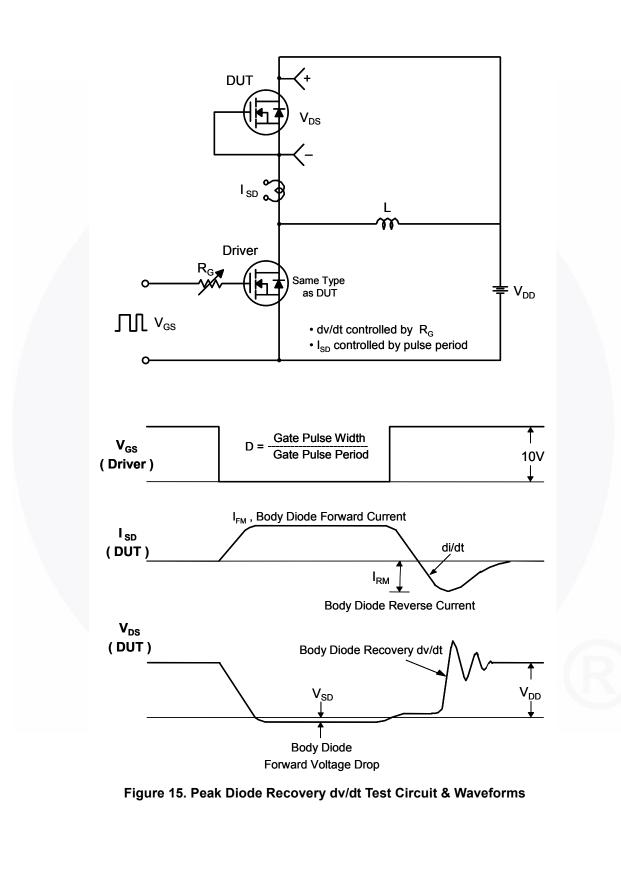


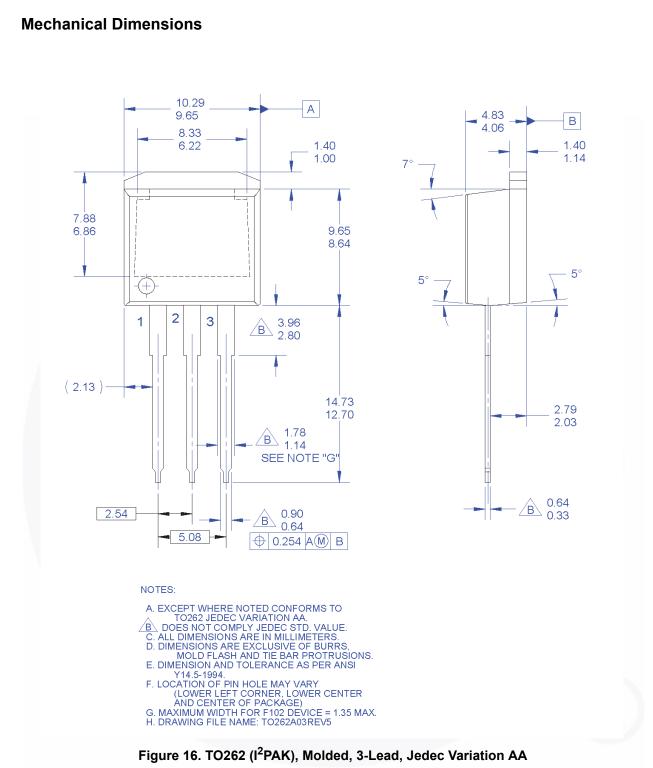
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