

# **MOSFET** – P-Channel, POWERTRENCH® Integrated with Schottky Diode

-20 V, -3.1 A, 95 m $\Omega$ 

## FDFMA2P029Z, FDFMA2P029Z-F106

## **General Description**

This device is designed specifically as a single package solution for the battery charge switch in cellular handset and other ultra-portable applications. It features a MOSFET with very low on-state resistance and an independently connected low forward voltage schottky diode allows for minimum conduction losses.

The MicroFET<sup>™</sup> 2X2 package offers exceptional thermal performance for its physical size and is well suited to linear mode applications.

## **Features**

## **MOSFET**

- Max  $r_{DS(on)} = 95 \text{ m}\Omega$  at  $V_{GS} = -4.5 \text{ V}$ ,  $I_D = -3.1 \text{ A}$
- Max  $r_{DS(on)} = 141 \text{ m}\Omega$  at  $V_{GS} = -2.5 \text{ V}$ ,  $I_D = -2.5 \text{ A}$
- HBM ESD Protection Level > 2.5 kV (Note 1) Schottky
- $V_F < 0.37 V @ 500 mA$
- Low Profile 0.8 mm Maximum In the New Package MicroFET
- These Devices are Pb-Free and are RoHS Compliant

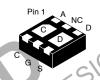
1. The diode connected between the gate and source serves only protection against ESD. No gate overvoltage rating is implied.

### **MOSFET**

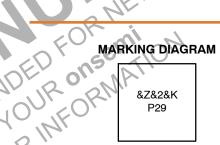
V <sub>DS</sub> MAX	r <sub>DS(on)</sub> MAX	I <sub>D</sub> MAX
-20 V	95 mΩ @ -4.5 V	–3.1 A
	141 mΩ @ -2.5 V	

## **SCHOTTKY DIODE**

V <sub>RRM</sub> MAX	V <sub>F</sub> MAX	I <sub>O</sub> MAX	
20 V	0.37 V @ 500 mA	2 A	



WDFN6 2x2, 0.65P **MicroFET** CASE 511DA



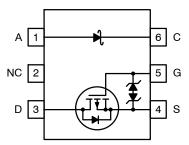
&Z = Assembly Plant Code

= 2-Digit Date Code

&K = 2-Digits Lot Run Traceability Code

P29 = Device Code

### **PIN CONNECTIONS**



### ORDERING INFORMATION

See detailed ordering and shipping information on page 6 of this data sheet.

1

## MOSFET MAXIMUM RATINGS (T<sub>A</sub> = 25°C unless otherwise noted)

Symbol	Para	meter	Ratings	Unit
V <sub>DS</sub>	Drain to Source Voltage		-20	V
$V_{GS}$	Gate to Source Voltage	Gate to Source Voltage		V
I <sub>D</sub>	Drain Current	Continuous (Note 2a)	-3.1	Α
		Pulsed	-6	
$P_{D}$	Power Dissipation	(Note 2a)	1.4	W
		(Note 2b)	0.7	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range		–55 to +150	°C
$V_{RRM}$	Schottky Repetitive Peak Reverse Voltage		20	V
Io	Schottky Average Forward Current		2	Α

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

## THERMAL CHARACTERISTICS

Symbol	Parameter Ratings	Unit
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note 2a)	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note 2b)	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note 2c)	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note 2d)	

- R<sub>θJA</sub> is determined with the device mounted on a 1 in<sup>2</sup> oz. copper pad on a 1.5 x 1.5 in. board of FR=4 material. R<sub>θJC</sub> is guaranteed by design while R<sub>θJA</sub> is determined by the user's board design.
  - a. MOSFET R<sub>0JA</sub> = 86°C/W when mounted on a 1 in<sup>2</sup> pad of 2 oz copper, 1.5" x 1.5" x 0.062" thick PCB.
  - b. MOSFET  $R_{\theta JA}$  = 173°C/W when mounted on a minimum pad of 2 oz copper.
  - c. Schottky  $R_{\theta JA} = 86^{\circ}$ C/W when mounted on a 1 in<sup>2</sup> pad of 2 oz copper, 1.5" x 1.5" x 0.062" thick PCB.
  - d. Schottky  $R_{\theta JA}$  = 140°C/W when mounted on a minimum pad of 2 oz copper.



## **ELECTRICAL CHARACTERISTICS** ( $T_J = 25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Test Conditio	ns	Min	Тур	Max	Unit
OFF CHARACTERISTICS							
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage	$I_D = -250 \mu A, V_{GS} = 0 V$	-20	-	-	V	
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	$I_D$ = $-250$ μA, referenced to $25^{\circ}$ C		-	-12	_	mV/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}$		-	-	-1	μΑ
I <sub>GSS</sub>	Gate to Source Leakage Current	$V_{GS} = \pm 12 \text{ V}, V_{DS} = 0 \text{ V}$		-	-	±10	μΑ
ON CHARAC	CTERISTICS		•			•	
V <sub>GS(th)</sub>	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = -250 \mu A$		-0.6	-1.0	-1.5	V
$\frac{\Delta V_{\rm GS(th)}}{\Delta T_{\rm J}}$	Gate to Source Threshold Voltage Temperature Coefficient	$I_D = -250 \mu A$ , referenced	to 25°C	-	4	_	mV/°C
r <sub>DS(on)</sub>	Static Drain to Source On-Resistance	V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -3.1 A		_	60	95	mΩ
		V <sub>GS</sub> = -2.5 V, I <sub>D</sub> = -2.5 A		-	88	141	
		V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -3.1 A	, T <sub>J</sub> = 125°C	-	87	140	
9FS	Forward Transconductance	V <sub>DS</sub> = -10 V, I <sub>D</sub> = -3.1 A			11	_	S
DYNAMIC C	HARACTERISTICS			TE	1		
C <sub>iss</sub>	Input Capacitance	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, $	= 1 MHz	1	540	720	pF
C <sub>oss</sub>	Output Capacitance		(O)	401	120	160	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		O \ 2	8 K	100	150	pF
	CHARACTERISTICS	OID.	0	W.	<u> </u>		
t <sub>d(on)</sub>	Turn-On Delay Time	$V_{DD} = -10 \text{ V}, V_{D} = -1 \text{ A}$	DY OY	-	13	24	ns
t <sub>r</sub>	Rise Time	$V_{GS} = -4.5 \text{ V}, R_{GEN} = 6.9$	3 11/10	-	11	20	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	CO'C' 22 113		-	37	59	ns
t <sub>f</sub>	Fall Time	LATH FO.	-	-	36	58	ns
Q <sub>g(TOT)</sub>	Total Gate Charge	$V_{DD} = -10 \text{ V}, I_D = -3.1 \text{ A}$		-	7	10	nC
Q <sub>gs</sub>	Gate to Source Gate Charge	$V_{GS} = -4.5 \text{ V}$		-	1.1	-	nC
Q <sub>gd</sub>	Gate to Drain "Miller" Charge	7/1		-	2.4	-	nC
DRAIN-SOL	JRCE DIODE CHARACTERISTICS	•					
I <sub>S</sub>	Maximum Continuous Drain-Source Diode	Forward Current		-	-	-1.1	Α
V <sub>SD</sub>	Source to Drain Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_{S} = -1.1 \text{ A (N)}$	ote 3)	-	-0.8	-1.2	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> = -3.1 A, di/dt = 100 A	/μs	-	25	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge			-	9	-	nC
SCHOTTKY DIODE CHARACTERISTICS							
$V_{R}$	Reverse Voltage	I <sub>R</sub> = 1 mA	$T_J = 25^{\circ}C$	20	-	-	V
I <sub>R</sub>	Reverse Leakage	V <sub>R</sub> = 20 V	T <sub>J</sub> = 25°C	-	30	300	μΑ
			T <sub>J</sub> = 125°C	-	10	45	mA
V <sub>F</sub>	Forward Voltage	I <sub>F</sub> = 500 mA	T <sub>J</sub> = 25°C	-	0.32	0.37	V
			T <sub>J</sub> = 125°C	-	0.21	0.26	
		I <sub>F</sub> = 1 A	T <sub>J</sub> = 25°C	-	0.37	0.435	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

3. Pulse Test: Pulse Width < 300 µs, Duty Cycle < 2.0%

## **TYPICAL CHARACTERISTICS**

(T<sub>J</sub> = 25°C unless otherwise noted)

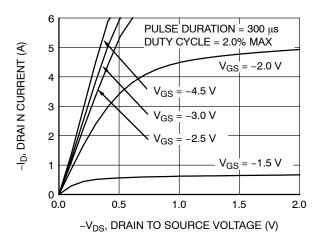


Figure 1. On Region Characteristics

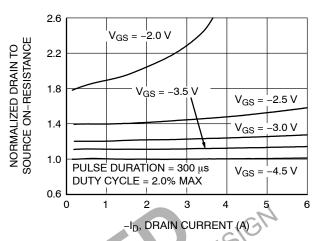


Figure 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

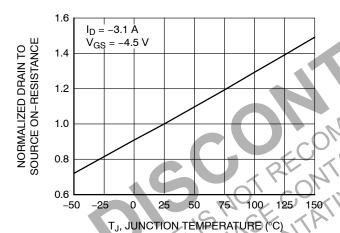


Figure 3. Normalized On-Resistance vs. Junction Temperature

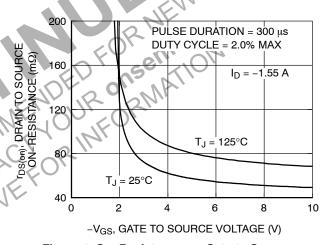


Figure 4. On-Resistance vs. Gate to Source Voltage

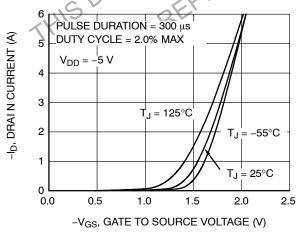


Figure 5. Transfer Characteristics

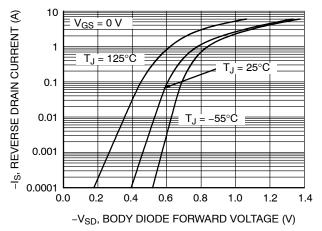


Figure 6. Source to Drain Diode Forward Voltage vs. Source Current

## **TYPICAL CHARACTERISTICS**

 $(T_J = 25^{\circ}C \text{ unless otherwise noted})$  (continued)

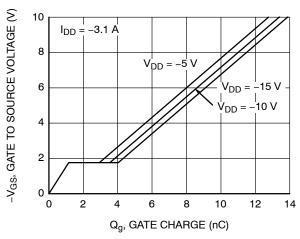


Figure 7. Gate Charge Characteristics

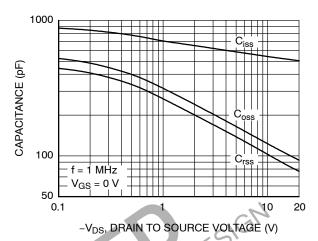


Figure 8. Capacitance Characteristics

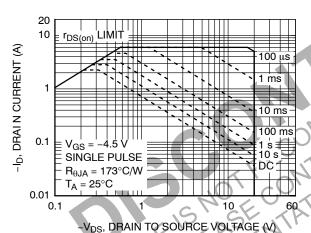


Figure 9. Forward Bias Safe Operating Area

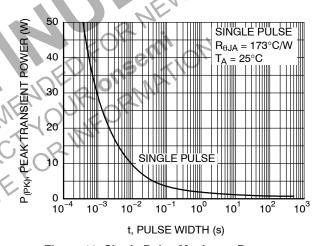


Figure 10. Single Pulse Maximum Power Dissipation

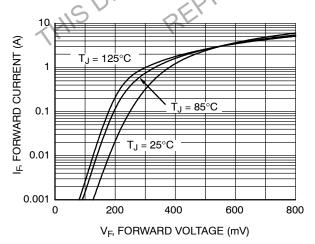


Figure 11. Schottky Diode Forward Voltage

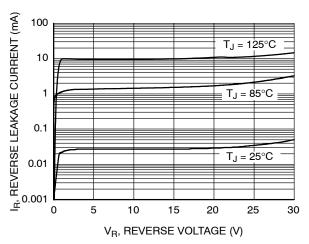


Figure 12. Schottky Diode Reverse Current

## **TYPICAL CHARACTERISTICS**

 $(T_J = 25^{\circ}C \text{ unless otherwise noted})$  (continued)

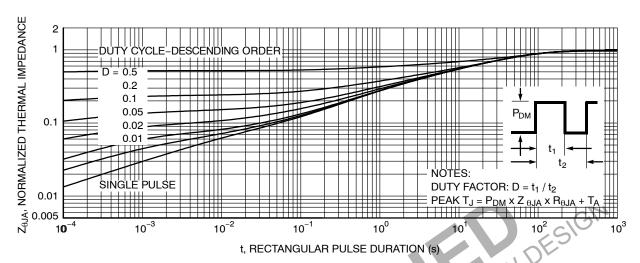


Figure 13. Transient Thermal Response Curve

## PACKAGE MARKING AND ORDERING INFORMATION

Device	Device Marking	Package Type	Reel Size	Tape Width	Shipping <sup>†</sup>
FDFMA2P029Z	P29	WDFN6 2x2, 0.65P MicroFET (Pb-Free)	NENLAUR	8 mm	3000 / Tape & Reel
FDFMA2P029Z-F106	P29	WDFN6 2x2, 0.65P MicroFET (Pb-Free)	OT OR IN	8 mm	3000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

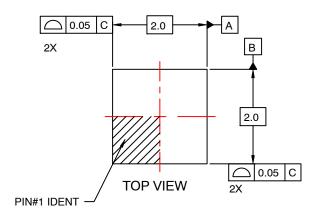
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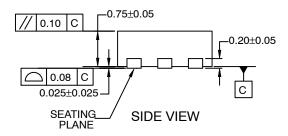
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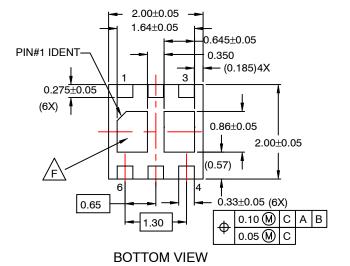


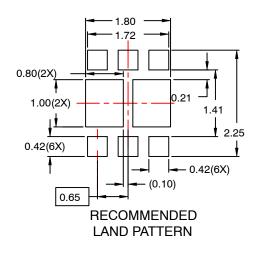
## WDFN6 2x2, 0.65P CASE 511DA ISSUE O

**DATE 31 JUL 2016** 









## NOTES:

- A. CONFORM TO JADEC REGISTRATIONS MO-229, VARIATION VCCC, EXCEPT WHERE NOTED.
- B. DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 2009.
- D. LAND PATTERN RECOMMENDATION IS EXISTING INDUSTRY LAND PATTERN.

F. NON-JEDEC DUAL DAP

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