ON Semiconductor

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$\frac{\text{MOSFET}}{\text{SUPERFET}^{\text{R}}} - \text{N-Channel,} \\ \text{SUPERFET}^{\text{R}} \text{ II} \\ \text{600 V, 20.6 A, 190 m} \\ \Omega$

FCPF190N60E-F154

Description

SUPERFET II MOSFET is ON Semiconductor's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This technology is tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. Consequently, SUPERFET II MOSFET Easy drive series helps manage EMI issues and allows for easier design implementation.

Features

- 650 V @ $T_J = 150$ °C
- Typ. $R_{DS(on)} = 160 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. Q_g = 63 nC)
- Low Effective Output Capacitance (Typ. Coss.eff = 178 pF)
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

Applications

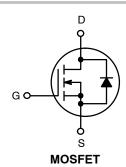
- Computing / Display Power Supplies
- Telecom / Server Power Supplies
- Industrial Power Supplies
- Lighting / Charger / Adapter



ON Semiconductor®

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V _{DSS}	R _{DS(ON)} MAX	I _D MAX
600 V	190 mΩ @ 10 V	20.6 A





TO-220F Ultra Narrow Lead CASE 221BN

MARKING DIAGRAM



\$Y = ON Semiconductor Logo &Z = Assembly Plant Code &3 = Data Code (Year & Week)

&K = Lot

FCPF190N60E = Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

MOSFET MAXIMUM RATINGS ($T_C = 25^{\circ}C$, Unless otherwise noted)

Symbol	Par	Value	Unit	
V _{DSS}	Drain to Source Voltage		600	V
V _{GSS}	Gate to Source Voltage	- DC	±20	V
		- AC (f > 1 Hz)	±30	
I _D	Drain Current	− Continuous (T _C = 25°C)	20.6*	Α
		− Continuous (T _C = 100°C)	13.1*	1
I _{DM}	Drain Current	- Pulsed (Note 1)	61.8*	Α
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		400	mJ
I _{AS}	Avalanche Current (Note 2)		4.0	А
E _{AR}	Repetitive Avalanche Energy (Note 1)		2.1	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3) MOSFET dv/dt		20	V/ns
			100	1
P _D	Power Dissipation	(T _C = 25°C)	39	W
		- Derate Above 25°C	0.31	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C
TL	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds		300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. *Drain current limited by maximum junction temperature.

1. Repetitive rating: pulse width limited by maximum junction temperature.

2. $I_{AS} = 4 \text{ A}$, $V_{DD} = 50 \text{ V}$, $R_{G} = 25 \Omega$, starting $T_{J} = 25^{\circ}\text{C}$.

3. $I_{SD} \le 10 \text{ A}$, I_{SD

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{ heta JC}$	Thermal Resistance, Junction to Case, Max.	3.2	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	°C/W

PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Shipping
FCPF190N60E-F154	FCPF190N60E	TO-220F (Pb-Free)	50 Units / Tube

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
OFF CHARACT	ERISTICS				-	
BV _{DSS}	Drain to Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 10 \text{ mA}, T_J = 25^{\circ}\text{C}$	600	-	_	V
		V _{GS} = 0 V, I _D = 10 mA, T _J = 150°C	650	-	_	V
$\Delta BV_{DSS} / \Delta T_{J}$	Breakdown Voltage Temperature Coefficient	I _D = 10 mA, Referenced to 25°C	-	0.67	_	V/°C
BV _{DS}	Drain-Source Avalanche Breakdown Voltage	V _{GS} = 0 V, I _D = 20 A	-	700	_	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 480 V, V _{GS} = 0 V	-	-	1.0	μΑ
		V _{DS} = 480 V, T _C = 125°C	-	-	10	1
I _{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	-	-	±100	nA
ON CHARACTE	RISTICS					
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250 \mu A$	2.5	-	3.5	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 10 A	-	0.16	0.19	Ω
9FS	Forward Transconductance	V _{DS} = 20 V, I _D = 10 A	_	20	_	S
OYNAMIC CHAI	RACTERISTICS					
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	2385	3175	pF
C _{oss}	Output Capacitance	†	_	1795	2396	pF
C _{rss}	Reverse Transfer Capacitance		_	110	165	pF
C _{oss}	Output Capacitance	V _{DS} = 380 V, V _{GS} = 0 V, f = 1 MHz	-	42	_	pF
C _{oss (eff.)}	Effective Output Capacitance	V _{DS} = 0 V to 480 V, V _{GS} = 0 V	-	178	_	pF
Q _{g(tot)}	Total Gate Charge at 10 V	V _{DS} = 380 V, I _D = 10 A, V _{GS} = 10 V	-	63	82	nC
Q _{gs}	Gate to Source Gate Charge	(Note 4)	_	10	_	nC
Q _{gd}	Gate to Drain "Miller" Charge		_	24	_	nC
ESR	Equivalent Series Resistance	f = 1 MHz	-	5	-	Ω
WITCHING CH	ARACTERISTICS			•		•
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 380 \text{ V}, I_D = 10 \text{ A}, V_{GS} = 10 \text{ V},$	_	23	56	ns
t _r	Turn-On Rise Time	$R_g = 4.7 \Omega$ (Note 4)	_	14	38	ns
t _{d(off)}	Turn-Off Delay Time	(1010 1)	_	101	212	ns
t _f	Turn-Off Fall Time		_	15	40	ns
	N DIODE CHARACTERISTICS			•	•	
I _S	Maximum Continuous Source to Drain Diode Forward Current		-	-	20.2	Α
I _{SM}	Maximum Pulsed Source to Drain Diode Forward Current		_	-	60.6	Α
V _{SD}	Source to Drain Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 10 A	_	_	1.2	V
t _{rr}	Reverse Recovery Time	V _{DD} = 400 V, I _{SD} = 10 A,	_	308	-	ns
Q _{rr}	Reverse Recovery Charge	dl _F /dt = 100 A/μs	_	4.8		μС

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

TYPICAL PERFORMANCE CHARACTERISTICS

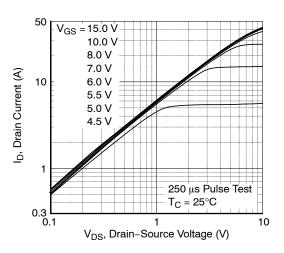


Figure 1. On-Region Characteristics

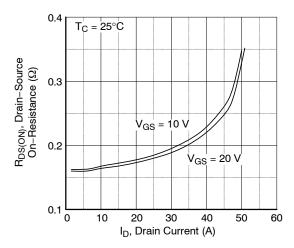


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

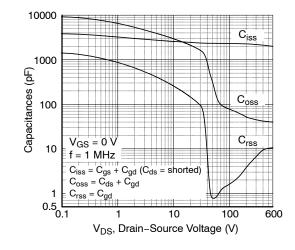


Figure 5. Capacitance Characteristics

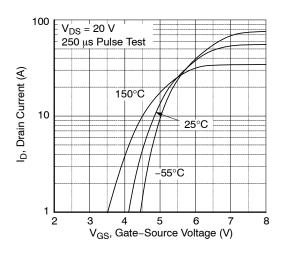


Figure 2. Transfer Characteristics

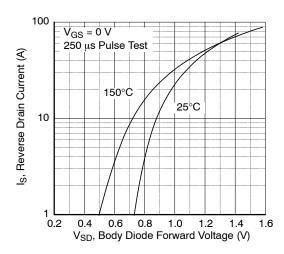


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

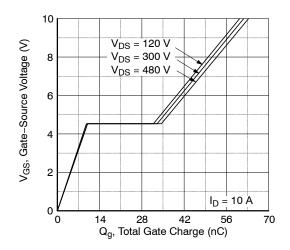


Figure 6. Gate Charge Characteristics

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

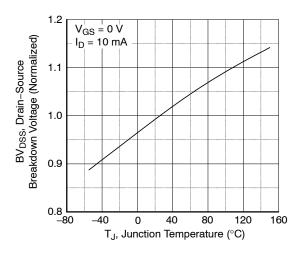


Figure 7. Breakdown Voltage Variation vs. Temperature

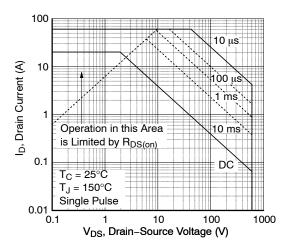


Figure 9. Maximum Safe Operating Area

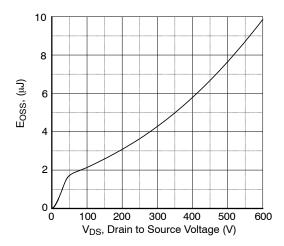


Figure 11. E_{OSS} vs. Drain-to-Source Voltage

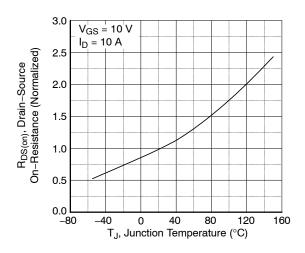


Figure 8. On–Resistance Variation vs. Temperature

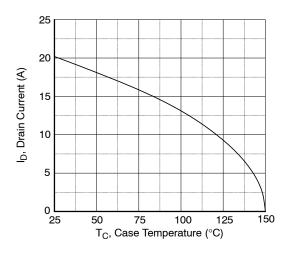


Figure 10. Maximum Drain Current vs. Case Temperature

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

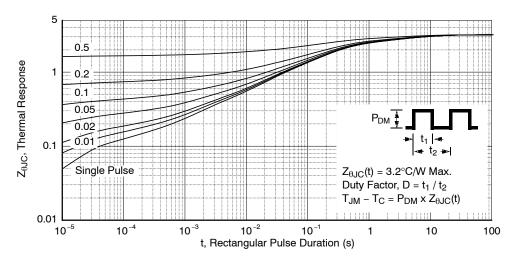


Figure 12. Transient Thermal Response Curve

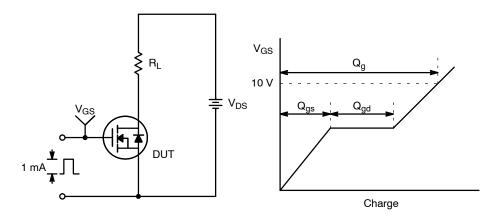


Figure 13. Gate Charge Test Circuit & Waveform

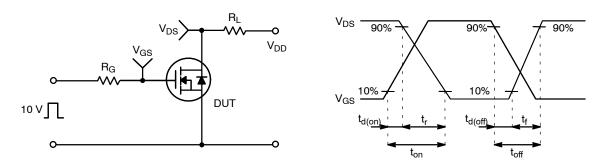


Figure 14. Resistive Switching Test Circuit & Waveforms

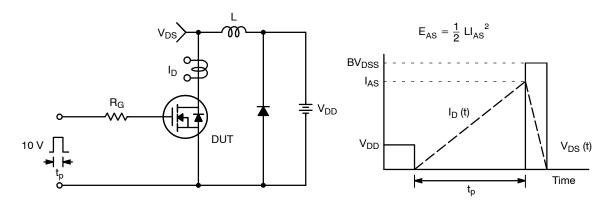


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

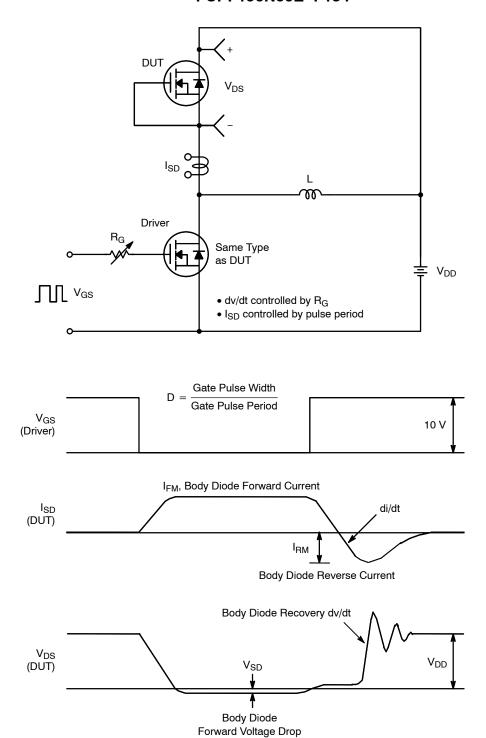


Figure 16. Peak Recovery dv/dt Test Circuit & Waveforms

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PACKAGE DIMENSIONS

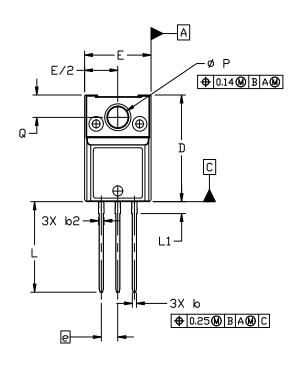
TO-220 FULLPACK, 3-LEAD

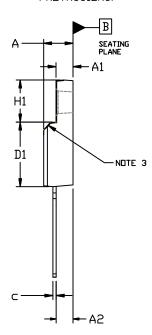
CASE 221BN **ISSUE O**



NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- CONTROLLING DIMENSION: MILLIMETERS
- CONTOUR UNCONTROLLED IN THIS AREA.
- DIMENSIONS EXCLUDE BURRS, MOLD FLASH, AND TIE BAR PROTRUSIONS.





	MILLIMETERS			
DIM	MIN.	N□M.	MAX.	
Α	4.60	4.70	4.80	
A1	2.50	2.60	2.70	
A2	2.47	2.57	2.67	
b	0.56	0.63	0.69	
b2			0.90	
С	0.46	0.53	0.59	
D	15.80	16.00	16.20	
D1	9.58	9.68	9.78	
Ε	10.00	10.20	10.40	
е	2.54 BSC			
H1	6.32 REF			
L	13.45	13.60	13.75	
L1	1.70	1.80	1.90	
Р	3.00	3.10	3.20	
Q	3,25	3.35	3.45	

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