

ESD Protection Diodes

Ultra Low Capacitance ESD Protection Diode for High Speed Data Line

ESD8101, ESD8111

The ESD81x1 Series ESD protection diodes are designed to protect high speed data lines from ESD. Ultra-low capacitance and low ESD clamping voltage make this device an ideal solution for protecting voltage sensitive high speed data lines.

Features

- Low Capacitance (0.20 pF Typ, I/O to GND)
- Protection for the Following IEC Standards:
IEC 61000-4-2 (Level 4)
- Low ESD Clamping Voltage
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

Typical Applications

- USB 3.0/3.1
- MHL 2.0
- eSATA

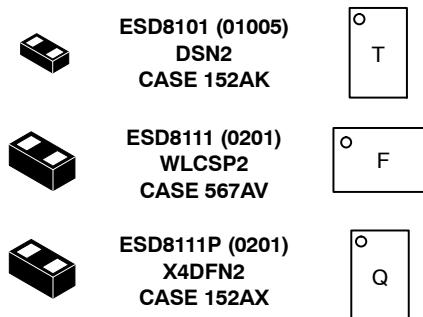
MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Operating Junction Temperature Range	T _J	-55 to +150	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C
Lead Solder Temperature – Maximum (10 Seconds)	T _L	260	°C
ESD8101: IEC 61000-4-2 Contact IEC 61000-4-2 Air ESD8111: IEC 61000-4-2 Contact IEC 61000-4-2 Air	ESD	±23 ±23 ±30 ±30	kV kV kV kV

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

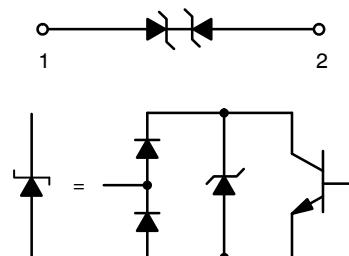
See Application Note AND8308/D for further description of survivability specs.

MARKING DIAGRAMS



T, F, Q = Device Code

PIN CONFIGURATION AND SCHEMATIC



ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

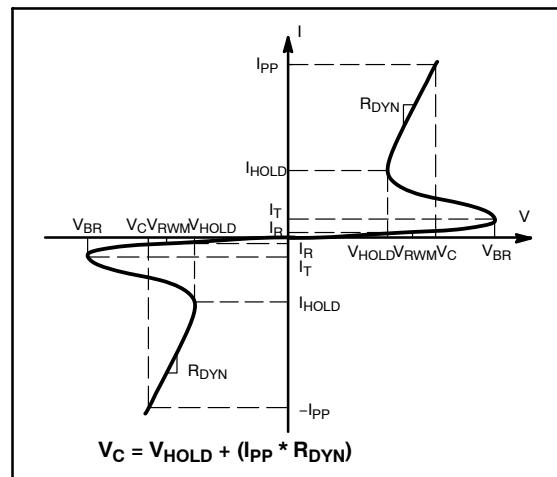
NOTE: Some of the devices on this data sheet have been DISCONTINUED. Please refer to the table on page 2.

ESD8101, ESD8111

ELECTRICAL CHARACTERISTICS

($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter
V_{RWM}	Working Peak Voltage
I_R	Maximum Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
V_{HOLD}	Holding Reverse Voltage
I_{HOLD}	Holding Reverse Current
R_{DYN}	Dynamic Resistance
I_{PP}	Maximum Peak Pulse Current
V_C	Clamping Voltage @ I_{PP} $V_C = V_{HOLD} + (I_{PP} * R_{DYN})$



ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Reverse Working Voltage	V_{RWM}	I/O Pin to GND			3.3	V
Breakdown Voltage	V_{BR}	$I_T = 1 \text{ mA}$, I/O Pin to GND	5.5	7.9	8.6	V
Reverse Leakage Current	I_R	$V_{RWM} = 3.3 \text{ V}$, I/O Pin to GND			1.0	μA
Reverse Holding Voltage	V_{HOLD}	I/O Pin to GND		2.1		V
Holding Reverse Current	I_{HOLD}	I/O Pin to GND		17		mA
ESD8111 Clamping Voltage	V_C	$I_{PP} = 7.1 \text{ A}$, (8/20 μs pulse)			8.0	V
ESD8101, ESD8111 Clamping Voltage TLP (Note 1)	V_C	$I_{PP} = 8 \text{ A}$ } IEC 61000-4-2 Level 2 equivalent } ($\pm 4 \text{ kV}$ Contact, $\pm 4 \text{ kV}$ Air) $I_{PP} = 16 \text{ A}$ } IEC 61000-4-2 Level 4 equivalent } ($\pm 8 \text{ kV}$ Contact, $\pm 15 \text{ kV}$ Air)		6.5		V
Dynamic Resistance	R_{DYN}	I/O Pin to GND		0.46		Ω
Junction Capacitance	C_J	$V_R = 0 \text{ V}$, $f = 1 \text{ MHz}$		0.2	0.4	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. ANSI/ESD STM5.5.1 – Electrostatic Discharge Sensitivity Testing using Transmission Line Pulse (TLP) Model.

TLP conditions: $Z_0 = 50 \Omega$, $t_p = 100 \text{ ns}$, $t_r = 4 \text{ ns}$, averaging window; $t_1 = 30 \text{ ns}$ to $t_2 = 60 \text{ ns}$.

ORDERING INFORMATION

Device	Package	Shipping [†]
ESD8111FCT5G	WLCSP2 (Pb-Free)	10,000 / Tape & Reel
ESD8111PFCT5G	WLCSP2 Side wall Isolated 0201 (Pb-Free)	10,000 / Tape & Reel

DISCONTINUED (Note 2)

ESD8101FCT5G	DSN2 (Pb-Free)	10,000 / Tape & Reel
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[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, [BRD8011/D](#).

2. **DISCONTINUED:** This device is not recommended for new design. Please contact your **onsemi** representative for information. The most current information on this device may be available on www.onsemi.com.

ESD8101, ESD8111

TYPICAL CHARACTERISTICS

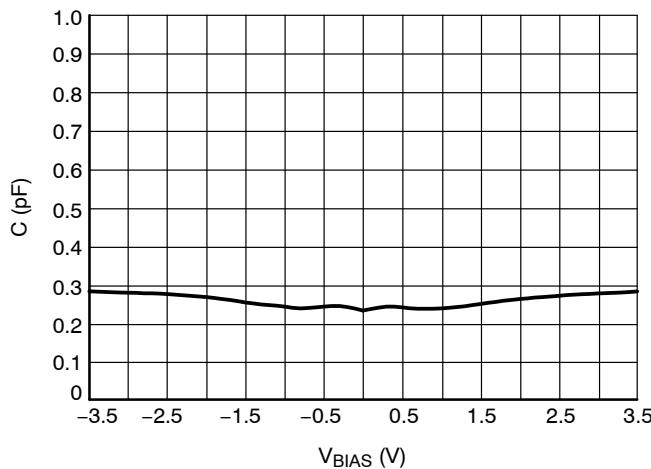


Figure 1. ESD8101 CV Characteristics

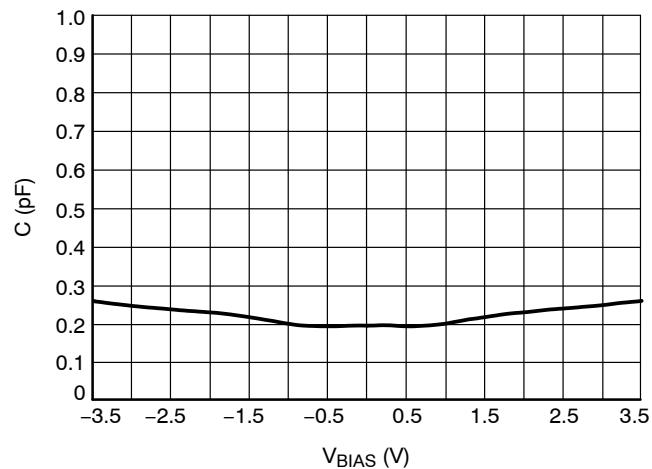


Figure 2. ESD8111 CV Characteristics

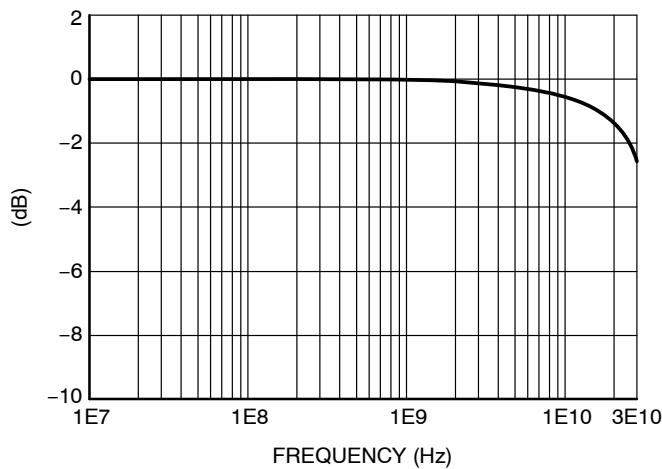


Figure 3. ESD8101 S21 Insertion Loss

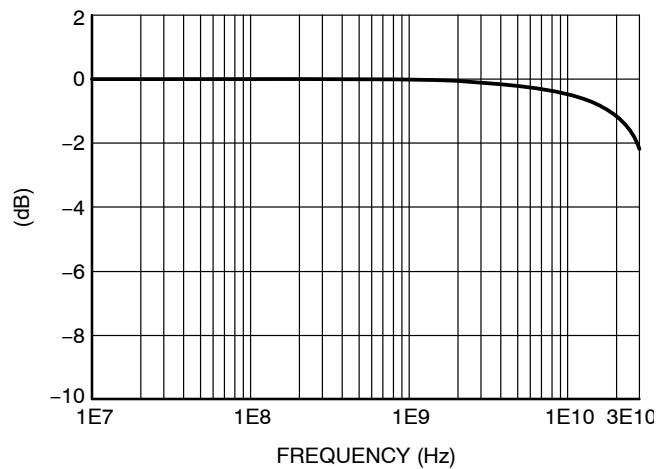


Figure 4. ESD8111 S21 Insertion Loss

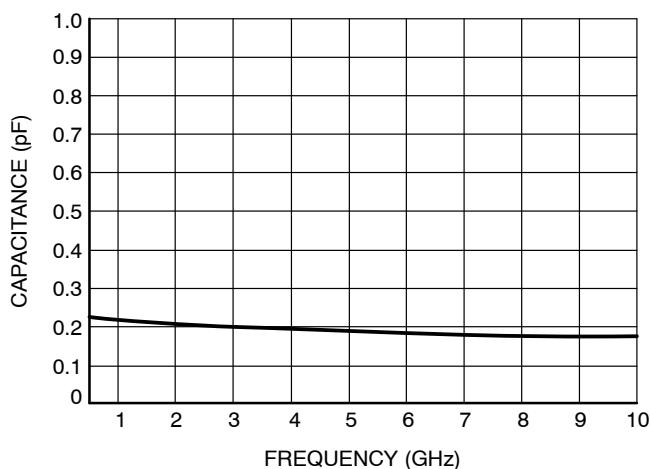


Figure 5. ESD8101 Capacitance over Frequency

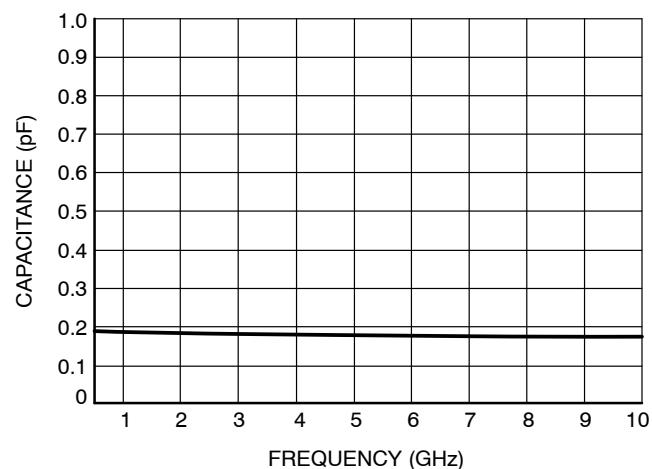


Figure 6. ESD8111 Capacitance over Frequency

ESD8101, ESD8111

TYPICAL CHARACTERISTICS

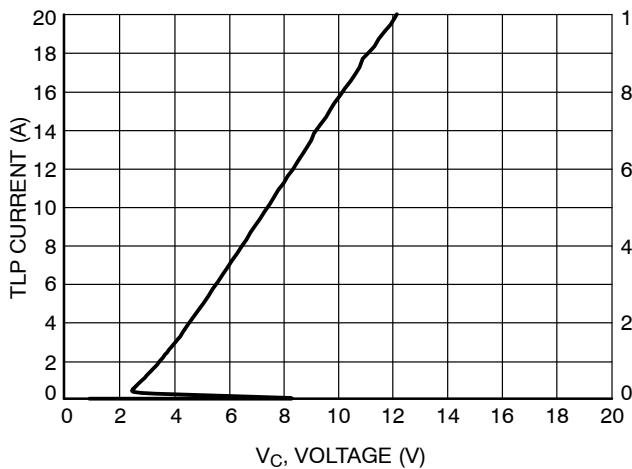


Figure 7. ESD8101 Positive TLP I-V Curve

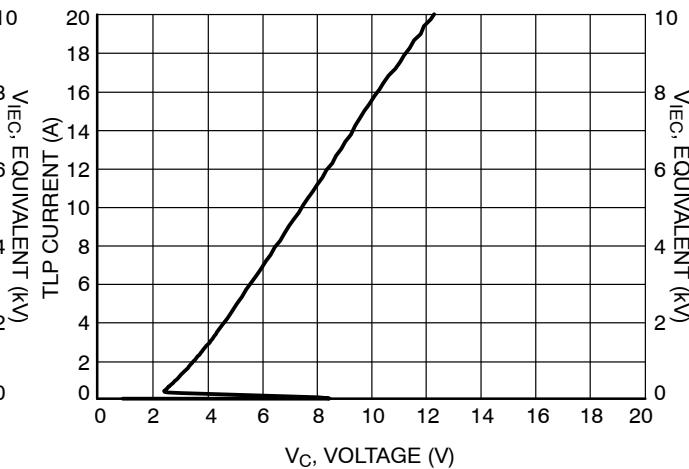


Figure 8. ESD8111 Positive TLP I-V Curve

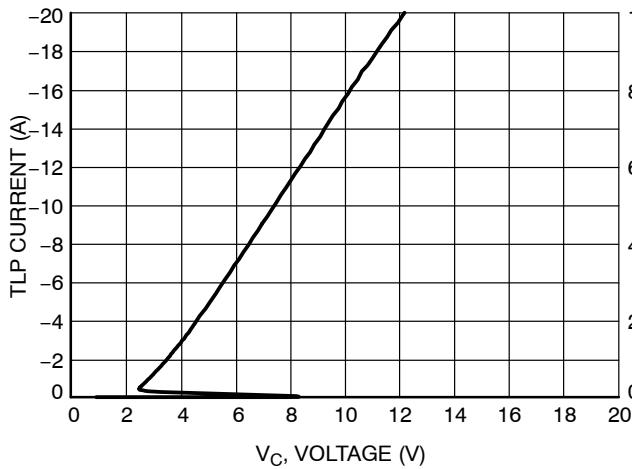


Figure 9. ESD8101 Negative TLP I-V Curve

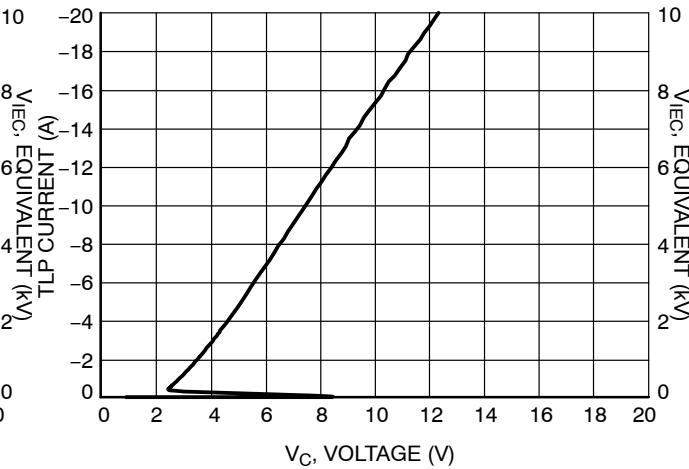


Figure 10. ESD8111 Negative TLP I-V Curve

ESD8101, ESD8111

IEC 61000-4-2 Spec.

Level	Test Voltage (kV)	First Peak Current (A)	Current at 30 ns (A)	Current at 60 ns (A)
1	2	7.5	4	2
2	4	15	8	4
3	6	22.5	12	6
4	8	30	16	8

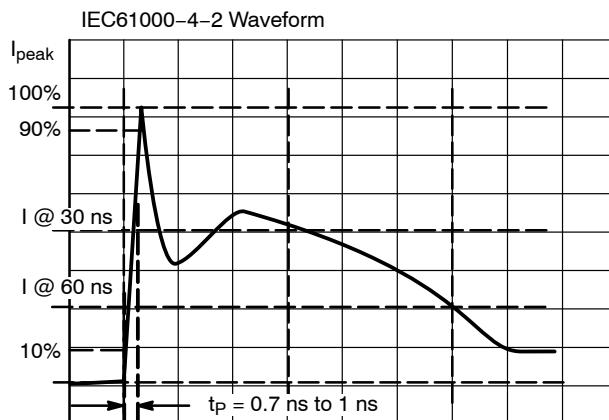


Figure 11. IEC61000-4-2 Spec

Transmission Line Pulse (TLP) Measurement

Transmission Line Pulse (TLP) provides current versus voltage (I-V) curves in which each data point is obtained from a 100 ns long rectangular pulse from a charged transmission line. A simplified schematic of a typical TLP system is shown in Figure 12. TLP I-V curves of ESD protection devices accurately demonstrate the product's ESD capability because the 10s of amps current levels and under 100 ns time scale match those of an ESD event. This is illustrated in Figure 13 where an 8 kV IEC 61000-4-2 current waveform is compared with TLP current pulses at 8 A and 16 A. A TLP I-V curve shows the voltage at which the device turns on as well as how well the device clamps voltage over a range of current levels.

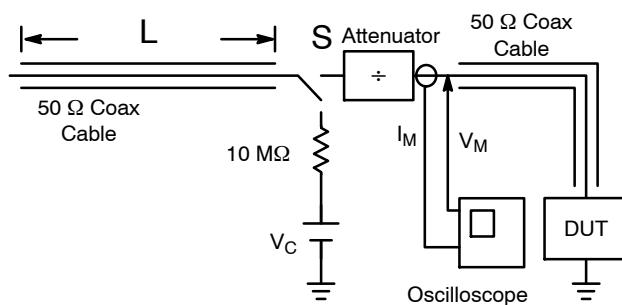


Figure 12. Simplified Schematic of a Typical TLP System

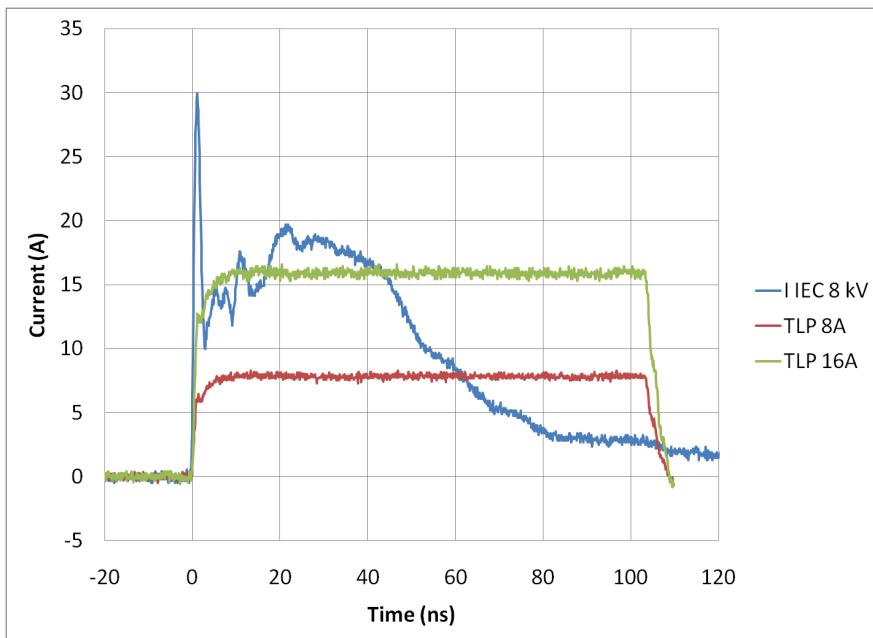


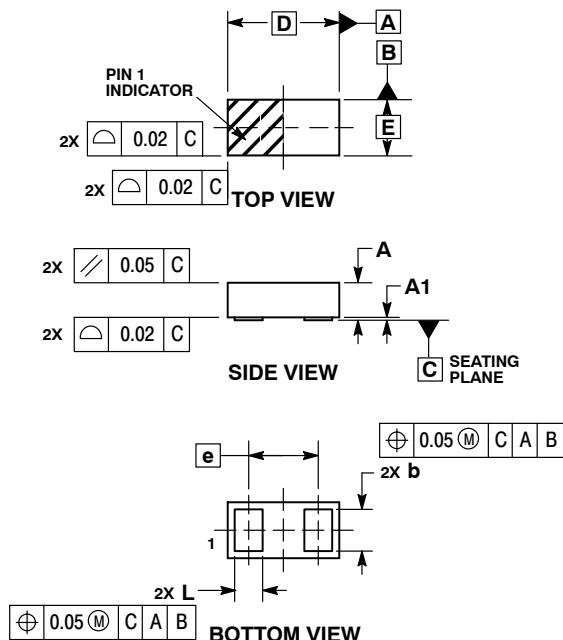
Figure 13. Comparison Between 8 kV IEC 61000-4-2 and 8 A and 16 A TLP Waveforms



SCALE 16:1

DSN2, 0.435x0.23, 0.27P, (01005)
CASE 152AK
ISSUE A

DATE 17 FEB 2015



NOTES:

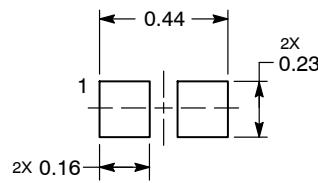
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.

MILLIMETERS		
DIM	MIN	MAX
A	0.165	0.195
A1	---	0.030
b	0.177	0.193
D	0.435	BSC
E	0.230	BSC
e	0.270	BSC
L	0.112	0.128

GENERIC
MARKING DIAGRAM*

X = Specific Device Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G", may or not be present.

RECOMMENDED
SOLDER FOOTPRINT*

DIMENSIONS: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the [onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D](#).

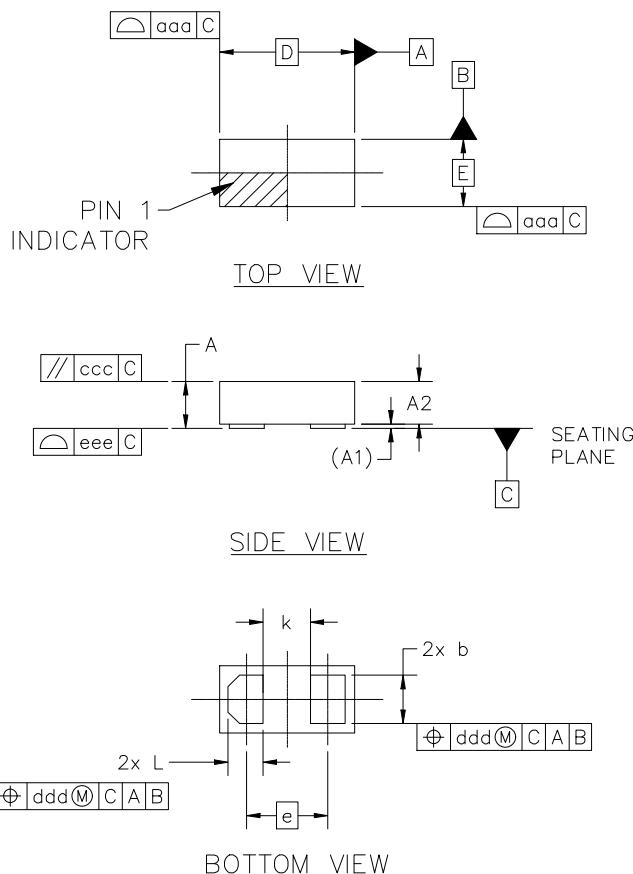
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DESCRIPTION:	DSN2, 0.435X0.23, 0.27P (01005)	PAGE 1 OF 1

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X4DFN2, 0.60x0.30x0.19, 0.36P
CASE 152AX
ISSUE J

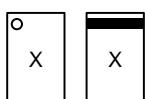
DATE 06 FEB 2025



NOTES:

1. DIMENSIONING AND TOLERANCING AS PER ASME Y14.5M, 2018.
2. CONTROLLING DIMENSION: MILLIMETERS.

**GENERIC
MARKING DIAGRAM***

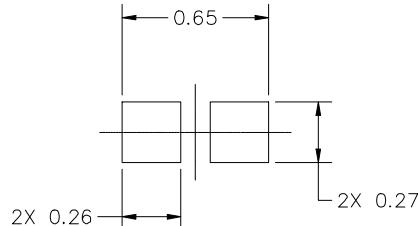


X = Specific Device Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G", may or not be present. Some products may not follow the Generic Marking.

MILLIMETERS			
DIM	MIN	NOM	MAX
A	0.175	0.200	0.225
A1	0.018 REF		
A2	0.180	0.190	0.200
b	0.205	0.215	0.225
D	0.600 BSC		
E	0.300 BSC		
e	0.360 BSC		
k	0.180	---	---
L	0.145	0.155	0.165

TOLERANCE FORM AND POSITION	
aaa	0.025
ccc	0.020
ddd	0.050
eee	0.010



RECOMMENDED MOUNTING FOOTPRINT

- * For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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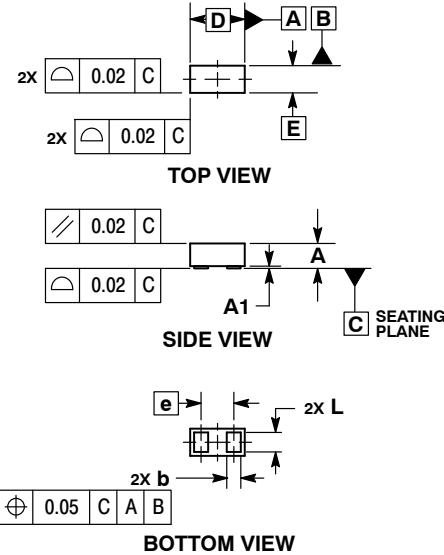
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WLCSP2, 0.6x0.3

CASE 567AV

ISSUE C

DATE 22 SEP 2017



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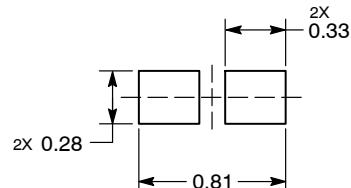
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.

DIM	MILLIMETERS		
	MIN	NOM	MAX
A	0.250	0.275	0.300
A1	0.000	0.025	0.050
b	0.140	0.155	0.170
D	0.570	0.600	0.630
E	0.270	0.300	0.330
e	0.36	0.36 BSC	
L	0.190	0.215	0.240

GENERIC
MARKING DIAGRAM*

X = Specific Device Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

RECOMMENDED
SOLDER FOOTPRINT*

DIMENSIONS: MILLIMETERS

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