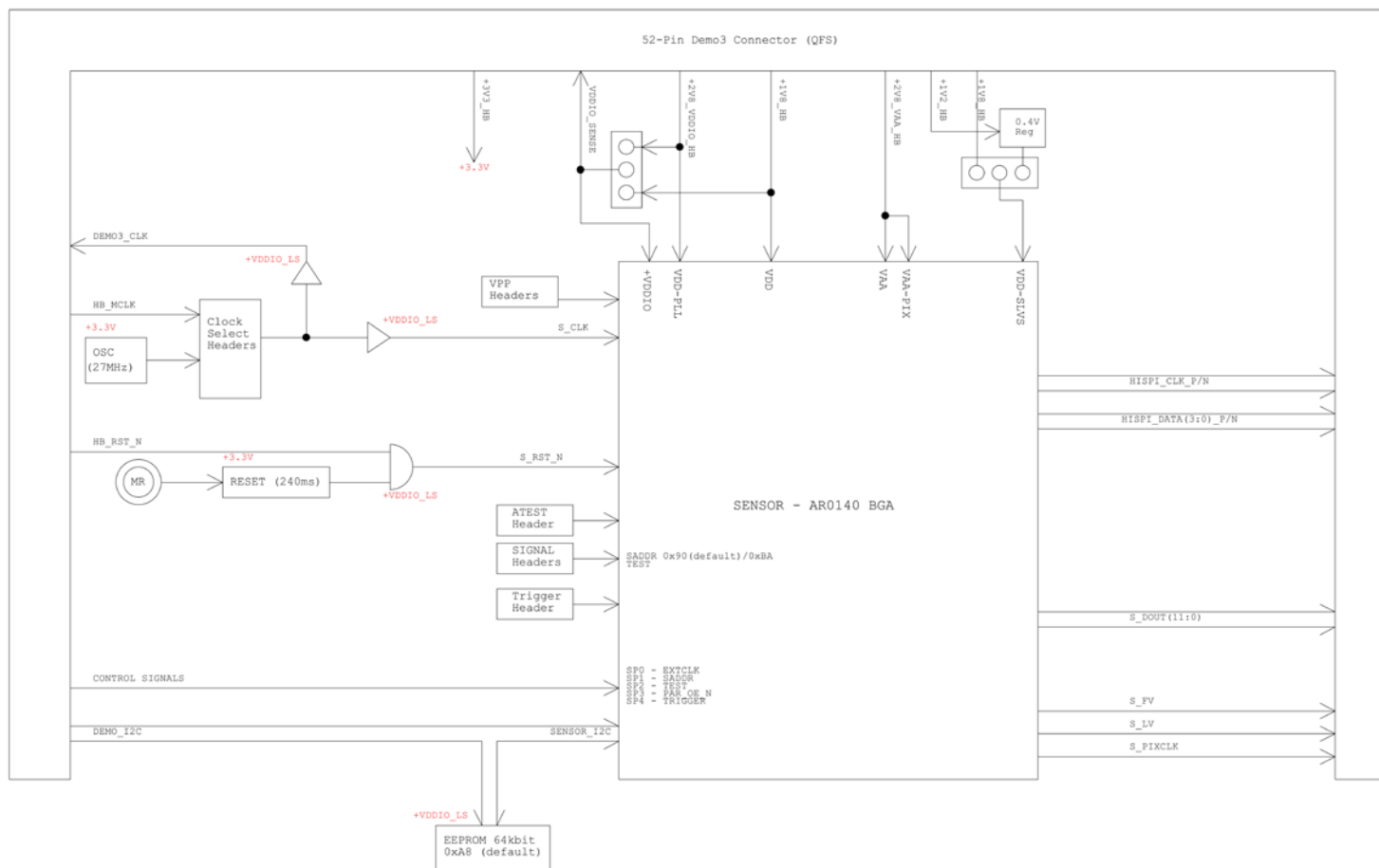
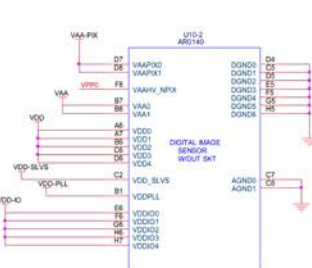
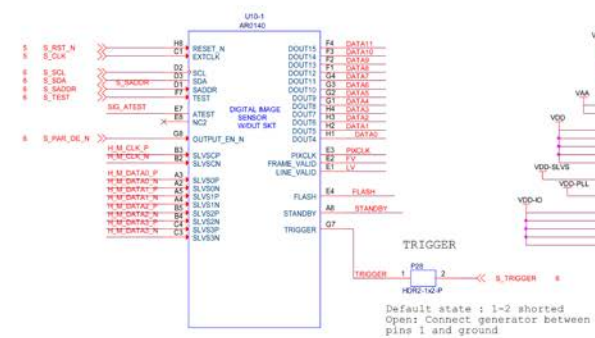
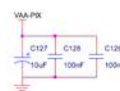
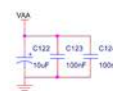
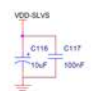
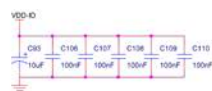
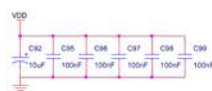


## Block Diagram



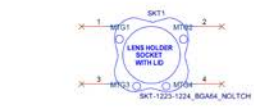
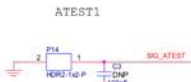
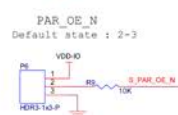
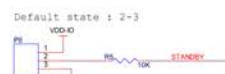
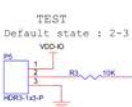
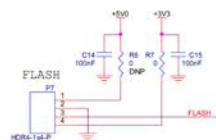
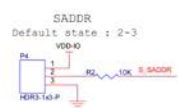
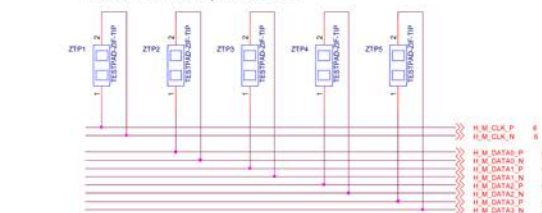
Power supply	Power supply	Power supply
+5V0	+5V0	4
+3V3	+3V3	4.5
+VDDIO_L5	+VDDIO_L5	4.5.6
VDD	VDD	4
VDD-IO	VDD-IO	4
VDD-SLVS	VDD-SLVS	4
VDD-PLL	VDD-PLL	4
VAA	VAA	4
VAA-PXK	VAA-PXK	4



Jumper open =  
Normal Oper.



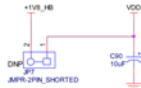
(Note for layout: - Place these testpads near the Demo3 I/F connector at the top side of PCB)



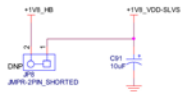


Debug Headers: Cut away the shorted trace and mount header for power debugging

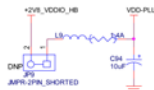
### VDD 1.8V SUPPLY



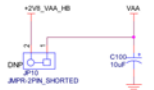
### VDD-SLVS 1.8V SUPPLY



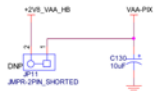
### VDD-PLL 2.8V SUPPLY



### VAA 2.8V SUPPLY

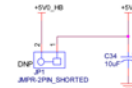


### VAA-PIX 2.8V SUPPLY

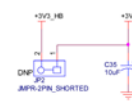


## Power

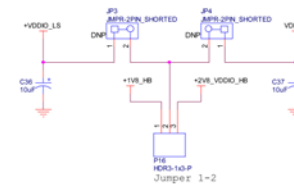
### PERIPHERAL 5V SUPPLY



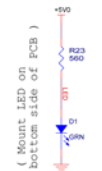
### PERIPHERAL 3.3V SUPPLY



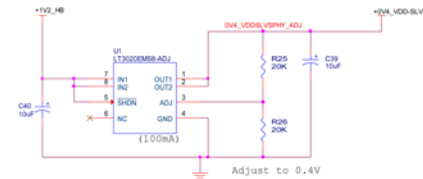
### VDDIO & VDDIO LS 1.8V/2.8V SUPPLY



### 5V LED



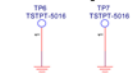
### VDDSLVSPHY 0.4V SUPPLY



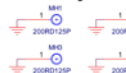
Selection of 0.4V or 1.2V/1V8 for VDDSLVSPHY supply



### Ground Testpoints



### Mounting Holes

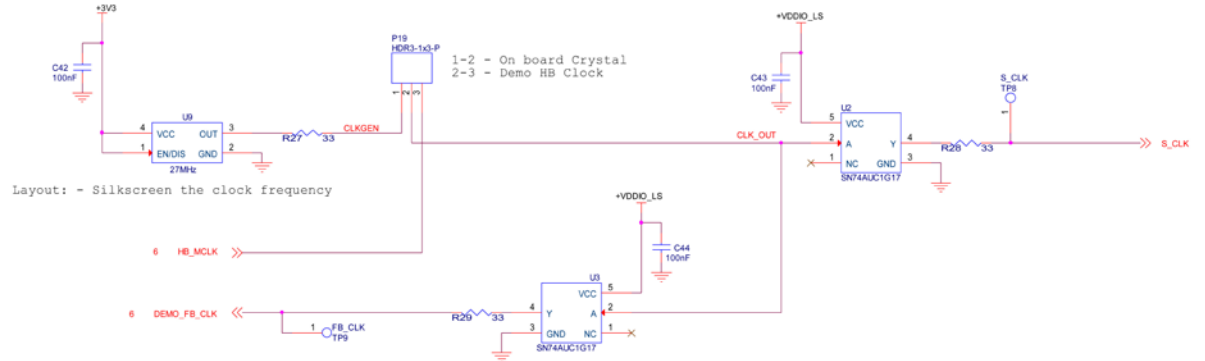




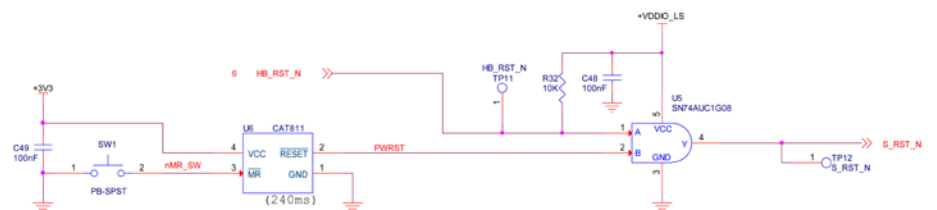
# Clock and Reset

+5V0 3.4  
+3V3 3.4  
+VDDIO\_L5 4.6

## CLOCK CIRCUIT



## RESET CIRCUIT



+5V0_HB		+5V0_HB	4
+3V3_HB		+3V3_HB	4
+2V8_VAA_HB		+2V8_VAA_HB	4
+2V8_VDDIO_HB		+2V8_VDDIO_HB	4
+1V8_HB		+1V8_HB	4
+1V2_HB		+1V2_HB	4
+3V3	3.4.5	+3V3	3.4.5
+VDDIO_LS	4.6	+VDDIO_LS	4.6

Jumper: 1-2 & 3-4 (default status)

**EPROM Address Switch Settings:**

- A2 = HIGH, A1 = LOW, A0 = LOW; Address => 0xA8 (default)
- A2 = HIGH, A1 = HIGH, A0 = LOW; Address => 0xAC
- A2 = LOW, A1 = HIGH, A0 = LOW; Address => 0xA4
- A2 = LOW, A1 = LOW, A0 = LOW; Address => 0xA0

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