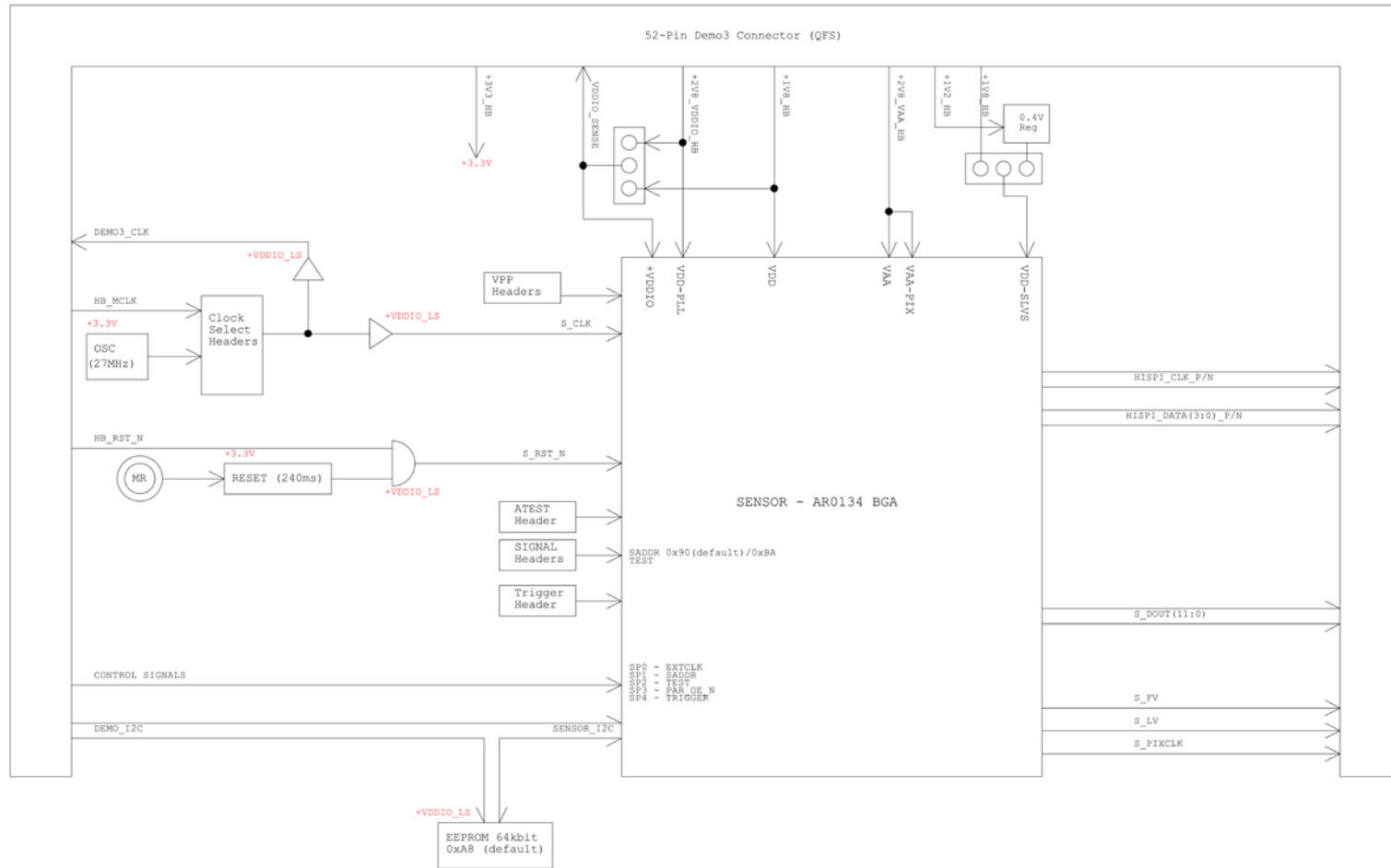




Schematic for the AR0134CSSM25SUEAH3-GEVB Evaluation Board

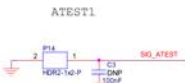
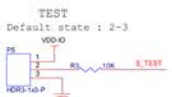
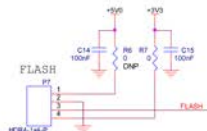
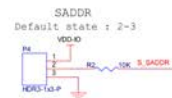
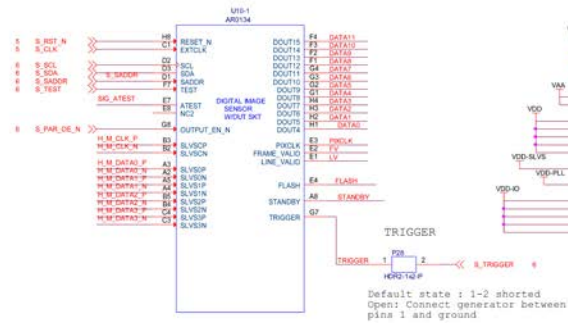
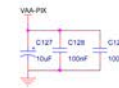
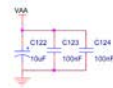
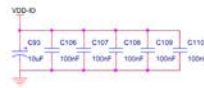
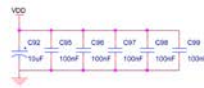
Block Diagram



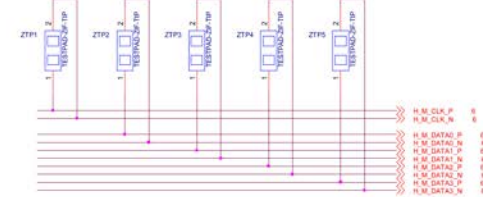


Sensor

+VDD	4
+VDD	4.5
+VDDIO_LB	4.5.5
VDD	4
VDD-I/O	4
VDD-SLV5	4
VDD-PLL	4
VAA	4
VAA-PK	4



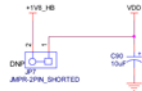
(Note for layout: - Place these testpads near the Demo3 I/F connector at the top side of PCB)





Debug Headers: Cut away the shorted trace and mount header for power debugging

VDD 1.8V SUPPLY



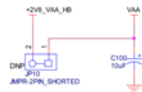
VDD-SLVS 1.8V SUPPLY



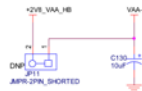
VDD-PLL 2.8V SUPPLY



VAA 2.8V SUPPLY

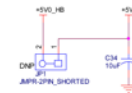


VAA-PIX 2.8V SUPPLY



Power

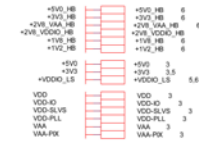
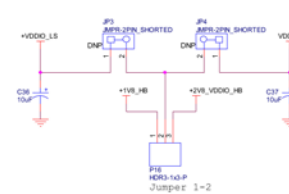
PERIPHERAL 5V SUPPLY



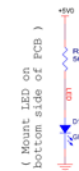
PERIPHERAL 3.3V SUPPLY



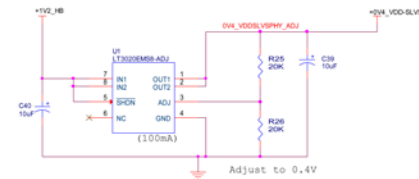
VDDIO & VDDIO LS 1.8V/2.8V SUPPLY



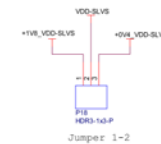
5V LED



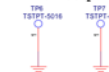
VDDSLVSPHY 0.4V SUPPLY



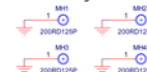
Selection of 0.4V or 1.2V/1V8 for VDDSLVSPHY supply



Ground Testpoints



Mounting Holes



+5V0		+5V0	3,4
+3V3		+3V3	3,4
+VDDIO_LS		+VDDIO_LS	4,6

[illegible]

+5V0_HB		+5V0_HB	4
+3V3_HB		+3V3_HB	4
+2V8_VAA_HB		+2V8_VAA_HB	4
+2V8_VDDIO_HB		+2V8_VDDIO_HB	4
+1V8_HB		+1V8_HB	4
+1V2_HB		+1V2_HB	4
+3V3		+3V3	3.4.1
+VDDIO_LS		+VDDIO_LS	4

[illegible]

Figure 1: Schematic diagram of the I2C interface circuit. The diagram shows a 2-wire I2C interface between a microcontroller (MCU) and an external device (DS18B20). The MCU's I2C pins are connected to the DS18B20's SDA and SCL pins. The DS18B20 is powered by VDD and GND. The MCU is powered by VDD and GND. The DS18B20 has a 10k pull-up resistor on its SDA pin and a 10k pull-up resistor on its SCL pin. The MCU has a 10k pull-up resistor on its SDA pin and a 10k pull-up resistor on its SCL pin. The DS18B20 has a 10k pull-up resistor on its SDA pin and a 10k pull-up resistor on its SCL pin. The MCU has a 10k pull-up resistor on its SDA pin and a 10k pull-up resistor on its SCL pin.

EPPROM Address Switch Settings:

A2 = HIGH, A1 = LOW, A0 = LOW; Address => 0xA8 (default)
 A2 = HIGH, A1 = HIGH, A0 = LOW; Address => 0xA9
 A2 = LOW, A1 = HIGH, A0 = LOW; Address => 0xA4
 A2 = LOW, A1 = LOW, A0 = LOW; Address => 0xA0

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