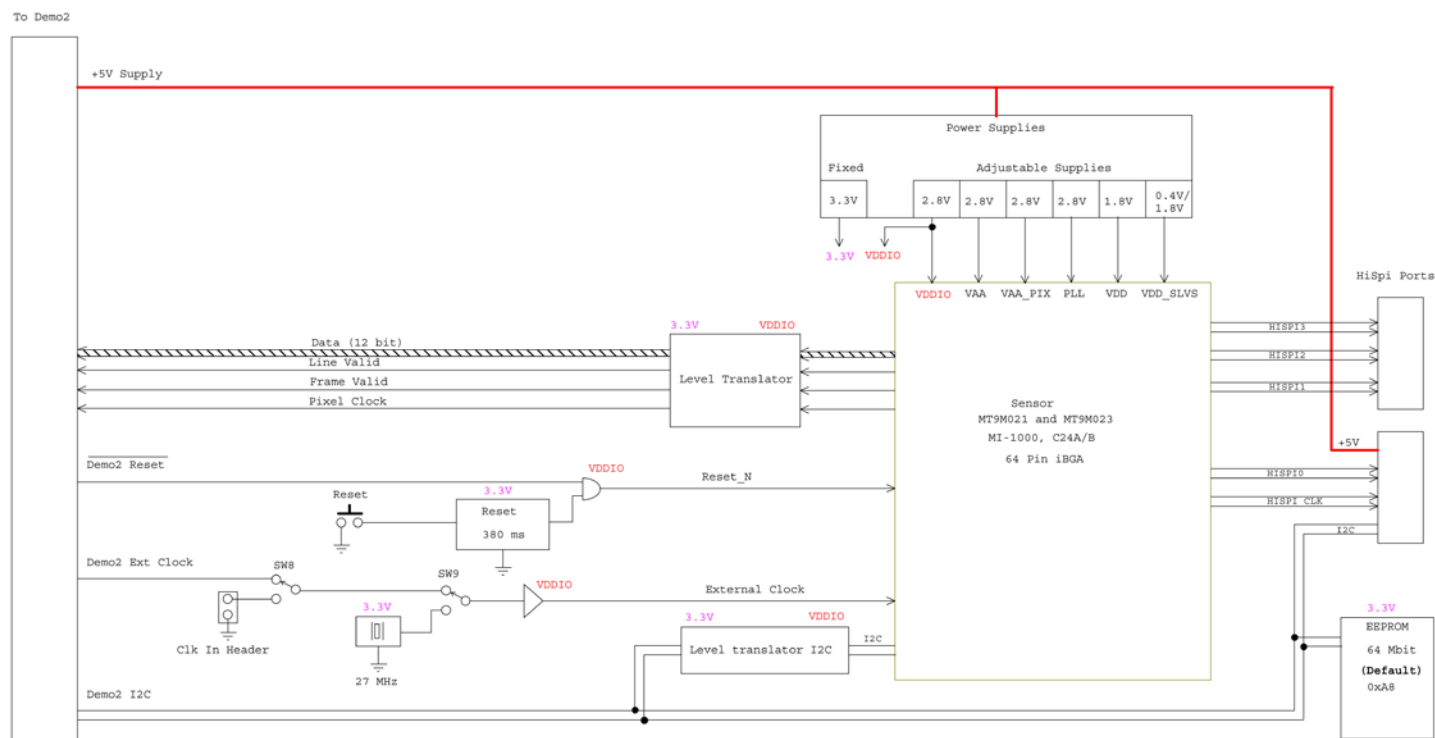
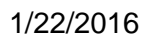




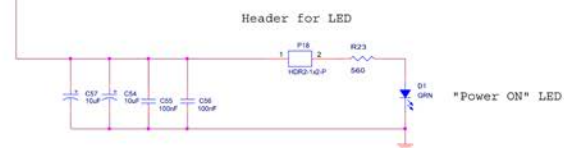
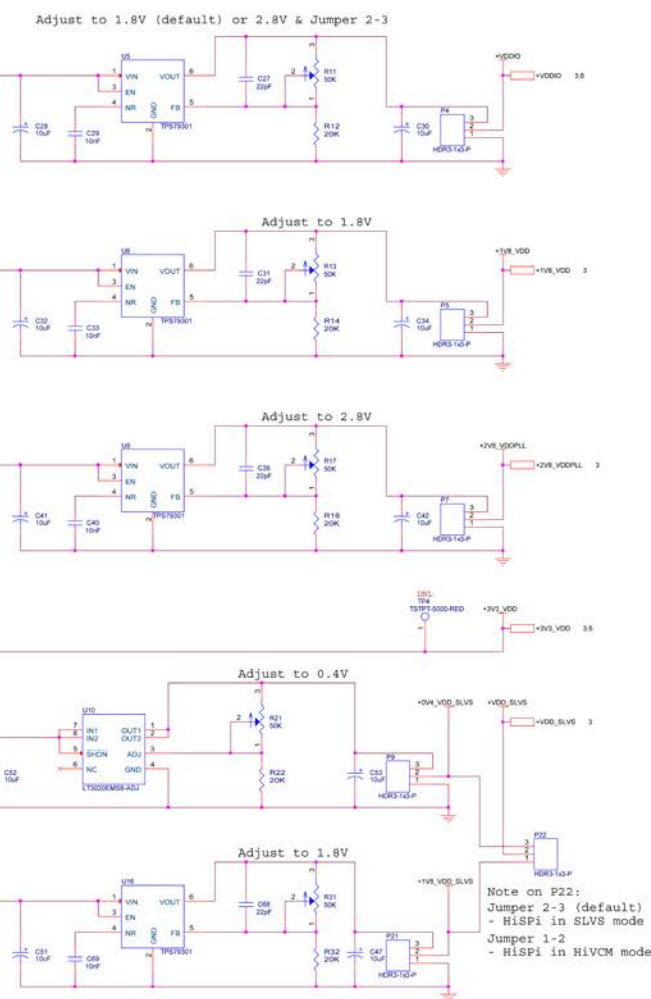
Schematic for the AR0132AT6M00XPEAH3-GEVB Evaluation Board

Block Diagram - MT9M021 IBGA and MT9M023 IBGA

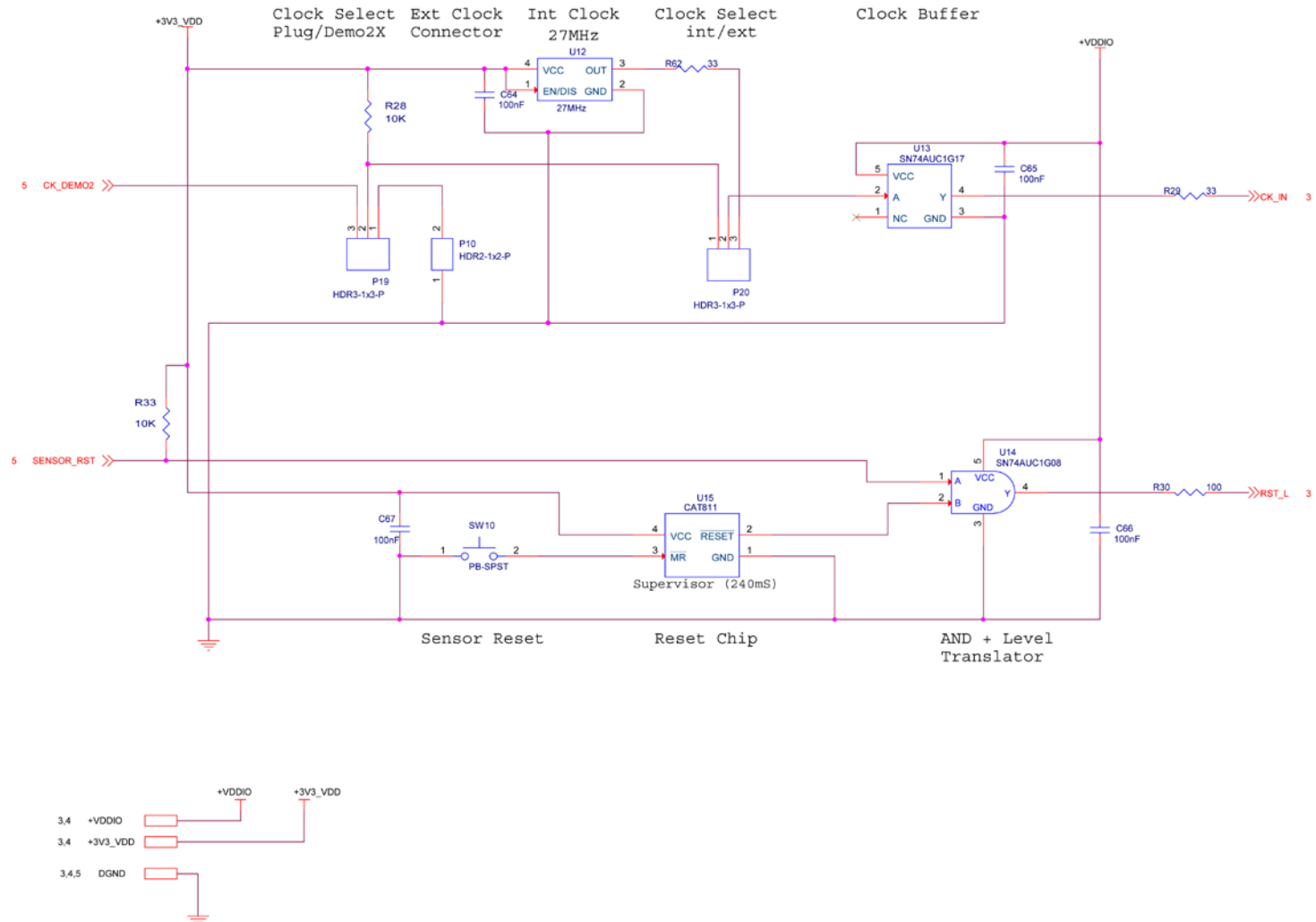




Digital



Note on P22:
Jumper 2-3 (default)
- HiSpi in SLVS mode
Jumper 1-2
- HiSpi in HiVCM mode



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