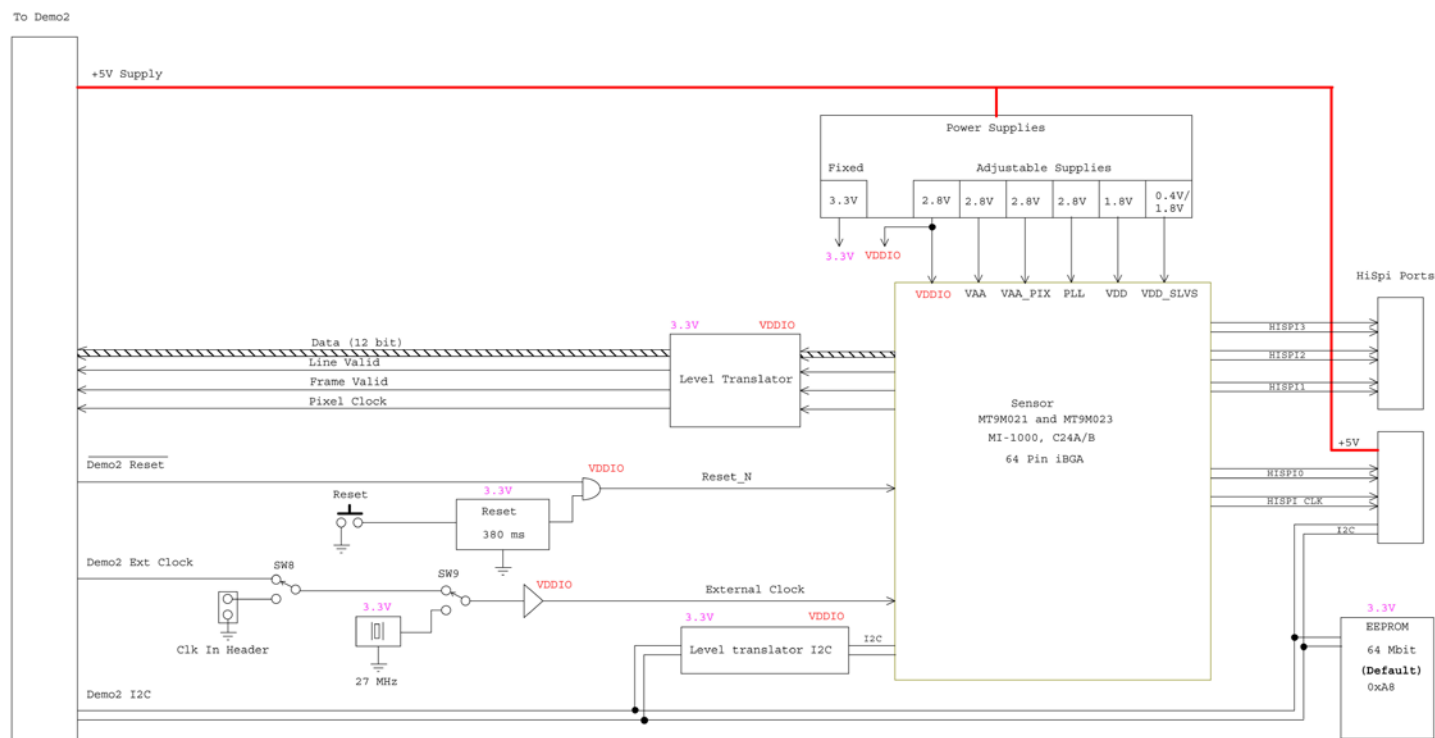
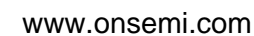




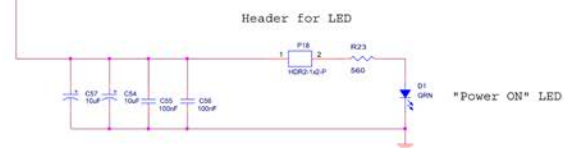
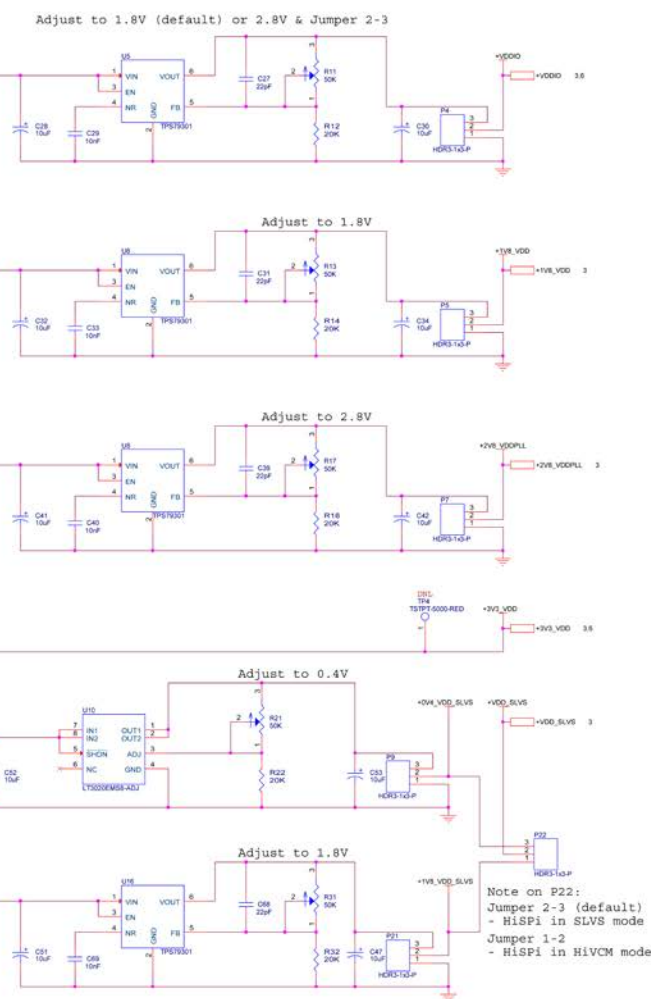
## Schematic for the AR0132AT6G00XPEAH3-GEVB Evaluation Board

Block Diagram - MT9M021 IBGA and MT9M023 IBGA





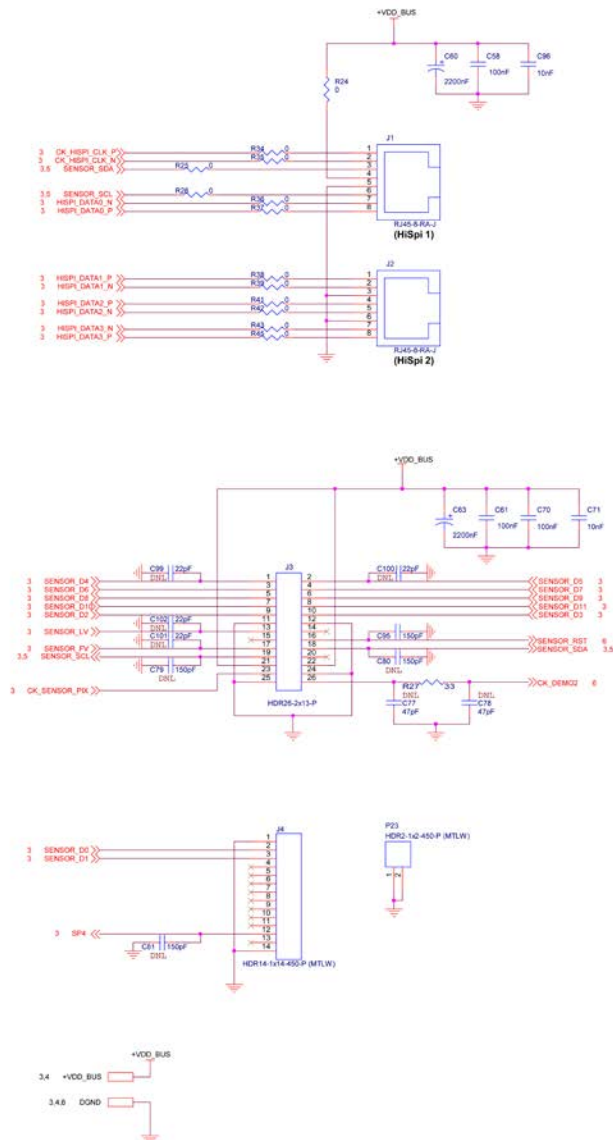
## Digital

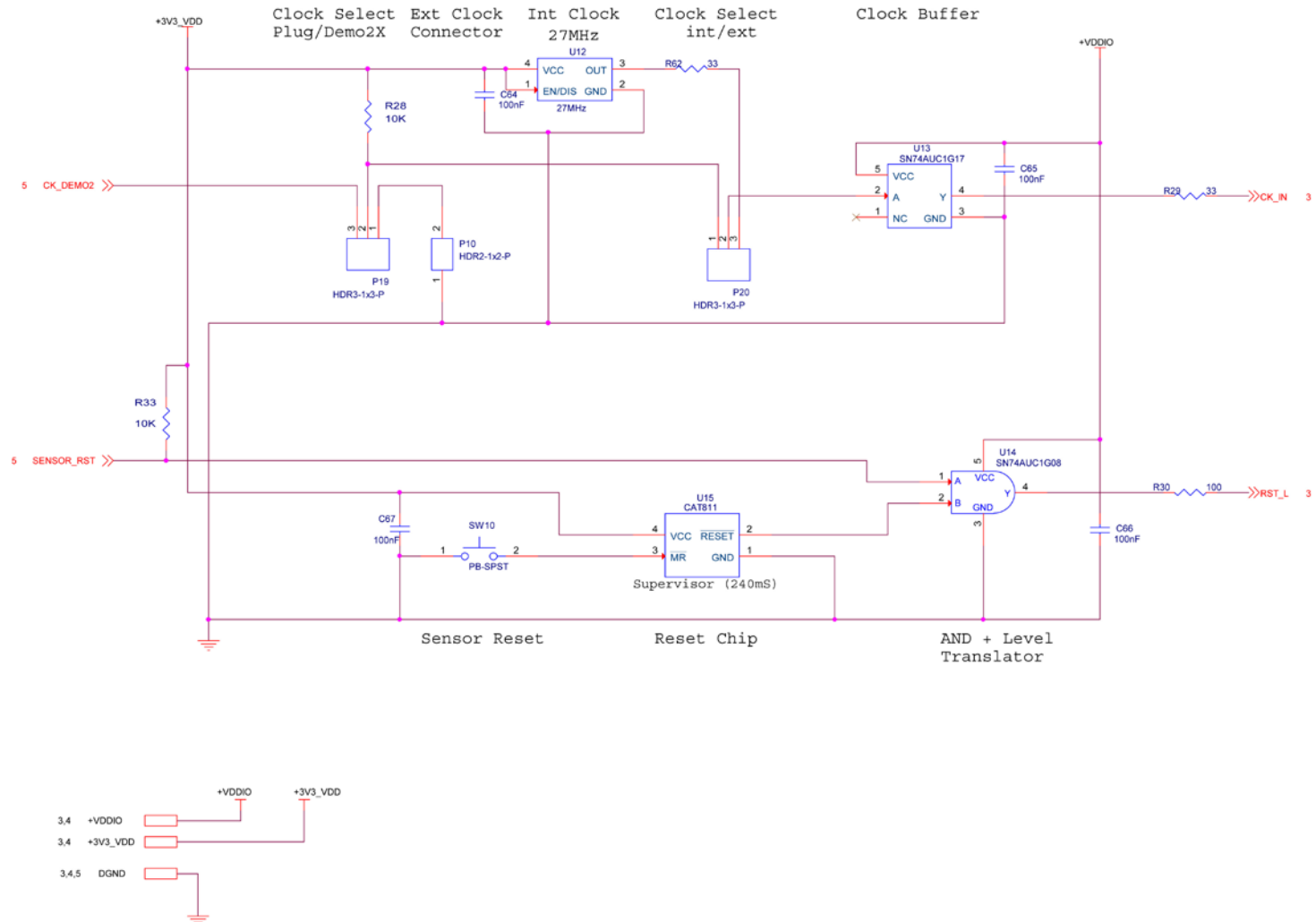


Note on P22:  
 Jumper 2-3 (default)  
 - HiSPi in SLVS mode  
 Jumper 1-2  
 - HiSPi in HiVCM mode



## Headboard Interface





# Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

[onsemi:](#)

[AR0132AT6G00XPEAH3-GEVB](#)