



Schematic for the AR0130CSSM00SPCAH-GEVB Evaluation Board

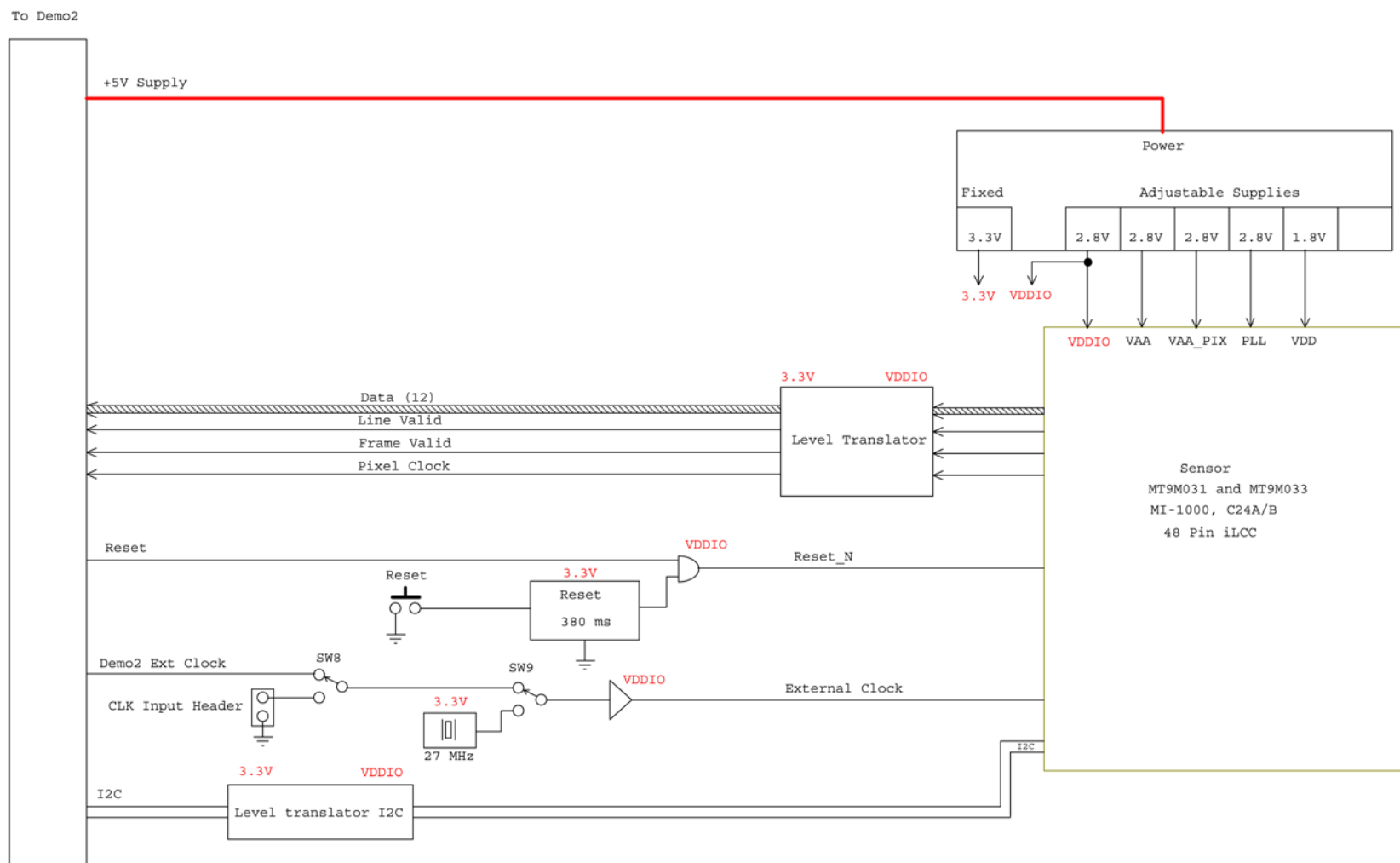
MT9M031 iLCC Parallel and MT9M033 iLCC Parallel

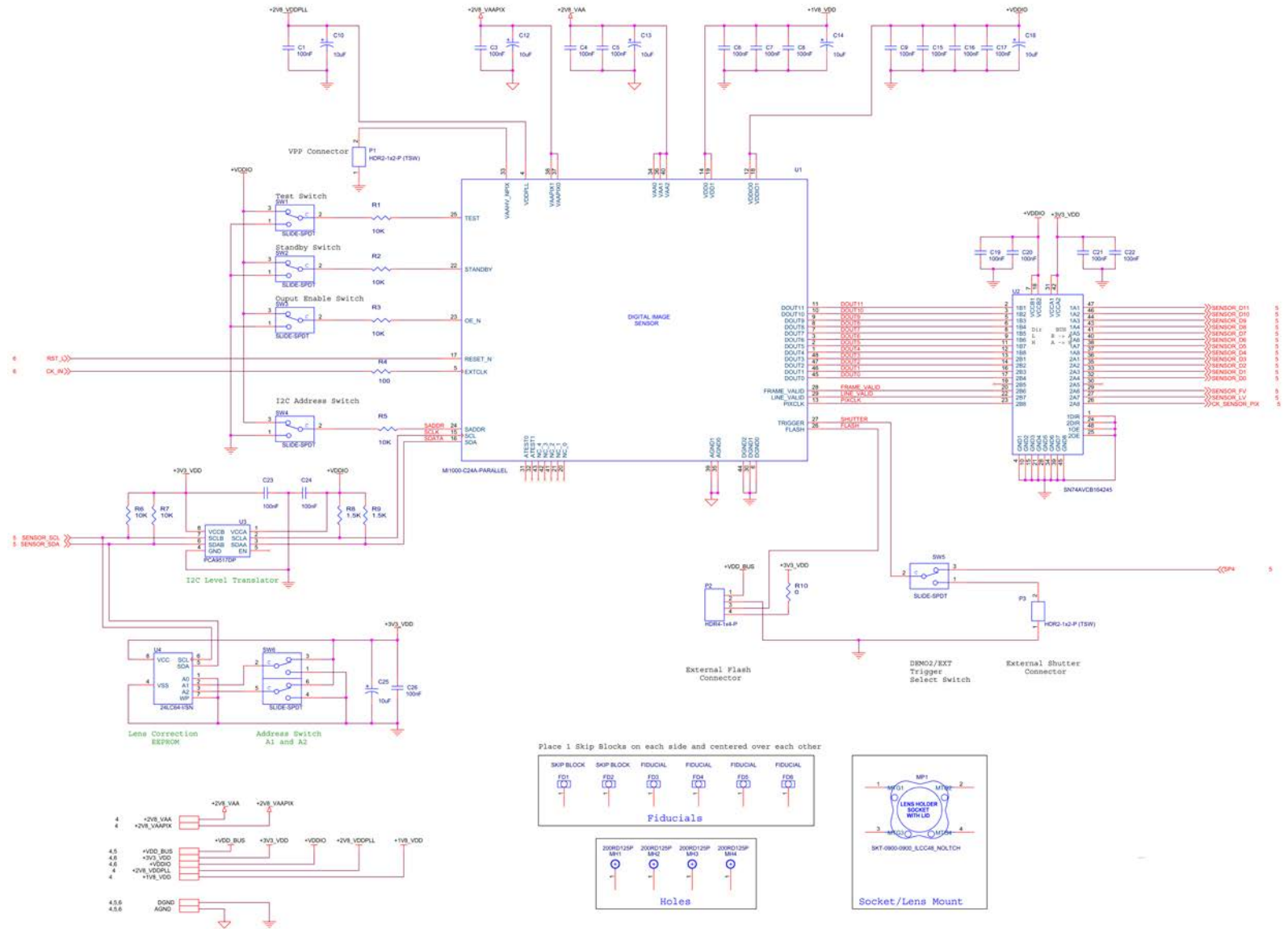
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2	Block Diagram
3	Sensor
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6	Clock and Reset

Rev	Who	Date	Description
Rev 0	PT	04/20/2009	First Release



Block Diagram - MT9M031 ILCC and MT9M033 ILCC PARALLEL

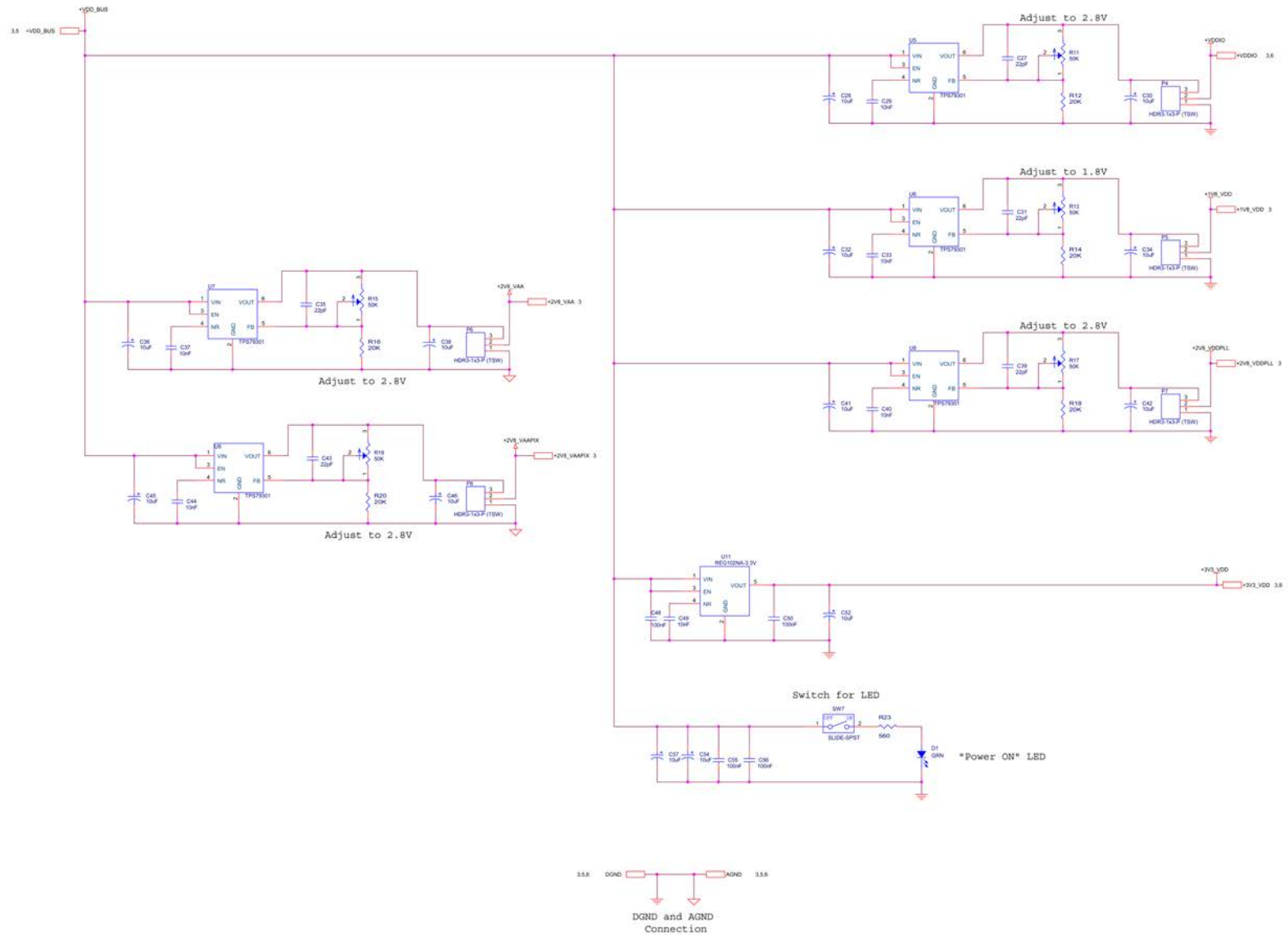






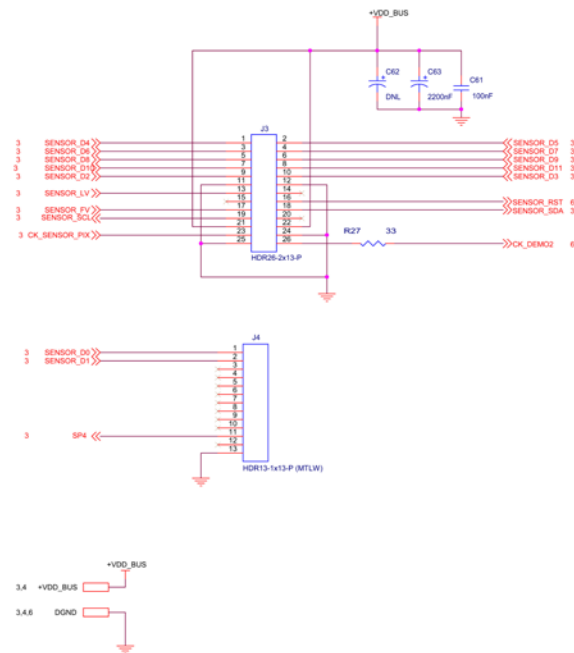
Analog

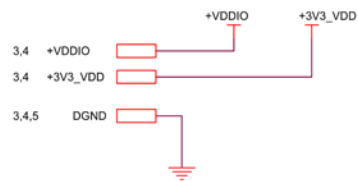
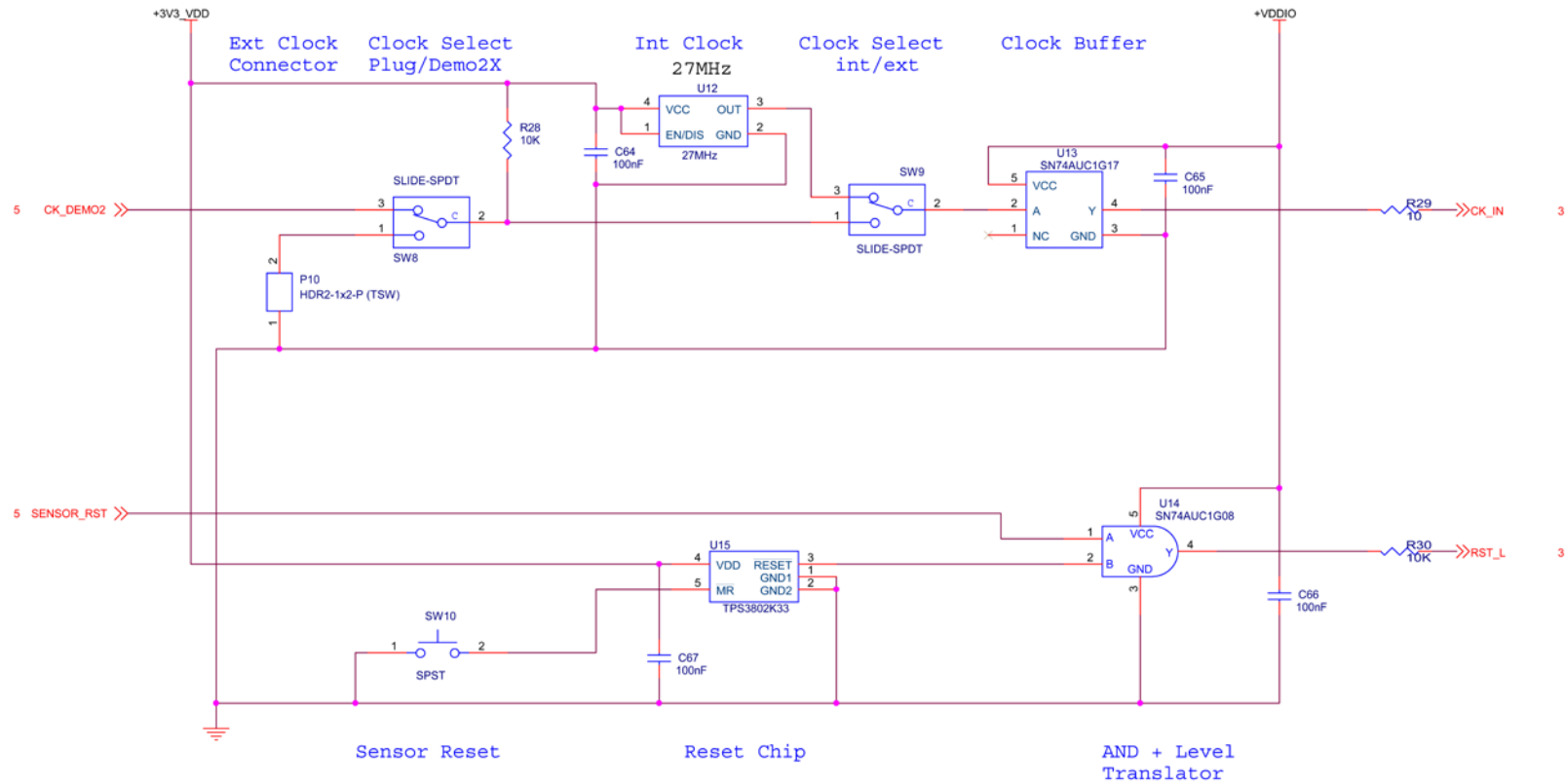
Digital





Headboard Interface





Mouser Electronics

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