

AP0200AT

Image Signal Processor, 2 MP

Product Overview

For complete documentation, see the data sheet.

AP0200AT is a dedicated automotive image co-processor that enables flexible camera platforms using high performance ON Semiconductor megapixel high dynamic range (HDR) sensors. The two-chip solution of sensor and co-processor allows for multiple camera price and performance points with re-use of circuit board design, fast time to market and design flexibility. Sensor performance is enhanced by the reduction in heat resulting from a separate co-processor chip while at the same time enabling high performance features. The AP0200AT offers Ethernet output and supports up to 2 Megapixel sensors. It includes a spatial transfer engine that works with up to 1.2 MP sensors and offers 185-degree fisheye lens distortion correction, perspective correction and multiple view options such as split side view, triptych and trailer hitch. This co-processor works with ON Semiconductor's AR0230AT, AR0132AT, AR0140AT and AR0136AT HDR automotive sensors, and is ideal for Ethernet rear view cameras.

Features

- HDR
- Ethernet

Applications

- Automotive

| Part Electrical Specifications |                   |  |        |                  |       |               |                    |              |
|--------------------------------|-------------------|--|--------|------------------|-------|---------------|--------------------|--------------|
| Product                        | Pricing (\$/Unit) | Compliance   | Status | Frame Rate (fps) | Video | Output Format | Compatible Sensors | Package Type |
| AP0200AT2L00 XEGA0-DR          |                   | <div><div>A</div><div>Pb</div><div>P</div><div>H</div></div> | Active | 30               |       |               |                    | VFPGA-100    |
| AP0200AT2L00 XEGA0-DR1         |                   | <div><div>A</div><div>Pb</div><div>P</div><div>H</div></div> | Active | 30               |       |               |                    | VFPGA-100    |
| AP0200AT2L00 XEGA0-TR          |                   | <div><div>A</div><div>Pb</div><div>P</div><div>H</div></div> | Active | 30               |       |               |                    | VFPGA-100    |

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