



Schematic for the AP0100AT2L00XUGAH3-GEVB Evaluation Board

AP0100 100BGA HB DEMO3 Card

Page	Description
1	Title Page
2	Block Diagram
3	Pinout
4	AP0100 Companion chip
5	Clock, Reset and Auto Config
6	Power
7	BEAGLE Interface and Video Out
8	External Interfaces
9	Configuration Setting

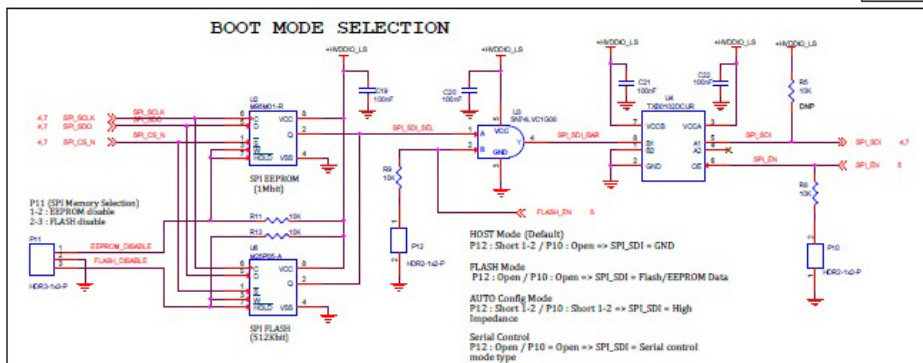
Rev	Who	Date	Description
Rev 0.0	aralex	05/29/2014	Base Schematic for Custom Design
Rev 0.1	aralex	06/23/2014	implemented review comments after online review changed the reference designators in this schematic to align with those of the AP0101 board for ease of layout
	aralex	08/12/2014	deleted P4, P39 headers in order to use 1.2V internal regulator of ISP schematic cleanup
Rev 0.2	aralex	08/21/2014 08/24/2014	changed R44, R45, R46, R47, R53, R54, R60, R61, R62, R63, R69, R70 pkg from 0603 to 0402 Added R98, R67, R95, TP18, TP19
Rev 0.3	aralex	08/25/2014	Deleted R92 being redundant. modified connectivity of R91
Rev 0.4	aralex	08/26/2014	Changed TP13-15, TP18-19 from thru hole to SMT Deleted headers P39, P41, P5 and P8 to consolidate the 4 nets to one new 4 X 2 header, P52



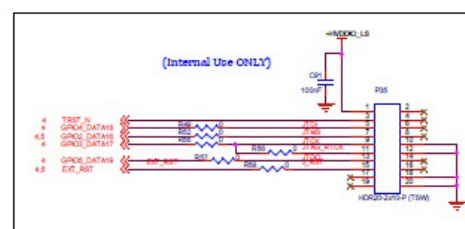
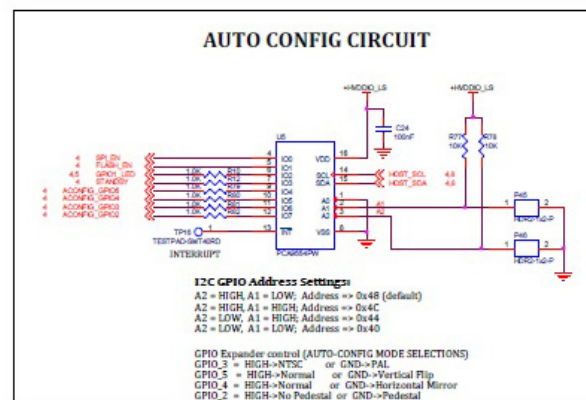
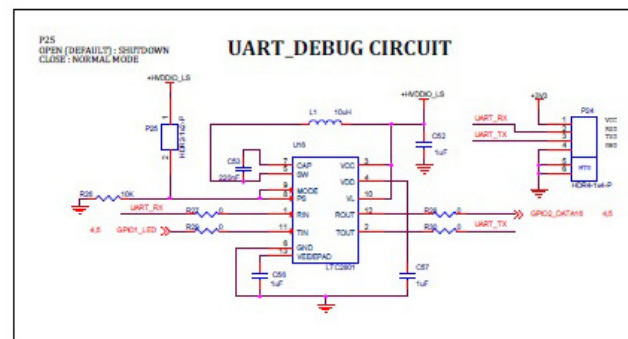


PINOUT

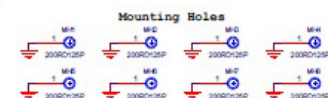
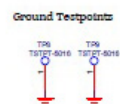
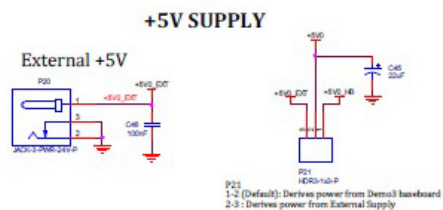
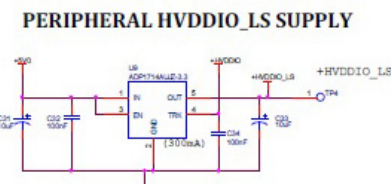
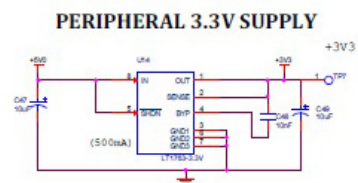
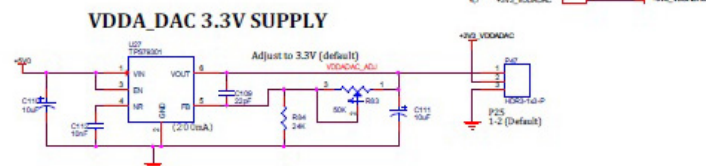
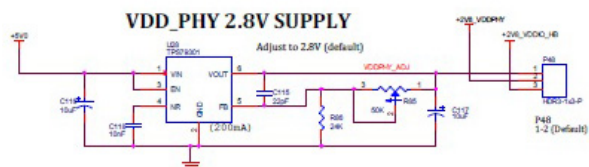
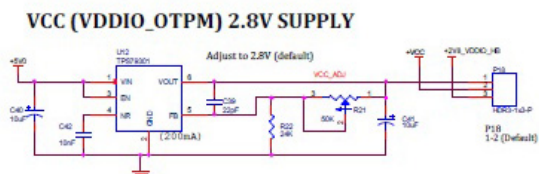
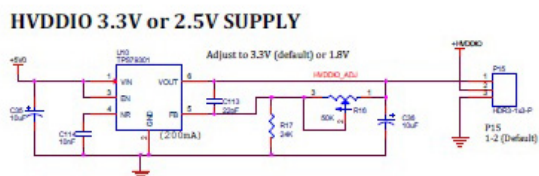
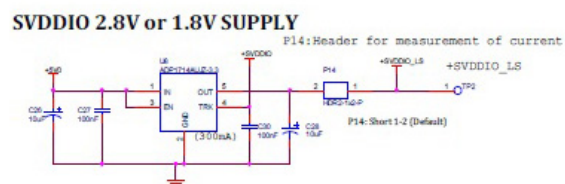
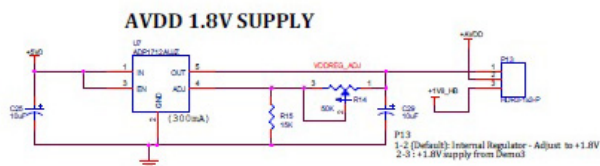
		1	2	3	4	5	6	7	8	9	10
	BALL A1 ID										
A		DDI7H	DDI7J	DDI7K	LAC_VLD	OPD_2	TRST_A	ST_SEI	SWDR	SCN	STANDBY
B		DDI7S	DDI7D	DDI7H	FRAME_VALID	OPD_3	OPD_5	SP_LCLK	SDATA	OPD_0	RESET_A_OUT
C		DDI7B	DDI7R	DDI7S	OPD_1	OPD_4	SP_LCLK_A	SP_SDB	VIDEO_RESET	R_SDATA	M_SCLK
D		DDI7S	DDI7B	DDI7P	VIDEO_RESET	VIDEO_RESET	VBI	FRAME_SYNC	VBI	TV_IN	MOCK_OUT
E		DDI7S	DDI7S	DDI7A	VIDEO_RESET	VBI	VBI	VBI	LV_IN	VIDEO_IN	DM1
F		DDI7D	DDI7H	DDI7B	VIDEO_RESET	VBI	VBI	VBI	VIDEO_SENSOR	DM0	DM2
G		VBI	VBI_PULL_A	XTAL	VBI	VBI	VBI	VBI	DM0	DM1	DM2
H		VBI_PULL_RESET	VBI_PULL_RESET	DDI7B/TPUT	VBI	DAC_HES	DAC_RESET	DACA_CLK	VBI_PHY	DM4	DM5
J		DCT_RESET	RESET_A	RESET	VBI_DAC	DAC_RESET	RESET	RESET	RESET	DM0	DM2
K		RESET	RESET	RESET	VBI_DAC	VBI_DAC	RESET	RESET	RESET	DM0	DM2



6.7 +3V3 +3V3
4.5/7.8 +VDDIO_L3 +VDDIO_L3

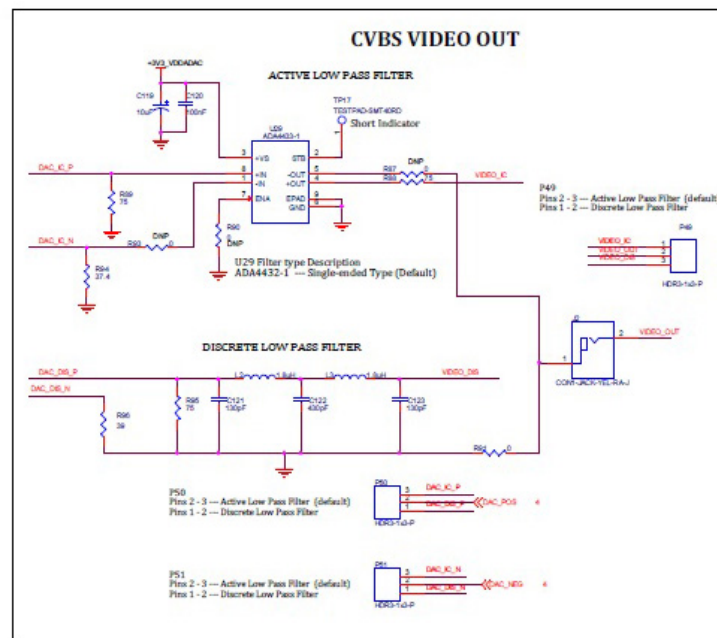
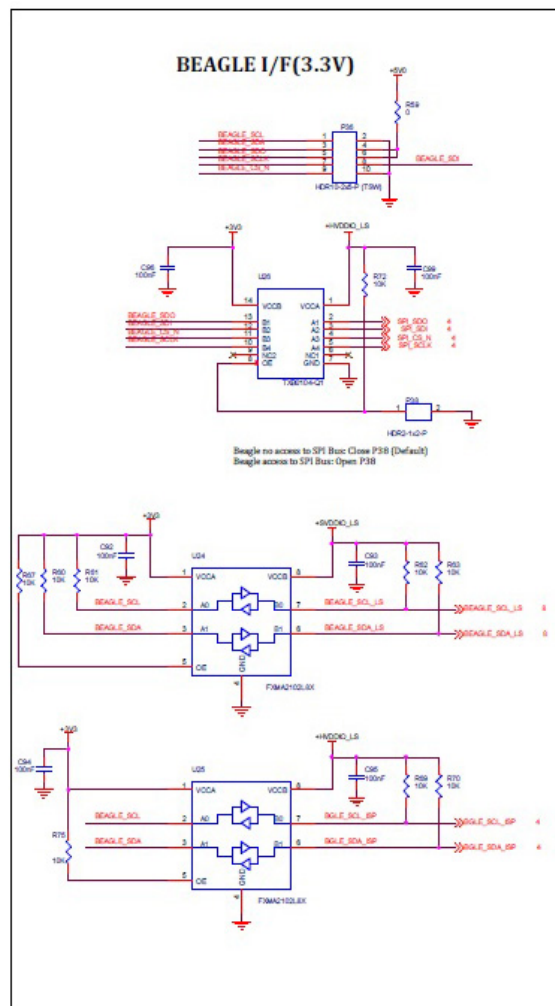


Power

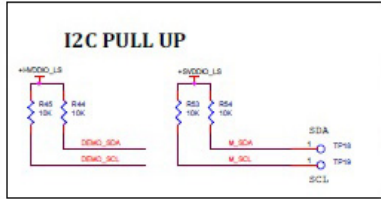
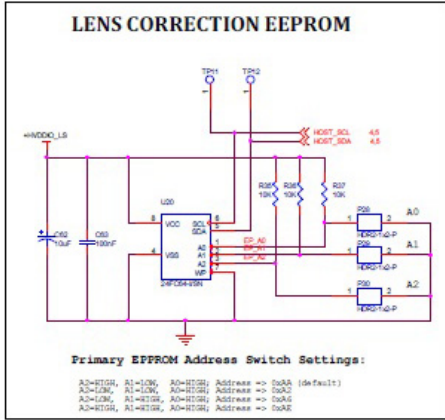
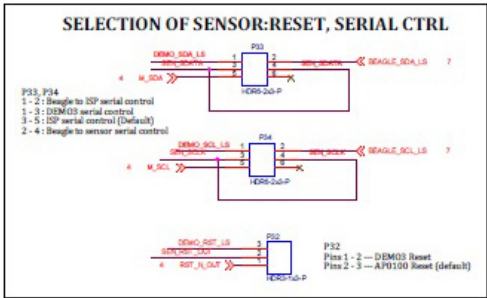
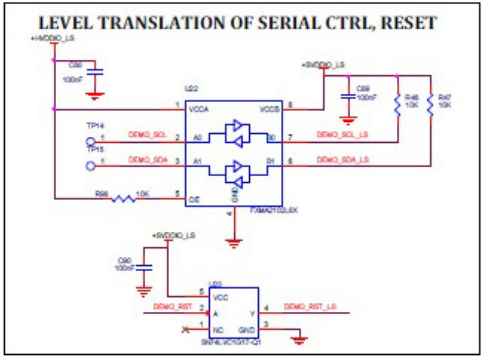
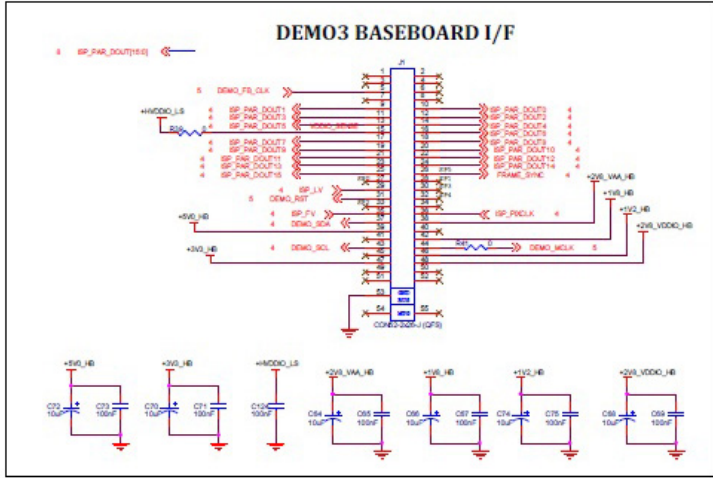
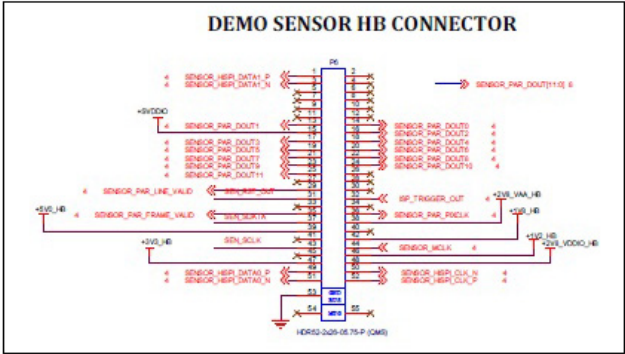




BEAGLE Interface / Video Out



External Interface



Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

[onsemi:](#)

[AP0100AT2L00XUGAH3-GEVB](#)