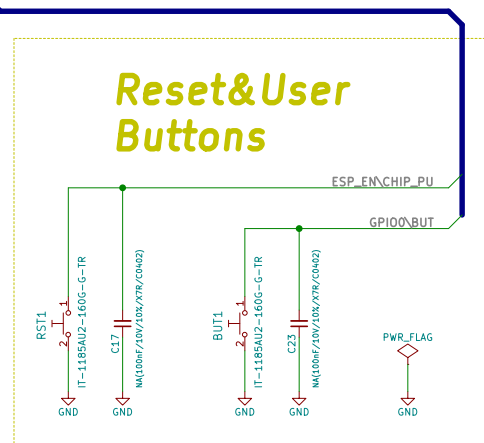
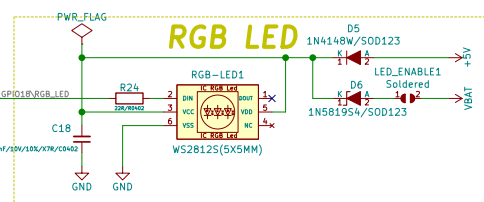
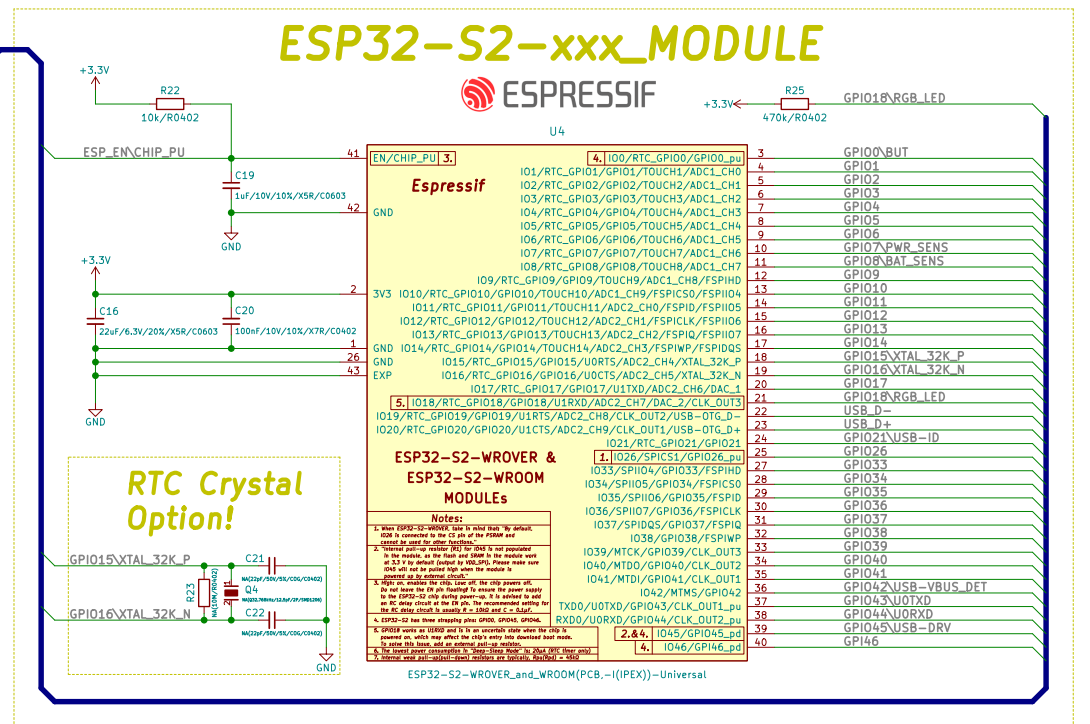
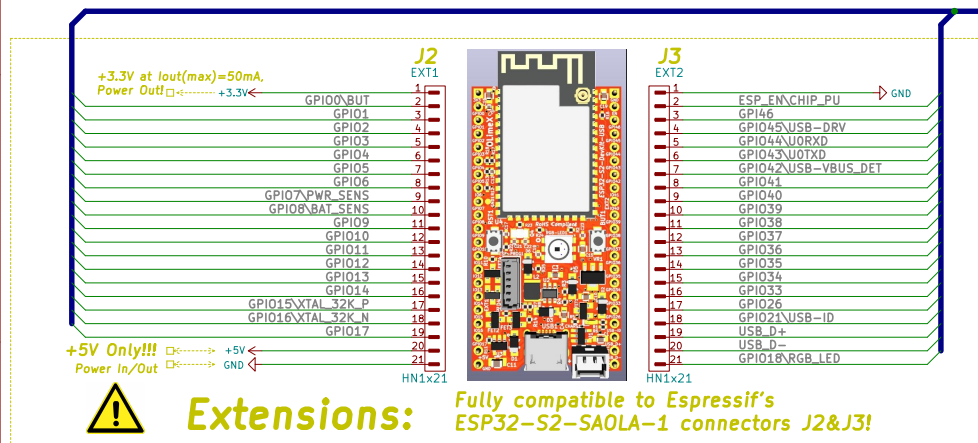
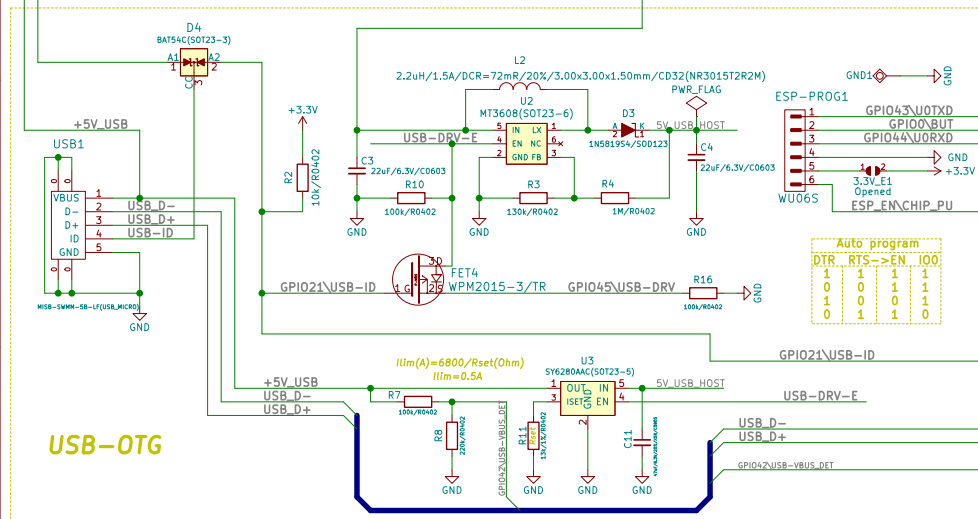




Fiducials:		UART_PRINT Control	
		0	– ROM Code will always print information to UART during boot. GPIO46 is not used.
		1	0 Print is enabled during boot 1 Print is disabled
		2	0 Print is disabled 1 Print is enabled during boot
		3	– Print is always disabled during boot. GPIO46 is not used.

ESP32-S2 has three strapping pins: GPIO0, GPIO16, GPIO46

Voltage of Internal LDO (VDD_SPI Voltage)		Boot Mode (2)
Pin	Default	3.3V
IO45/GPIO45	Pull-Down	0
Pin	Default	1.8V
IO45/GPIO45	Pull-Down	0
Bootstrapping Pins Settings		
Pin	Default	SPI Flash Boot
IO45/GPIO45	Pull-Up	1
IO46/GPIO46	Pull-Down	0
IO46/GPIO46	Pull-Down	Don't care
Enabling/Disabling ROM Code Print During Booting		
Pin	Default	Enabled
IO45/GPIO45	Pull-Down	See the fourth note
IO46/GPIO46	Pull-Down	See the fourth note

Notes:

1. **Firmware can configure register bits to change the settings of "VDD_SPI Voltage".**
2. **Internal pull-up resistor (R4) for IO45 is not populated in the module, as the flash and SRAM in the module will at 3.3V by its output (output by VDD_SPI). Please make sure IO45 will not be pulled high when the module is powered up by external circuit.**
3. **ROM code can be printed over TXD0 (by default) or DAC1_1 (IO17), depending on the eFuse bit.**
4. **When eFuse UART_PRINT_CONTROL value is:**
 - 0, print is normal during boot and not controlled by IO46,
 - 1 and IO46 is 0, print is normal during boot; but if IO46 is 1, print is disabled.
 - 2 and IO46 is 0, print is disabled; but if IO46 is 1, print is normal.
 - 3, print is disabled and not controlled by IO46.



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