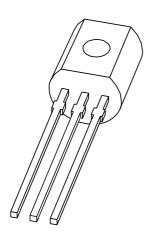
DISCRETE SEMICONDUCTORS

DATA SHEET



2PC1815 NPN general purpose transistor

Product specification Supersedes data of 1999 May 28 2004 Nov 05





NPN general purpose transistor

2PC1815

FEATURES

- Low current (max. 150 mA)
- Low voltage (max. 50 V).

APPLICATIONS

• General purpose switching and amplification, e.g. audio amplifier driver stages.

DESCRIPTION

NPN transistor in a TO-92 (SOT54) plastic package. PNP complement: 2PA1015.

PINNING

| PIN | DESCRIPTION | | | |
|-----|-------------|--|--|--|
| 1 | base | | | |
| 2 | collector | | | |
| 3 | emitter | | | |

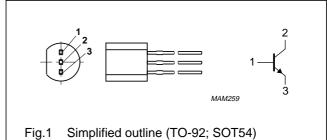


Fig.1 Simplified outline (TO-92; SOT54) and symbol.

ORDERING INFORMATION

| TYPE NUMBER | | PACKAGE | | | |
|--------------|--------------------------|---|-------|--|--|
| ITPE NOWIBER | NAME DESCRIPTION VERSION | | | | |
| 2PC1815 | SC-43A | plastic single-ended leaded (through hole) package; 3 leads | SOT54 | | |

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

| SYMBOL | PARAMETER | PARAMETER CONDITIONS | | | |
|------------------|---------------------------|----------------------------------|-----|------|----|
| V _{CBO} | collector-base voltage | open emitter | _ | 60 | V |
| V _{CEO} | collector-emitter voltage | open base | _ | 50 | V |
| V _{EBO} | emitter-base voltage | open collector | _ | 5 | V |
| I _C | collector current (DC) | | _ | 150 | mA |
| I _{CM} | peak collector current | | _ | 200 | mA |
| I _{BM} | peak base current | | _ | 200 | mA |
| P _{tot} | total power dissipation | T _{amb} ≤ 25 °C; note 1 | _ | 500 | mW |
| T _{stg} | storage temperature | | -65 | +150 | °C |
| T _j | junction temperature | | _ | 150 | °C |
| T _{amb} | ambient temperature | | -65 | +150 | °C |

Note

1. Transistor mounted on an FR4 printed-circuit board.

NPN general purpose transistor

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THERMAL CHARACTERISTICS

| SYMBOL | PARAMETER | CONDITIONS | VALUE | UNIT |
|----------------------|---|------------|-------|------|
| R _{th(j-a)} | thermal resistance from junction to ambient | note 1 | 250 | K/W |

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

| SYMBOL | PARAMETER | CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|--------------------|---|---|------|------|------|------|
| I _{CBO} | collector-base cut-off current $V_{CB} = 60 \text{ V}; I_E = 0 \text{ A}$ | | _ | _ | 100 | nA |
| I _{EBO} | emitter-base cut-off current | V _{EB} = 5 V; I _C = 0 A | _ | _ | 100 | nA |
| h _{FE} | DC current gain | V _{CE} = 6 V; I _C = 150 mA | 25 | _ | - | |
| h _{FE} | DC current gain | V _{CE} = 6 V; I _C = 2 mA | | | | |
| | 2PC1815 | | 120 | - | 700 | |
| | 2PC1815Y | | 120 | - | 240 | |
| | 2PC1815GR | | 200 | - | 400 | |
| | 2PC1815BL | | 350 | _ | 700 | |
| V _{CEsat} | collector-emitter saturation voltage | I _C = 100 mA; I _B = 10 mA | _ | _ | 300 | mV |
| V _{BEsat} | base-emitter saturation voltage | I _C = 100 mA; I _B = 10 mA | _ | _ | 1.1 | V |
| C _c | collector capacitance | $V_{CB} = 10 \text{ V}; I_E = i_e = 0 \text{ A};$ f = 1 MHz | _ | 2.5 | 3.5 | pF |
| f _T | transition frequency | V _{CE} = 6 V; I _C = 1 mA; f = 100 MHz | 80 | _ | _ | MHz |
| F | noise figure | $V_{CE} = 5 \text{ V; } I_{C} = 200 \mu\text{A;}$ $R_{S} = 2 k\Omega; f = 1 k\text{Hz}$ | _ | _ | 10 | dB |

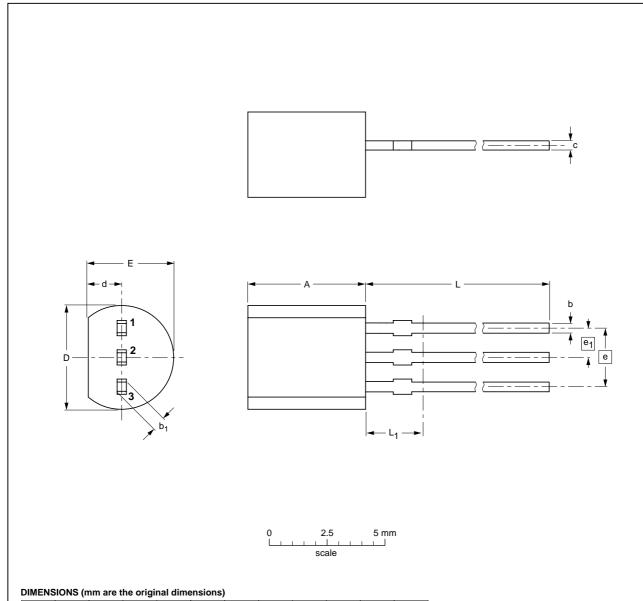
NPN general purpose transistor

2PC1815

PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; 3 leads

SOT54



| UNIT | A | b | b ₁ | С | D | d | E | е | e ₁ | L | L ₁ ⁽¹⁾ max. | |
|------|------------|--------------|----------------|--------------|------------|------------|------------|------|----------------|--------------|---------------------------------------|--|
| mm | 5.2 5.0 | 0.48 0.40 | 0.66 0.55 | 0.45 0.38 | 4.8 4.4 | 1.7 1.4 | 4.2 3.6 | 2.54 | 1.27 | 14.5 12.7 | 2.5 | |

Note

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

| OUTLINE | | REFER | EUROPEAN | ISSUE DATE | | |
|---------|-----|-------|----------|------------|------------|---------------------------------|
| VERSION | IEC | JEDEC | JEITA | | PROJECTION | 1330E DATE |
| SOT54 | | TO-92 | SC-43A | | | 97-02-28 04-06-28 |

NPN general purpose transistor

2PC1815

DATA SHEET STATUS

| LEVEL | DATA SHEET STATUS ⁽¹⁾ | PRODUCT STATUS(2)(3) | DEFINITION |
|-------|-------------------------------------|-------------------------|--|
| I | Objective data | Development | This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice. |
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