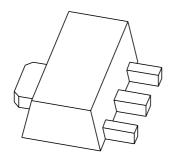
### **DISCRETE SEMICONDUCTORS**

## DATA SHEET



PBSS5480X 80 V, 4 A PNP low V<sub>CEsat</sub> (BISS) transistor

Product data sheet Supersedes data of 2004 Jun 8 2004 Nov 08



## 80 V, 4 A PNP low V<sub>CEsat</sub> (BISS) transistor

### **PBSS5480X**

#### **FEATURES**

- High h<sub>FE</sub> and low V<sub>CEsat</sub> at high current operation
- High collector current I<sub>C</sub>: 4 A
- High efficiency leading to less heat generation.

#### **APPLICATIONS**

- Medium power peripheral drivers (e.g. fans and motors)
- Strobe flash units for digital still cameras and mobile phones
- Inverter applications (e.g. TFT displays)
- · Power switch for LAN and ADSL systems
- Medium power DC-to-DC conversion
- · Battery chargers.

#### **DESCRIPTION**

PNP low V<sub>CEsat</sub> (BISS) transistor in a SOT89 (SC-62) plastic package.

NPN complement: PBSS4480X.

#### **MARKING**

TYPE NUMBER	MARKING CODE(1)
PBSS5480X	*1Z

#### Note

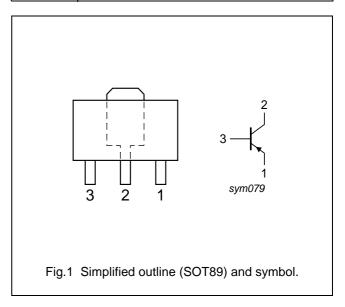
- 1. \* = p: made in Hong Kong.
  - \* = t: made in Malaysia.
  - \* = W: made in China.

#### **QUICK REFERENCE DATA**

SYMBOL	PARAMETER	MAX.	UNIT
$V_{CEO}$	collector-emitter voltage	-80	V
I <sub>C</sub>	collector current (DC)	-4	Α
I <sub>CM</sub>	peak collector current	-10	Α
R <sub>CEsat</sub>	equivalent on-resistance	75	mΩ

#### **PINNING**

PIN	DESCRIPTION
1	emitter
2	collector
3	base



#### **ORDERING INFORMATION**

TYPE NUMBER	PACKAGE			PACKAGE		
THE NOMBER	NAME DESCRIPTION		VERSION			
PBSS5480X	SC-62	plastic surface mounted package; collector pad for good heat transfer; 3 leads	SOT89			

## 80 V, 4 A PNP low $V_{CEsat}$ (BISS) transistor

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#### LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

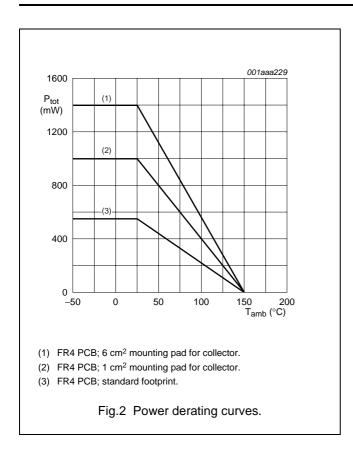
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	-80	V
V <sub>CEO</sub>	collector-emitter voltage	open base	_	-80	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	<b>-</b> 5	V
I <sub>C</sub>	collector current (DC)	note 1	_	-4	Α
I <sub>CM</sub>	peak collector current	$t_p \le 1$ ms or limited by $T_{j(max)}$	-	-10	Α
I <sub>CRP</sub>	repetitive peak collector current	$t_p \leq$ 10 ms; $\delta \leq$ 0.1	_	-6	Α
I <sub>B</sub>	base current (DC)		-	-1	Α
I <sub>BM</sub>	peak base current	$t_p \le 1 \text{ ms}$	-	-2	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C			
		notes 2 and 3	_	2.5	W
		note 3	_	0.55	W
		note 4	_	1	W
		note 1	_	1.4	W
		note 5	_	1.6	W
T <sub>stg</sub>	storage temperature		-65	+150	°C
T <sub>j</sub>	junction temperature		_	150	°C
T <sub>amb</sub>	ambient temperature		-65	+150	°C

#### **Notes**

- 1. Device mounted on a printed-circuit board, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.
- 2. Operated under pulsed conditions; pulse width  $t_p \leq 10$  ms; duty cycle  $\delta \leq 0.1$ .
- 3. Device mounted on a printed-circuit board, single-sided copper, tin-plated, standard footprint.
- 4. Device mounted on a printed-circuit board, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.
- 5. Device mounted on a 7 cm<sup>2</sup> ceramic printed-circuit board, 1 cm<sup>2</sup> single-sided copper, tin-plated.

## 80 V, 4 A PNP low $V_{CEsat}$ (BISS) transistor

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## 80 V, 4 A PNP low V<sub>CEsat</sub> (BISS) transistor

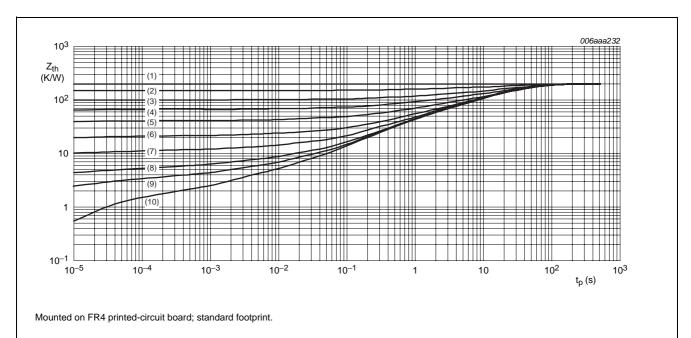
PBSS5480X

#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air		
		notes 1 and 2	50	K/W
		note 2	225	K/W
		note 3	125	K/W
		note 4	90	K/W
		note 5	80	K/W
R <sub>th(j-s)</sub>	thermal resistance from junction to soldering point		16	K/W

#### **Notes**

- 1. Operated under pulsed conditions; pulse width  $t_p \le 10$  ms; duty cycle  $\delta \le 0.2$ .
- 2. Device mounted on a printed-circuit board, single-sided copper, tin-plated, standard footprint.
- 3. Device mounted on a printed-circuit board, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>.
- 4. Device mounted on a printed-circuit board, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.
- 5. Device mounted on a 7 cm<sup>2</sup> ceramic printed-circuit board, 1 cm<sup>2</sup> single-sided copper, tin-plated.



- (1)  $\delta = 1$ .
- (3)  $\delta = 0.5$ .
- (5)  $\delta = 0.2$ .
- (7)  $\delta = 0.05$ .
- (9)  $\delta = 0.01$ .

- (2)  $\delta = 0.75$ .
- (4)  $\delta = 0.33$ .
- (6)  $\delta = 0.1$ .
- (8)  $\delta = 0.02$ .
- (10)  $\delta = 0$ .

Fig.3 Transient thermal impedance as a function of pulse time; typical values.

## 80 V, 4 A PNP low $V_{CEsat}$ (BISS) transistor

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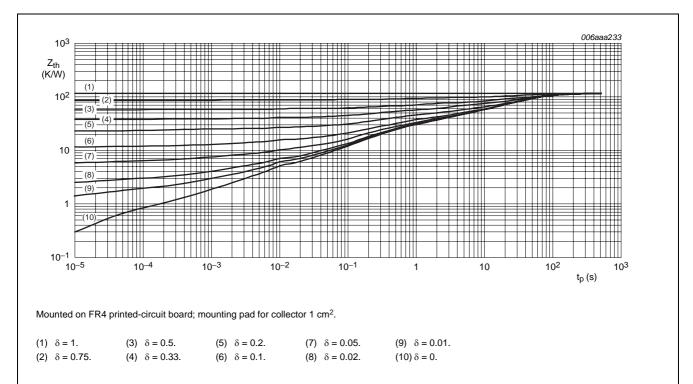
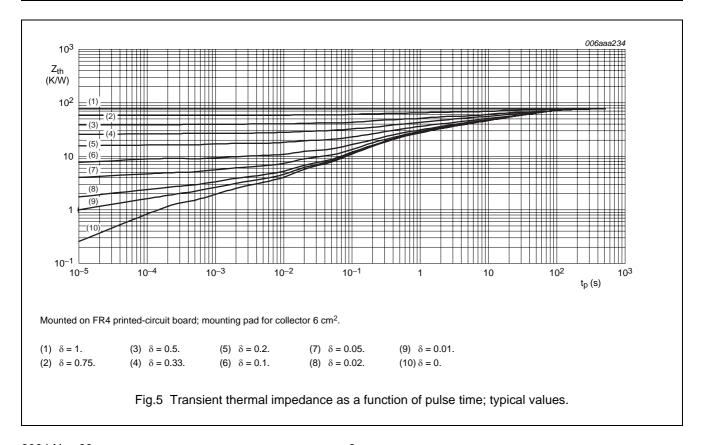


Fig.4 Transient thermal impedance as a function of pulse time; typical values.



# 80 V, 4 A PNP low $V_{CEsat}$ (BISS) transistor

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#### **CHARACTERISTICS**

 $T_{amb}$  = 25 °C unless otherwise specified.

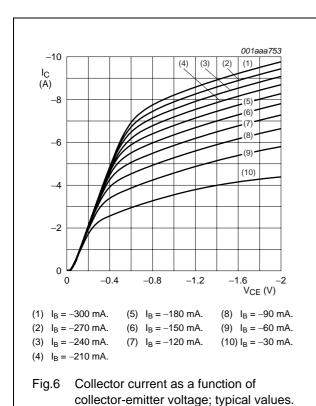
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector-base cut-off current	$V_{CB} = -80 \text{ V}; I_E = 0 \text{ A}$	-	_	-100	nA
		$V_{CB} = -80 \text{ V}; I_E = 0 \text{ A}; T_j = 150 ^{\circ}\text{C}$	-	_	-50	μΑ
I <sub>CES</sub>	collector-emitter cut-off current	$V_{CE} = -60 \text{ V}; V_{BE} = 0 \text{ V}$	-	_	-100	nA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; I_C = 0 \text{ A}$	-	-	-100	nA
h <sub>FE</sub>	DC current gain	$V_{CE} = -2 \text{ V}; I_{C} = -0.5 \text{ A}$	200	300	_	
		$V_{CE} = -2 \text{ V}; I_{C} = -1 \text{ A}; \text{ note } 1$	180	280	_	
		$V_{CE} = -2 \text{ V}; I_{C} = -2 \text{ A}; \text{ note 1}$	150	240	_	
		$V_{CE} = -2 \text{ V}; I_{C} = -4 \text{ A}; \text{ note 1}$	80	150	_	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = -0.5 \text{ A}; I_B = -50 \text{ mA}$	-	-35	<b>-55</b>	mV
		$I_C = -1 \text{ A}; I_B = -50 \text{ mA}$	-	-70	-105	mV
		$I_C = -2 \text{ A}; I_B = -40 \text{ mA}$	_	-170	-250	mV
		$I_C = -4 \text{ A}$ ; $I_B = -200 \text{ mA}$ ; note 1	_	-220	-340	mV
		$I_C = -5 \text{ A}$ ; $I_B = -500 \text{ mA}$ ; note 1	-	-250	-380	mV
R <sub>CEsat</sub>	equivalent on-resistance	$I_C = -5 \text{ A}$ ; $I_B = -500 \text{ mA}$ ; note 1	_	50	75	mΩ
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_C = -0.5 \text{ A}; I_B = -50 \text{ mA}$	_	-770	-850	mV
		$I_C = -1 \text{ A}; I_B = -50 \text{ mA}$	_	-810	-900	mV
		$I_C = -1 \text{ A}$ ; $I_B = -100 \text{ mA}$ ; note 1	_	-810	-900	mV
		$I_C = -4 \text{ A}$ ; $I_B = -400 \text{ mA}$ ; note 1	_	-930	-1000	mV
$V_{BEon}$	base-emitter turn-on voltage	$V_{CE} = -2 \text{ V}; I_{C} = -2 \text{ A}$	_	-760	-850	mV
f⊤	transition frequency	$I_C = -0.1 \text{ A}; V_{CE} = -10 \text{ V};$ f = 100 MHz	100	125	_	MHz
C <sub>c</sub>	collector capacitance	$V_{CB} = -10 \text{ V}; I_E = i_e = 0 \text{ A};$ f = 1 MHz	_	60	90	pF

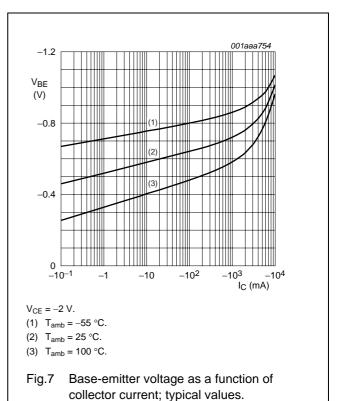
#### Note

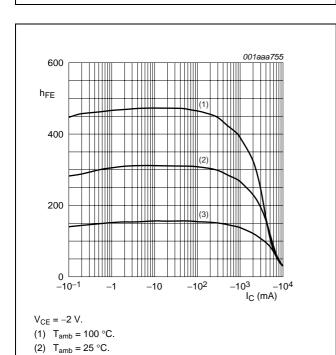
1. Pulse test:  $t_p \le 300~\mu s;~\delta \le 0.02.$ 

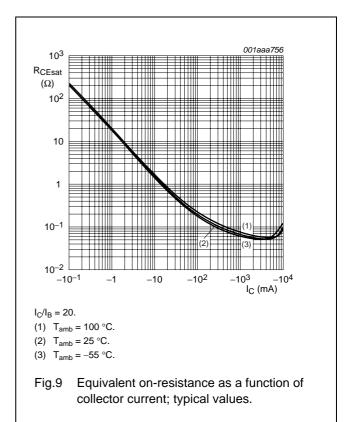
## 80 V, 4 A PNP low $V_{CEsat}$ (BISS) transistor

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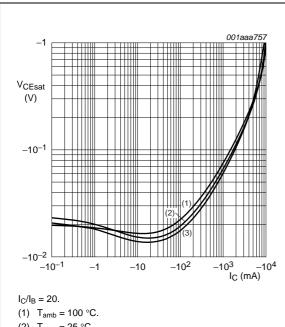
Fig.8 DC current gain as a function of collector

current; typical values.

(3)  $T_{amb} = -55 \, ^{\circ}C$ .

### 80 V, 4 A PNP low V<sub>CEsat</sub> (BISS) transistor

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- (2)  $T_{amb} = 25 \, ^{\circ}C$ .
- (3)  $T_{amb} = -55 \, ^{\circ}C$ .

Fig.10 Collector-emitter saturation voltage as a function of collector current; typical values.

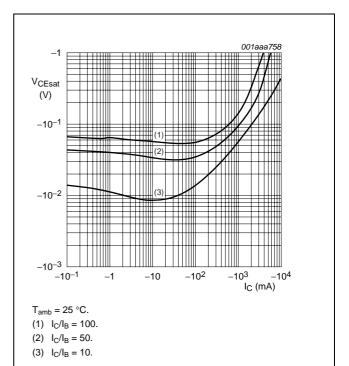
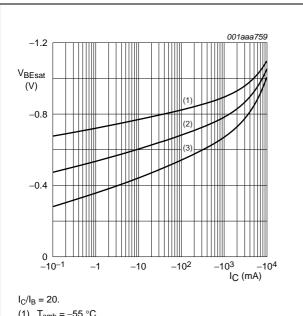


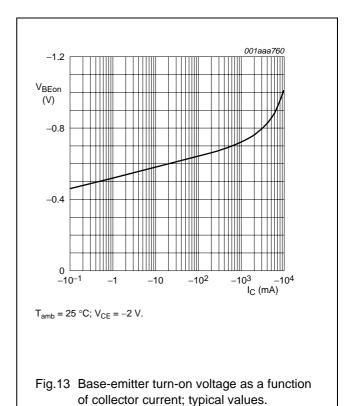
Fig.11 Collector-emitter saturation voltage as a

function of collector current; typical values.



- (1)  $T_{amb} = -55 \, ^{\circ}C$ .
- (2)  $T_{amb} = 25 \, ^{\circ}C$ .
- (3)  $T_{amb} = 100 \, ^{\circ}C$ .

Fig.12 Base-emitter saturation voltage as a function of collector current; typical values.

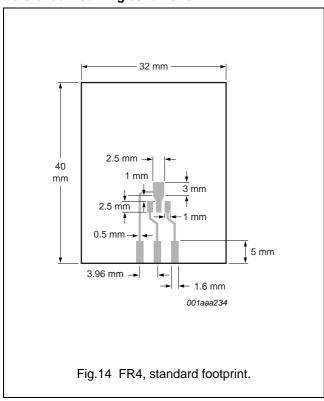


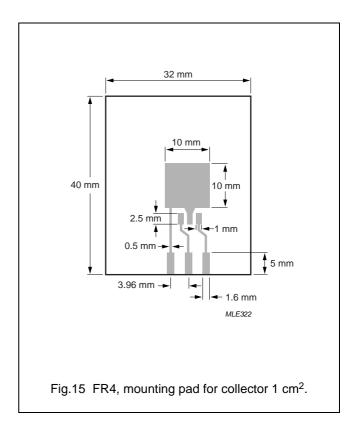
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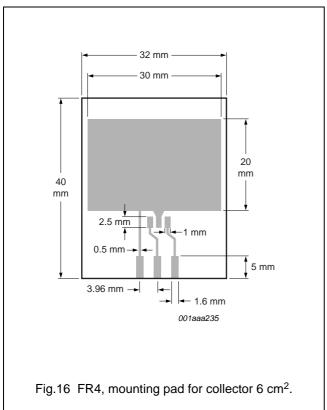
## 80 V, 4 A PNP low $V_{CEsat}$ (BISS) transistor

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#### Reference mounting conditions







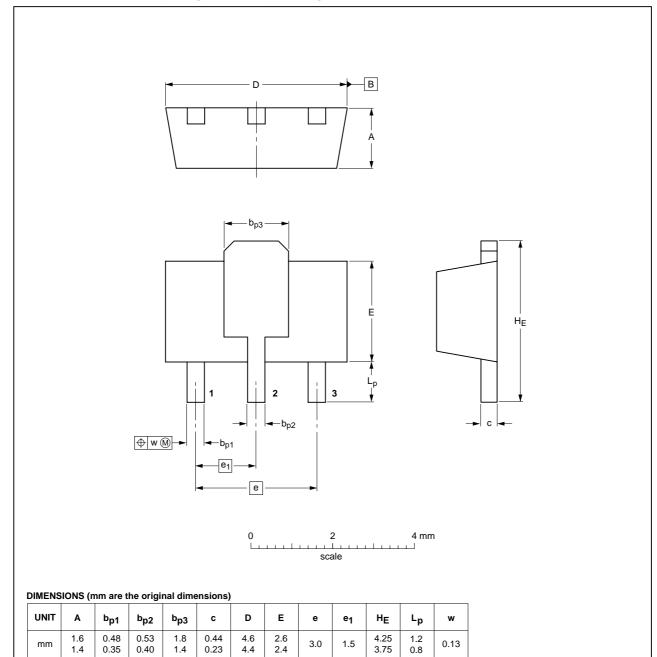
80 V, 4 A PNP low  $V_{CEsat}$  (BISS) transistor

PBSS5480X

#### **PACKAGE OUTLINE**

#### Plastic surface-mounted package; collector pad for good heat transfer; 3 leads

SOT89



OUTLINE	REFERENCES		EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT89		TO-243	SC-62			<del>04-08-03</del> 06-03-16

### 80 V, 4 A PNP low V<sub>CEsat</sub> (BISS) transistor

PBSS5480X

#### **DATA SHEET STATUS**

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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