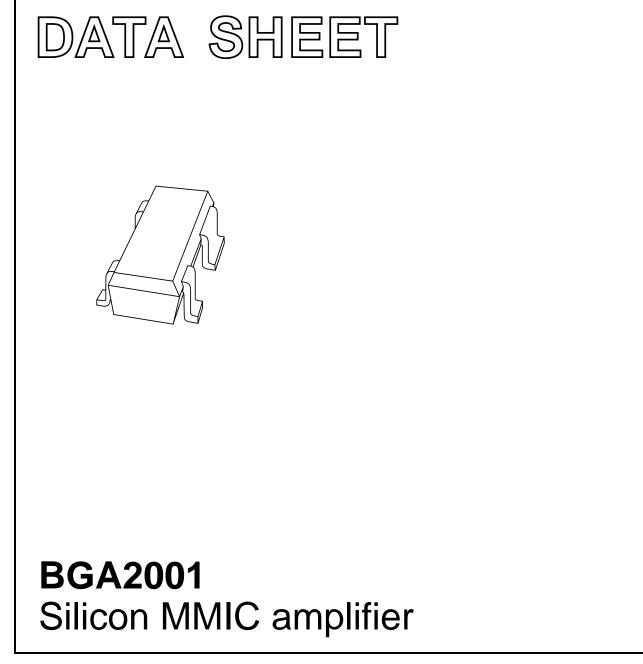
DISCRETE SEMICONDUCTORS



Product specification Supersedes data of 1999 Jul 23 1999 Aug 11



FEATURES

- Low current, low voltage
- Very high power gain
- Low noise figure
- Integrated temperature compensated biasing
- Supply and RF output pin combined.

APPLICATIONS

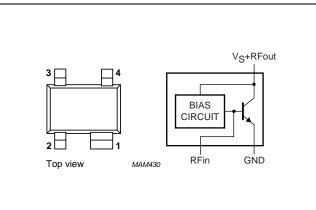
- RF front end
- Wideband applications, e.g. analog and digital cellular telephones, cordless telephones (PHS, DECT, etc.)
- Radar detectors
- Low noise amplifiers
- Satellite television tuners (SATV)
- High frequency oscillators.

DESCRIPTION

Silicon MMIC amplifier consisting of an NPN double polysilicon transistor with integrated biasing for low voltage applications in a plastic, 4-pin dual-emitter SOT343R package.

PINNING

PIN	DESCRIPTION
1	GND
2	RF in
3	GND
4	V _S + RFout



Marking code: A1.

Fig.1 Simplified outline (SOT343R) and symbol.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
Vs	DC supply voltage	RF input AC coupled	-	4.5	V
I _S	DC supply current	V _{VS-OUT} = 2.5 V; RF input AC coupled	4.5	-	mA
MSG	maximum stable gain	V _{VS-OUT} = 2.5 V; f = 1.8 GHz; T _{amb} = 25 °C	19.5	_	dB
NF	noise figure	V_{VS-OUT} = 2.5 V; f = 1.8 GHz; $\Gamma_S = \Gamma_{opt}$	1.3	-	dB

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LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Vs	supply voltage	RF input AC coupled	_	4.5	V
I _S	supply current (DC)	forced by DC voltage on RF input	-	30	mA
P _{tot}	total power dissipation	$T_s \le 100 \ ^{\circ}C$	-	135	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	operating junction temperature		_	150	°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	VALUE	UNIT
R _{th j-s}	thermal resistance from junction to soldering point	350	K/W

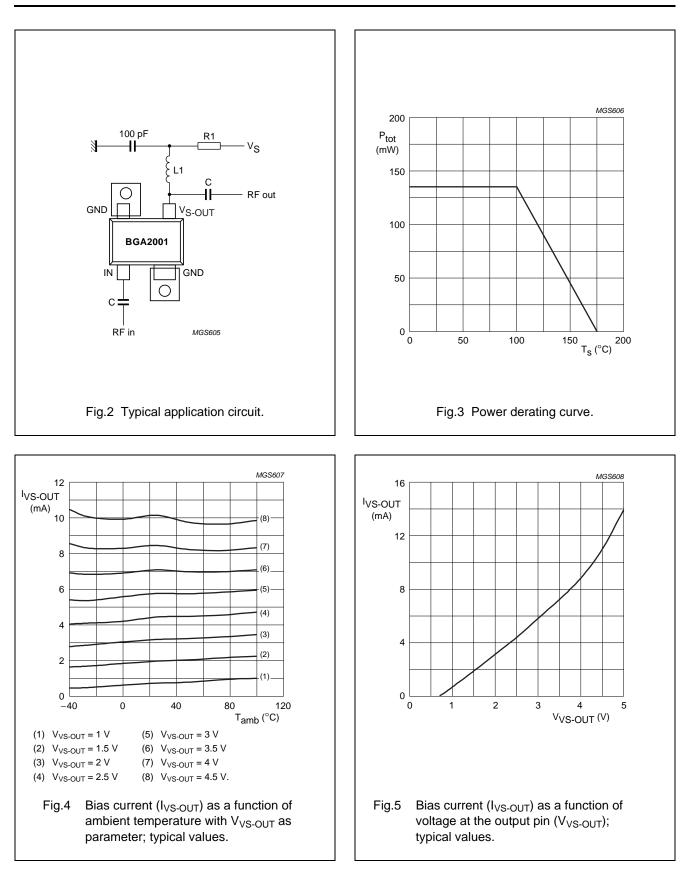
CHARACTERISTICS

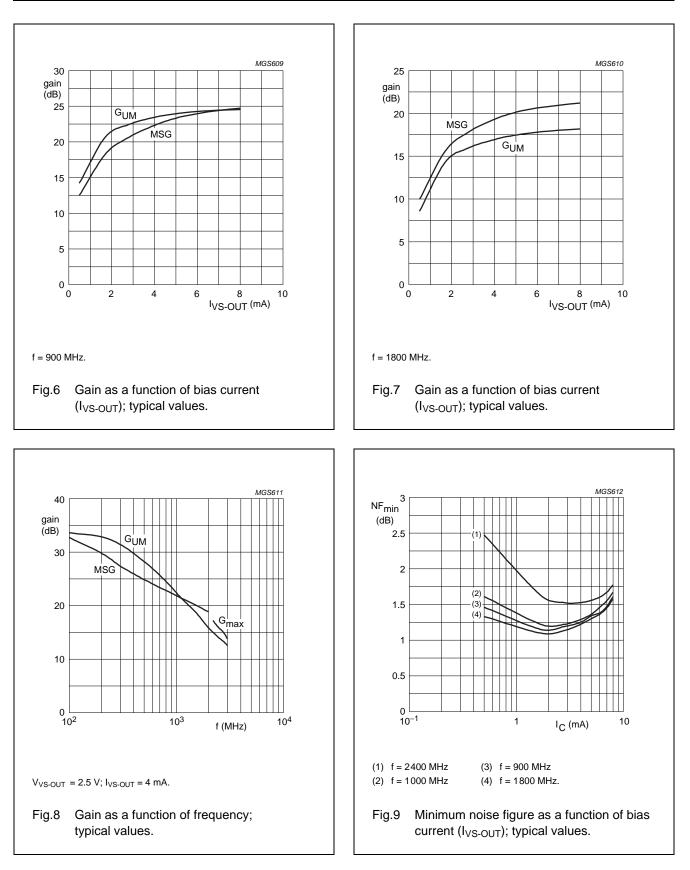
RF input AC coupled; $T_j = 25 \text{ °C}$; unless otherwise specified.

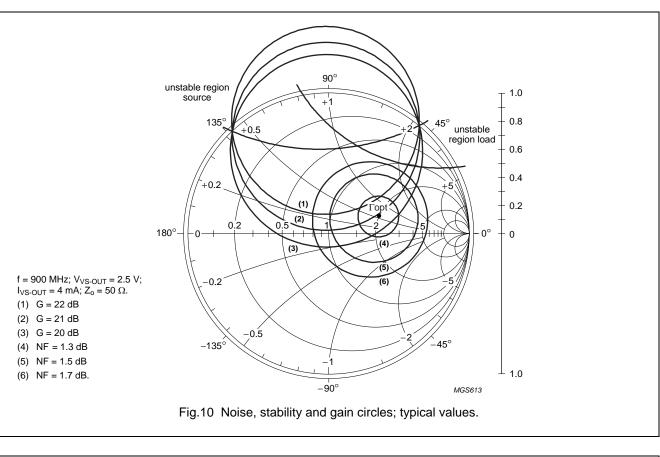
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _S	supply current	V _{VS-OUT} = 1 V	_	0.7	-	mA
		$V_{VS-OUT} = 2.5 V$	3	4.5	6	mA
		$V_{VS-OUT} = 4.5 V$	-	11	-	mA
MSG	maximum stable gain	V _{VS-OUT} = 2.5 V; I _{VS-OUT} = 4 mA; f = 900 MHz	-	22	-	dB
		V _{VS-OUT} = 2.5 V; I _{VS-OUT} = 4 mA; f = 1.8 GHz	-	19.5	-	dB
s ₂₁ ²	insertion power gain	V _{VS-OUT} = 2.5 V; I _{VS-OUT} = 4 mA; f = 900 MHz	-	18	-	dB
		$V_{VS-OUT} = 2.5 V;$ $I_{VS-OUT} = 4 mA; f = 1.8 GHz$	-	14	-	dB
PL	load power	at 1 dB gain compression point; $V_{VS-OUT} = 2.5 V$; $I_{VS-OUT} = 4.4 mA$; f = 900 MHz;	-	-2	-	dBm
NF	noise figure	$V_{VS-OUT} = 2.5 V;$ $I_{VS-OUT} = 4 \text{ mA}; \text{ f} = 900 \text{ MHz};$ $\Gamma_S = \Gamma_{opt}$	-	1.3	-	dB
		$\label{eq:VVS-OUT} \begin{array}{l} V_{\text{VS-OUT}} = 2.5 \text{ V};\\ I_{\text{VS-OUT}} = 4 \text{ mA}; \text{ f} = 1.8 \text{ GHz};\\ \boldsymbol{\Gamma}_{\text{S}} = \boldsymbol{\Gamma}_{\text{opt}} \end{array}$	-	1.3	-	dB
IP3 _(in)	input intercept point; note 1	V _{VS-OUT} = 2.5 V; I _{VS-OUT} = 4.4 mA; f = 900 MHz	-	-7.4	-	dBm
		$V_{VS-OUT} = 2.5 V;$ $I_{VS-OUT} = 4.5 mA; f = 1800 MHz$	-	-4.5	-	dBm

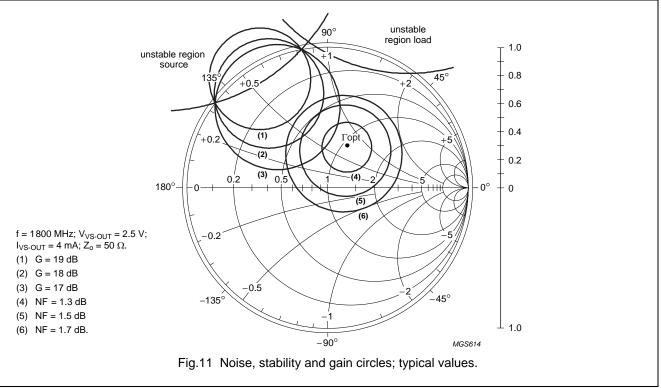
Note

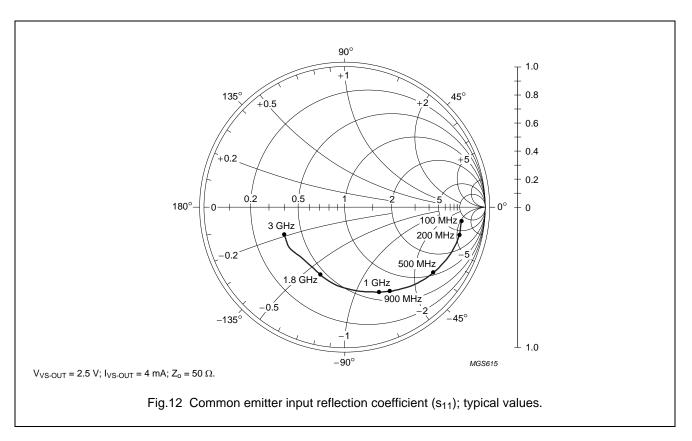
1. See application note: RNR-T45-99-B-0513.

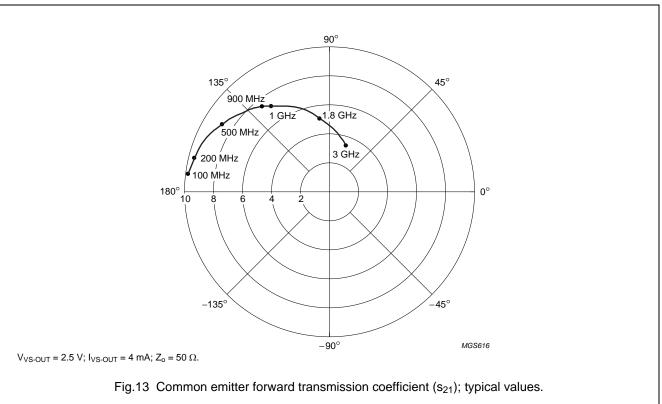


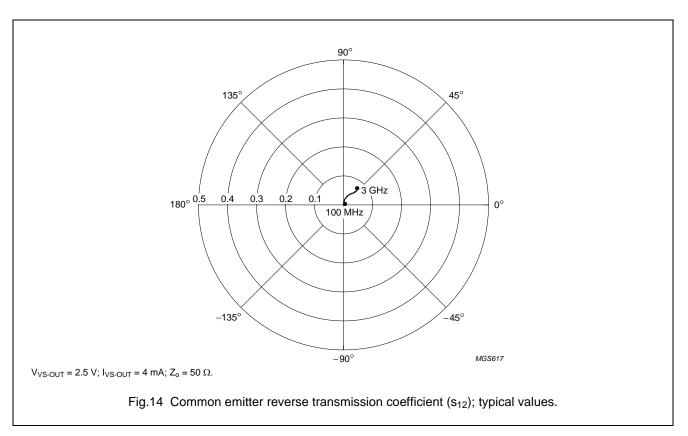


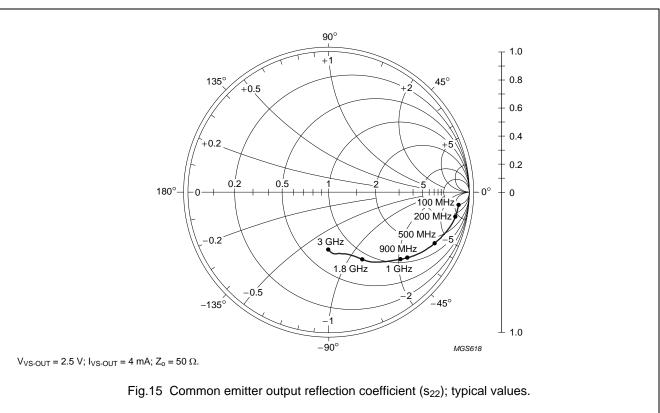








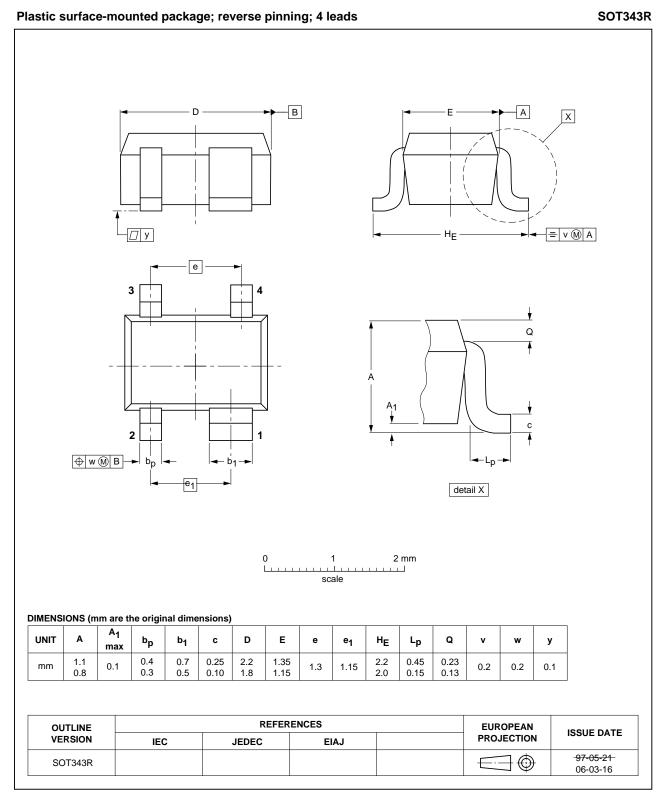




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Silicon MMIC amplifier

PACKAGE OUTLINE



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DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

DATA SHEET STATUS

Notes

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Contact information

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