

N-channel junction FETs Rev. 5 — 15 September 2011

Product data sheet

#### 1. **Product profile**

## **1.1 General description**

N-channel symmetrical junction field effect transistors in a SOT23 package.

#### CAUTION



The device is supplied in an antistatic package. The gate-source input must be protected against static discharge during transport or handling.

Low input capacitance

Low noise.

### 1.2 Features and benefits

- High transfer admittance
- Low feedback capacitance

### 1.3 Applications

Preamplifiers for AM tuners in car radios.

### 1.4 Quick reference data

Table 1.	Quick reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>DS</sub>	drain-source voltage (DC)		-	-	25	V
I <sub>DSS</sub>	drain current					
	BF861A	$V_{GS} = 0 V; V_{DS} = 8 V$	2	-	6.5	mA
	BF861B	$V_{GS} = 0 V; V_{DS} = 8 V$	6	-	15	mA
	BF861C	$V_{GS} = 0 V; V_{DS} = 8 V$	12	-	25	mA
P <sub>tot</sub>	total power dissipation	up to $T_{amb} = 25 \ ^{\circ}C$	-	-	250	mW
y <sub>fs</sub>	forward transfer admittance;					
	BF861A	$V_{GS} = 0 V; V_{DS} = 8 V$	12	-	20	mS
	BF861B	$V_{GS} = 0 V; V_{DS} = 8 V$	16	-	25	mS
	BF861C	$V_{GS} = 0 V; V_{DS} = 8 V$	20	-	30	mS
C <sub>iss</sub>	input capacitance	f = 1 MHz	-	-	10	pF
C <sub>rss</sub>	reverse transfer capacitance	f = 1 MHz	-	-	2.7	pF



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## 2. Pinning information

Table 2.	Discrete pinning	
Pin	Description	Simplified outline Symbol
1	source	
2	drain	
3	gate	sym053

## 3. Ordering information

Table 3.	Ordering in	formation				
Туре	Package	ige				
number	Name	Description	Version			
BF861A	-	plastic surface mounted package; 3 leads	SOT23			
BF861B	-	plastic surface mounted package; 3 leads	SOT23			
BF861C	-	plastic surface mounted package; 3 leads	SOT23			

## 4. Marking

Type number	Marking code <sup>[1]</sup>
BF861A	28*
BF861B	29*
BF861C	30*

[1] \* = p: Made in Hong Kong.

\* = t: Made in Malaysia.

\* = W: Made in China.

## 5. Limiting values

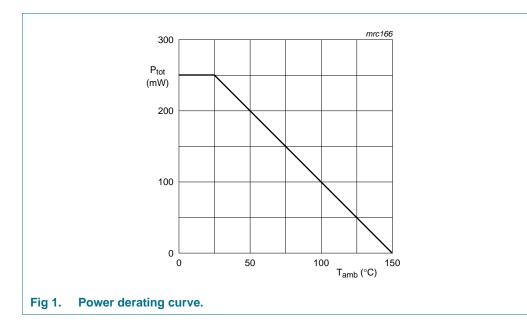
#### Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>DS</sub>	drain-source voltage (DC)		-	25	V
V <sub>GSO</sub>	gate-source voltage	open drain	-	25	V
V <sub>DGO</sub>	drain-gate voltage (DC)	open source	-	25	V
l <sub>G</sub>	forward gate current (DC)		-	10	mA
P <sub>tot</sub>	total power dissipation	up to $T_{amb} = 25 \ ^{\circ}C$	<u>[1]</u> _	250	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	operating junction temperature		-	150	°C

[1] Device mounted on an FR4 printed-circuit board.

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# 6. Thermal characteristics

Table 6.	Thermal characteristics			
Symbol	Parameter	Conditions	Тур	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient		<u>[1]</u> 500	K/W

[1] Device mounted on an FR4 printed-circuit board.

## 7. Characteristics

#### Table 7. Characteristics

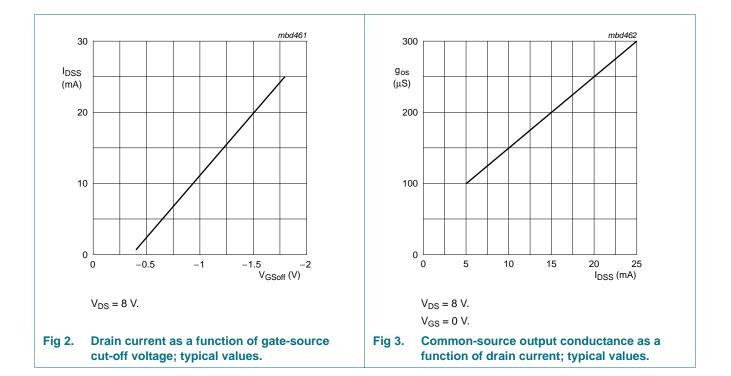
 $T_i = 25 \text{ °C}; V_{DS} = 8 \text{ V}; V_{GS} = 0 \text{ V}$  unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>(BR)GSS</sub>	gate-source breakdown voltage	$I_G = -1 \ \mu A$	-25	-	-	V
V <sub>GSoff</sub>	gate-source cut-off voltage					
	BF861A	$I_D = 1 \ \mu A$	-0.2	-	-1	V
	BF861B	$I_D = 1 \ \mu A$	-0.5	-	-1.5	V
	BF861C	I <sub>D</sub> = 1 μA	-0.8	-	-2	V
V <sub>GSS</sub>	gate-source forward voltage	$V_{DS} = 0 V; I_G = 1 mA$	-	-	1	V
I <sub>DSS</sub>	drain current					
	BF861A		2	-	6.5	mA
	BF861B		6	-	15	mA
	BF861C		12	-	25	mA
I <sub>GSS</sub>	gate cut-off current	V <sub>GS</sub> = -20 V; V <sub>DS</sub> = 0 V	-	-	-1	nA

### **N-channel junction FETs**

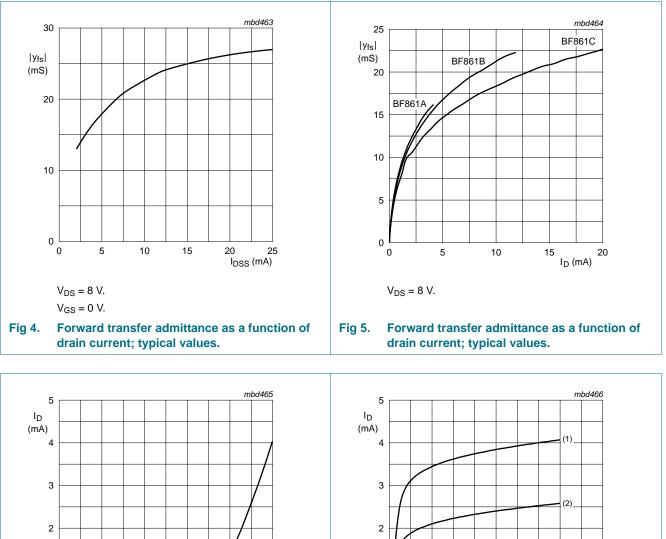
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
y <sub>fs</sub>	forward transfer admittance					
	BF861A		12	-	20	mS
	BF861B		16	-	25	mS
	BF861C		20	-	30	mS
9 <sub>os</sub>	common source output conductance					
	BF861A		-	-	200	μS
	BF861B		-	-	250	μS
	BF861C		-	-	300	μS
C <sub>iss</sub>	input capacitance	f = 1 MHz	-	-	10	pF
C <sub>rss</sub>	reverse transfer capacitance	f = 1 MHz	-	2.1	2.7	pF
V <sub>n</sub> /√B	equivalent input noise voltage	$V_{GS}$ = 0 V; f = 1 MHz	-	1.5	-	nV/√H:

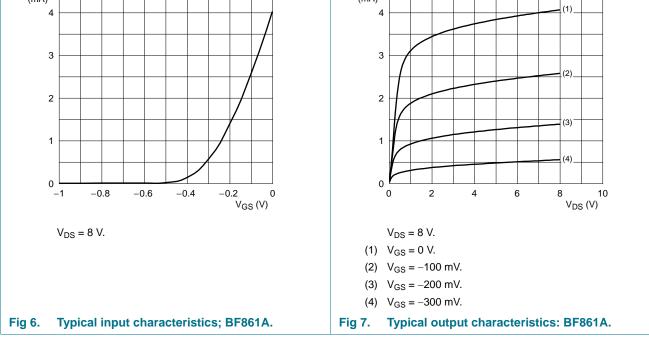




# BF861A; BF861B; BF861C

**N-channel junction FETs** 

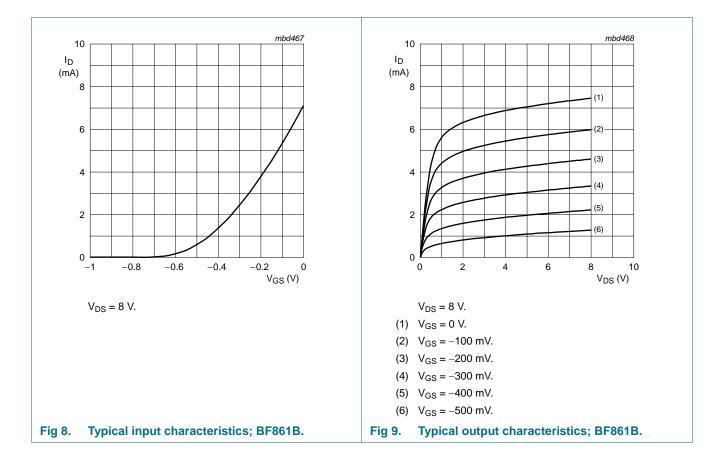




BF861A\_BF861B\_BF861C

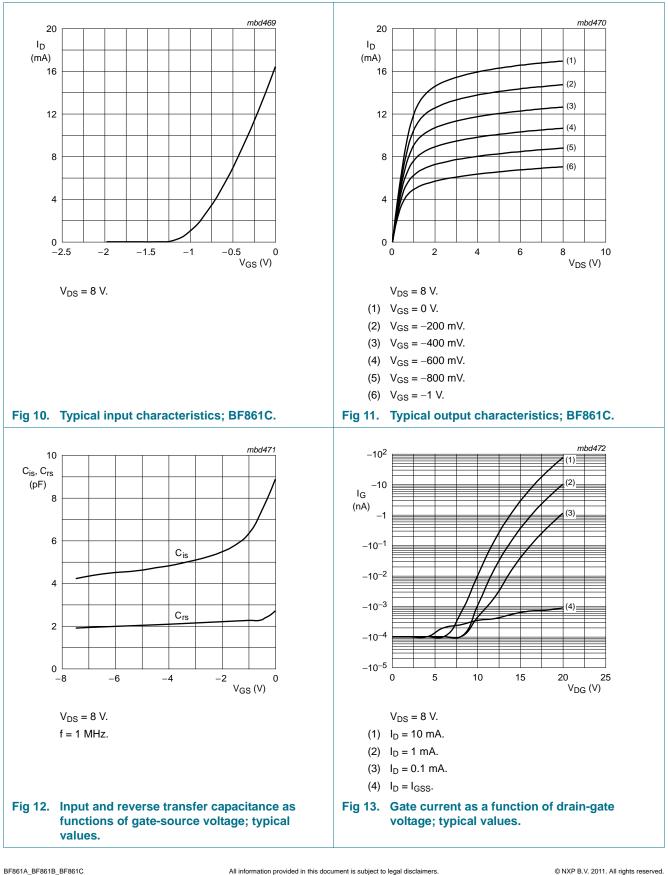
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**N-channel junction FETs** 



# BF861A; BF861B; BF861C

#### **N-channel junction FETs**

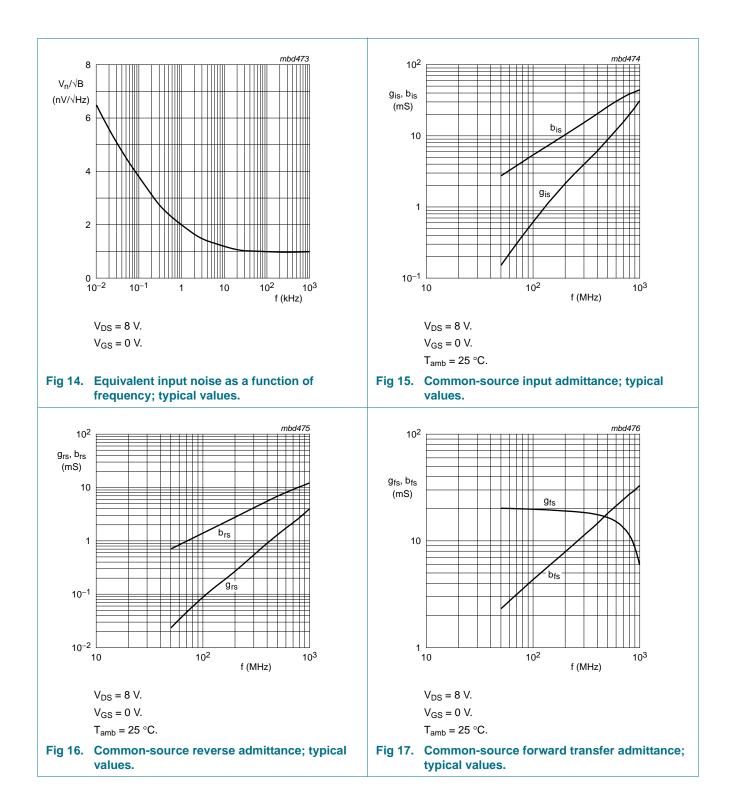


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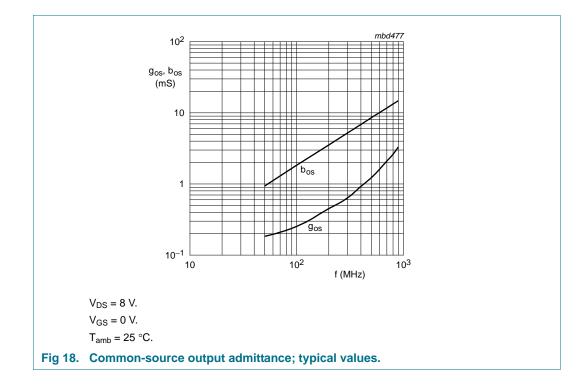
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# BF861A; BF861B; BF861C

#### **N-channel junction FETs**

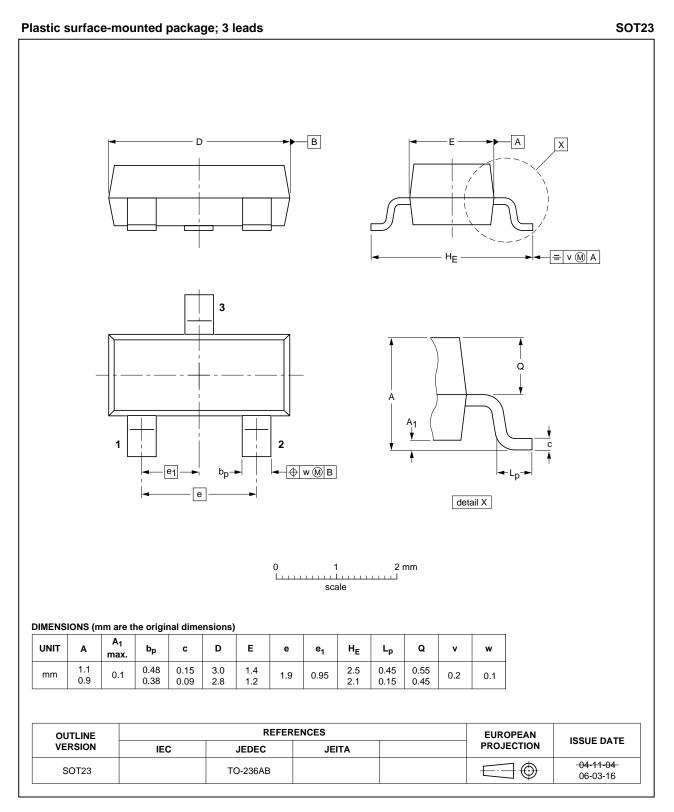


## N-channel junction FETs



**N-channel junction FETs** 

## 8. Package outline



#### Fig 19. Package outline

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Product data sheet

# 9. Revision history

Table 8.	<b>Revision history</b>				
Documen	t ID	Release date	Data sheet status	Change notice	Supersedes
BF861A_E	3F861B_BF861C v.5	20110915	Product data sheet	-	BF861A_BF861B_BF861C v.4
Modificatio	ons:		of this data sheet has be f NXP Semiconductors.	een redesigned to	comply with the new identity
		<ul> <li>Legal texts I</li> </ul>	have been adapted to th	e new company n	ame where appropriate.
		<ul> <li>Package ou</li> </ul>	tline drawings have bee	n updated to the la	atest version.
BF861A_E (9397 750	BF861B_BF861C v.4 13395)	20040924	Product data sheet	-	BF861 v.3
BF861 v.3 (9397 750		19970904	Product specification	-	BF861 v.2
BF861 v.2		19950414	-	-	BF861 v.1
BF861 v.1		19940829	-	-	-

## **10. Legal information**

### **10.1** Data sheet status

Document status[1][2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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[2] The term 'short data sheet' is explained in section "Definitions".

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