

## REGULATORY COMPLIANCE

 <b>Lead Free</b> COMPLIANT	 <b>EU RoHS</b> 2011/65 + 2015/863 COMPLIANT	 <b>China RoHS</b> COMPLIANT	 <b>REACH</b> <b>SVHC</b> COMPLIANT	 <b>DRC</b> <b>CONFLICT</b> <b>FREE</b>
--	--	--	--	--

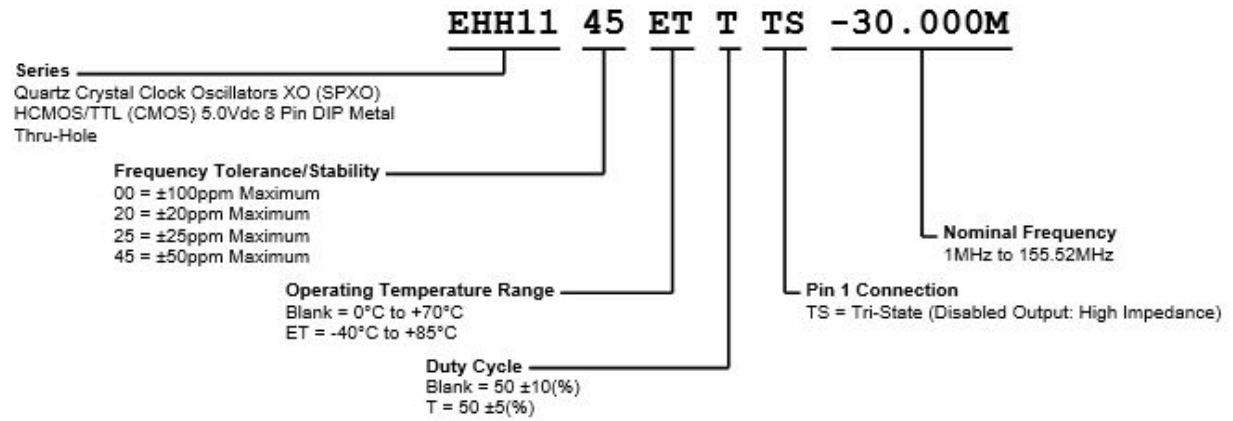
## ITEM DESCRIPTION

Quartz Crystal Clock Oscillators XO (SPXO) HCMOS/TTL (CMOS) 5.0Vdc 8 Pin DIP Metal Thru-Hole

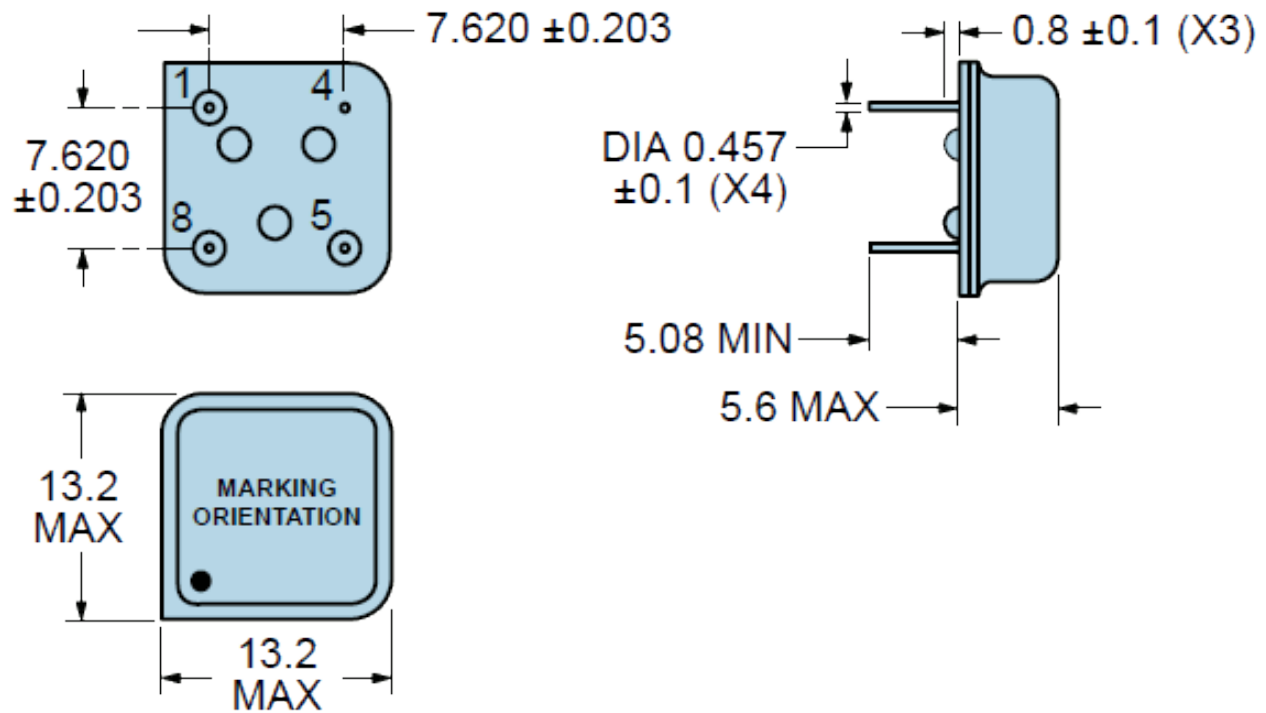
## ELECTRICAL SPECIFICATIONS

<b>Nominal Frequency</b>	1MHz to 155.52MHz
<b>Frequency Tolerance/Stability</b>	Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, First Year Aging at 25°C, Shock, and Vibration. ±100ppm Maximum ±20ppm Maximum ±25ppm Maximum ±50ppm Maximum
<b>Aging at 25°C</b>	±5ppm/year Maximum
<b>Operating Temperature Range</b>	0°C to +70°C -40°C to +85°C
<b>Supply Voltage</b>	5.0Vdc ±10%
<b>Input Current</b>	No Load 50mA Maximum
<b>Output Voltage Logic High (V<sub>OH</sub>)</b>	I <sub>OH</sub> = -16mA 2.4Vdc Minimum with TTL Load, V <sub>dd</sub> -0.4Vdc Minimum with HCMOS Load
<b>Output Voltage Logic Low (V<sub>OL</sub>)</b>	I <sub>OL</sub> = +16mA 0.4Vdc Maximum with TTL Load, 0.5Vdc Maximum with HCMOS Load
<b>Rise/Fall Time</b>	Measured at 0.8Vdc to 2.0Vdc with TTL Load; Measured at 20% to 80% of waveform with HCMOS Load 6nSec Maximum over Nominal Frequency of 1MHz to 70MHz 4nSec Maximum over Nominal Frequency of 70.000001MHz to 155.52MHz
<b>Duty Cycle</b>	50 ±10(%) (Measured at 1.4Vdc with TTL Load or at 50% of waveform with HCMOS Load over Nominal Frequency range of 1MHz to 70MHz; Measured at 50% of waveform over Nominal Frequency range of 70.000001MHz to 155.52MHz) 50 ±5(%) (Measured at 50% of waveform with TTL Load or with HCMOS Load)
<b>Load Drive Capability</b>	10TTL Load or 50pF HCMOS Load Maximum over Nominal Frequency of 1MHz to 70MHz 5TTL Load or 15pF HCMOS Load Maximum over Nominal Frequency of 70.000001MHz to 155.52MHz
<b>Output Logic Type</b>	CMOS
<b>Pin 1 Connection</b>	Tri-State (Disabled Output: High Impedance)
<b>Tri-State Input Voltage (V<sub>IH</sub> and V<sub>IL</sub>)</b>	+2.2Vdc Minimum to enable output, +0.8Vdc Maximum to disable output (High Impedance), No Connect to enable output.
<b>Absolute Clock Jitter</b>	±250pSec Maximum, ±100pSec Typical
<b>One Sigma Clock Period Jitter</b>	±50pSec Maximum, ±30pSec Typical
<b>Start Up Time</b>	10mSec Maximum
<b>Storage Temperature Range</b>	-55°C to +125°C

## PART NUMBERING GUIDE



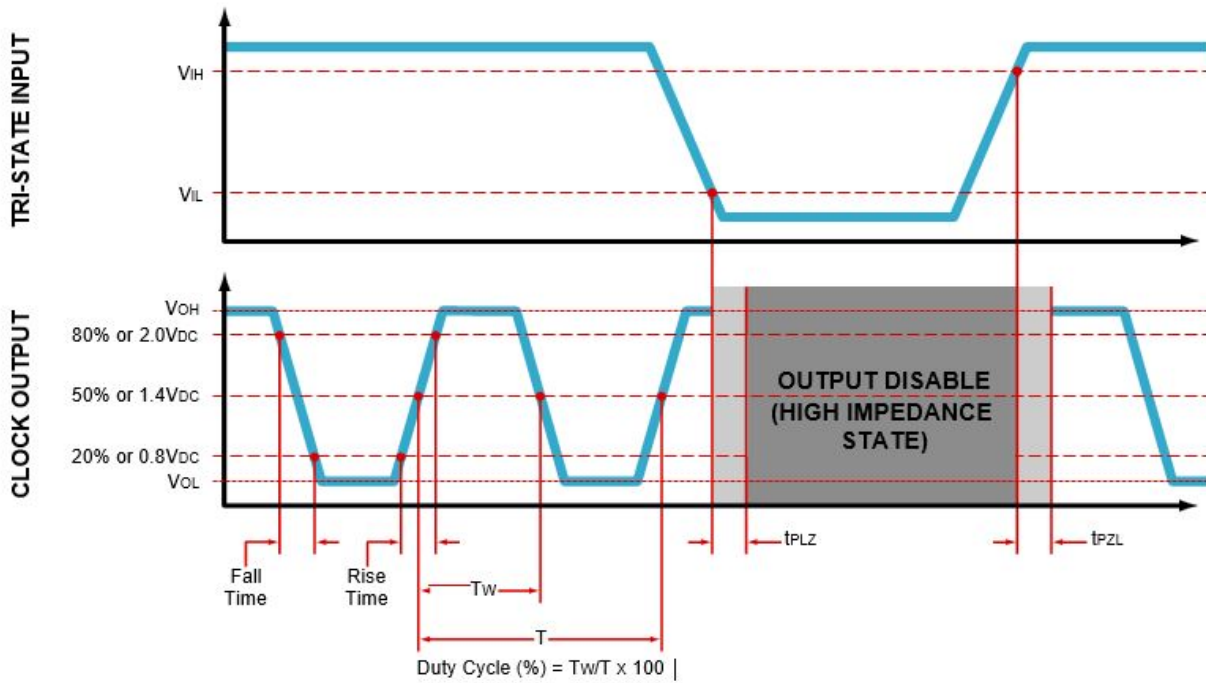
## MECHANICAL DIMENSIONS



PIN	CONNECTION
1	Tri-State (High Impedance)
2	Case/Ground
3	Output
4	Supply Voltage

**All Dimensions in Millimeters**

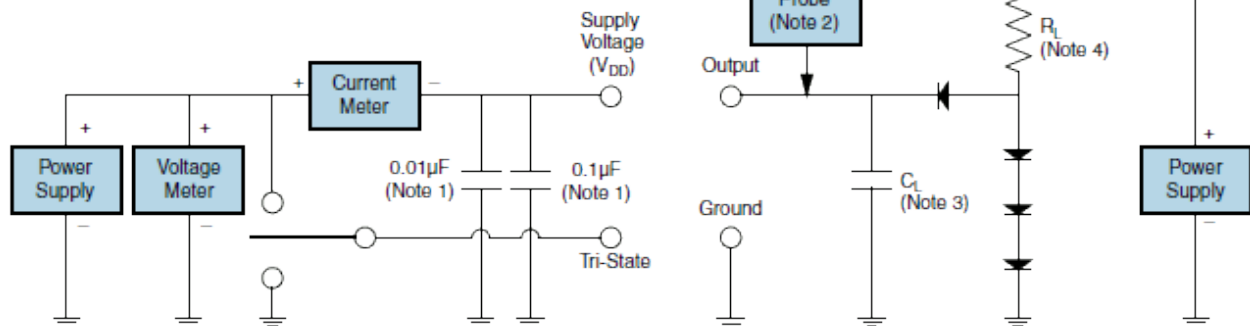
## OUTPUT WAVEFORM & TIMING DIAGRAM



## TEST CIRCUIT FOR TTL OUTPUT

Output Load Drive Capability	$R_L$ Value (Ohms)	$C_L$ Value (pF)
10TTL	390	15
5TTL	780	15

Table 1:  $R_L$  Resistance Value and  $C_L$  Capacitance Value Vs. Output Load Drive Capability



**Note 1:** An external 0.01 $\mu$ F ceramic bypass capacitor in parallel with a 0.1 $\mu$ F high frequency ceramic bypass capacitor close (less Than 2mm) to the package ground and supply voltage pin is required.

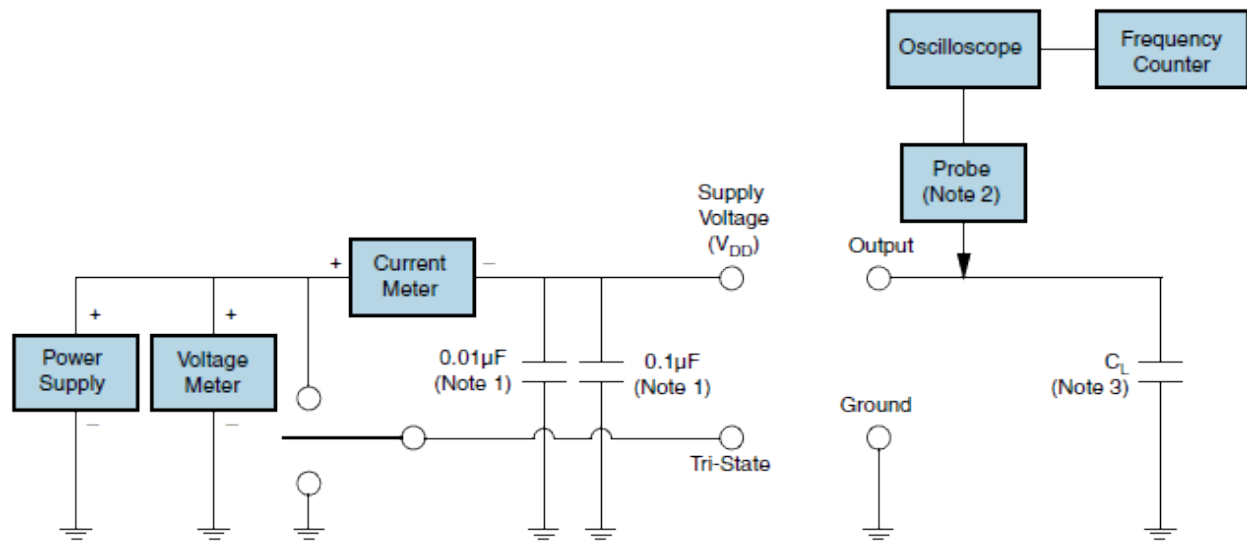
**Note 2:** A low capacitance (<12pF), 10X Attenuation Factor, High Impedance (>10Mohms), and High bandwidth (>300MHz) Passive probe is recommended.

**Note 3:** Capacitance value  $C_L$  includes sum of all probe and fixture capacitance.

**Note 4:** Resistance value  $R_L$  is shown in Table 1. See applicable specification sheet for "Load Drive Capability".

**Note 5:** All diodes are MMBD7000, MMBD914, or equivalent.

## TEST CIRCUIT FOR CMOS OUTPUT

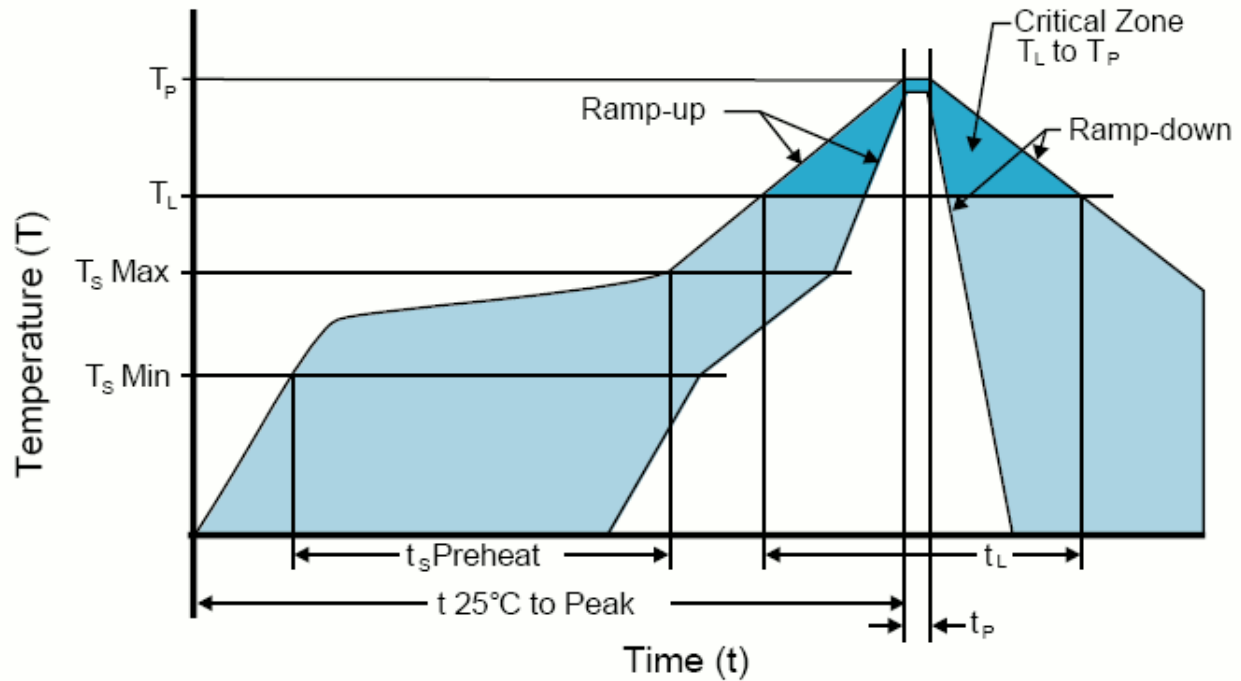


**Note 1:** An external  $0.01\mu F$  ceramic bypass capacitor in parallel with a  $0.1\mu F$  high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is required.

**Note 2:** A low capacitance ( $<12pF$ ), 10X Attenuation Factor, High Impedance ( $>10M\Omega$ ), and High bandwidth ( $>300MHz$ ) Passive probe is recommended.

**Note 3:** Capacitance value ( $C_L$ ) includes sum of all probe and fixture capacitance.

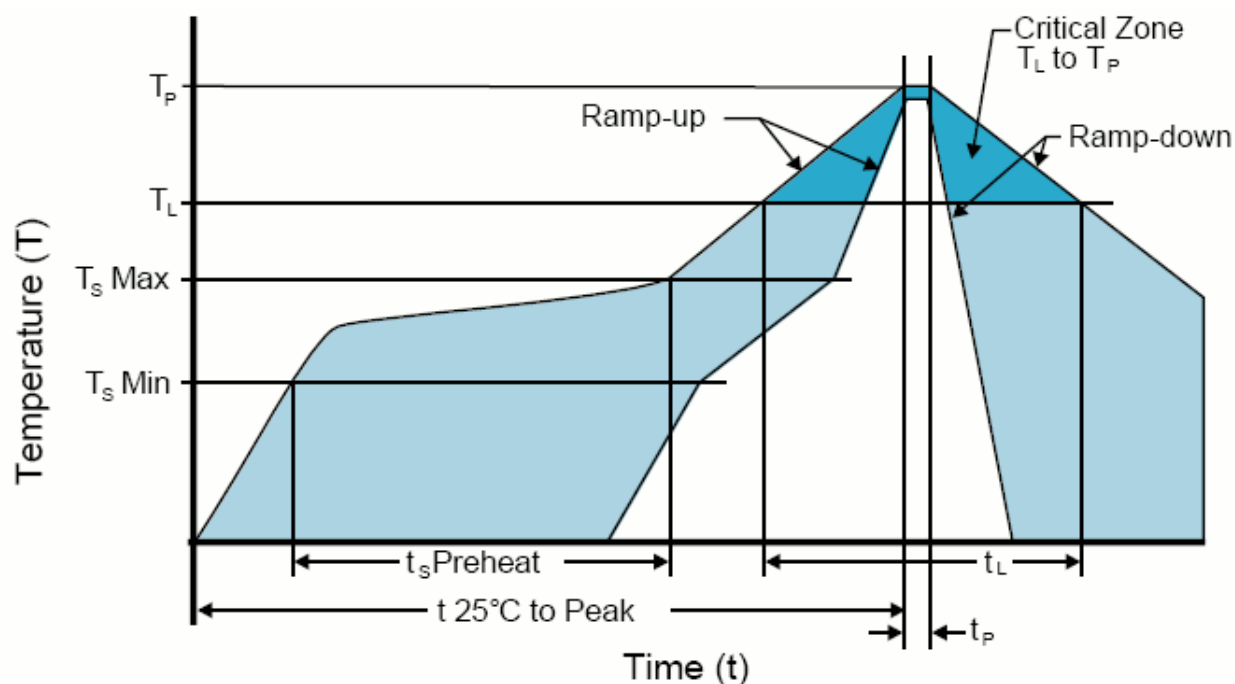
## RECOMMENDED SOLDER REFLOW METHOD



### HIGH TEMPERATURE SOLDER BATH (WAVE SOLDER)

T <sub>s</sub> MAX to T <sub>L</sub> (Ramp-up Rate)	3°C/Second Maximum
Preheat	
- Temperature Minimum (T <sub>s</sub> MIN)	150°C
- Temperature Typical (T <sub>s</sub> TYP)	175°C
- Temperature Maximum (T <sub>s</sub> MAX)	200°C
- Time (t <sub>s</sub> )	60 - 180 Seconds
Ramp-up Rate (T <sub>L</sub> to T <sub>P</sub> )	3°C/Second Maximum
Time Maintained Above:	
- Temperature (T <sub>L</sub> )	217°C
- Time (t <sub>L</sub> )	60 - 150 Seconds
Peak Temperature (T <sub>P</sub> )	260°C Maximum for 10 Seconds Maximum
Target Peak Temperature (T <sub>P</sub> Target)	250°C +0/-5°C
Time within 5°C of actual peak (t <sub>p</sub> )	20 - 40 Seconds
Ramp-down Rate	6°C/Second Maximum
Time 25°C to Peak Temperature (t)	8 Minutes Maximum
Moisture Sensitivity Level	Level 1
Additional Notes	Temperatures shown are applied to back of PCB board and device leads only. Do not use this method for product with the Gull Wing Option

## RECOMMENDED SOLDER REFLOW METHOD

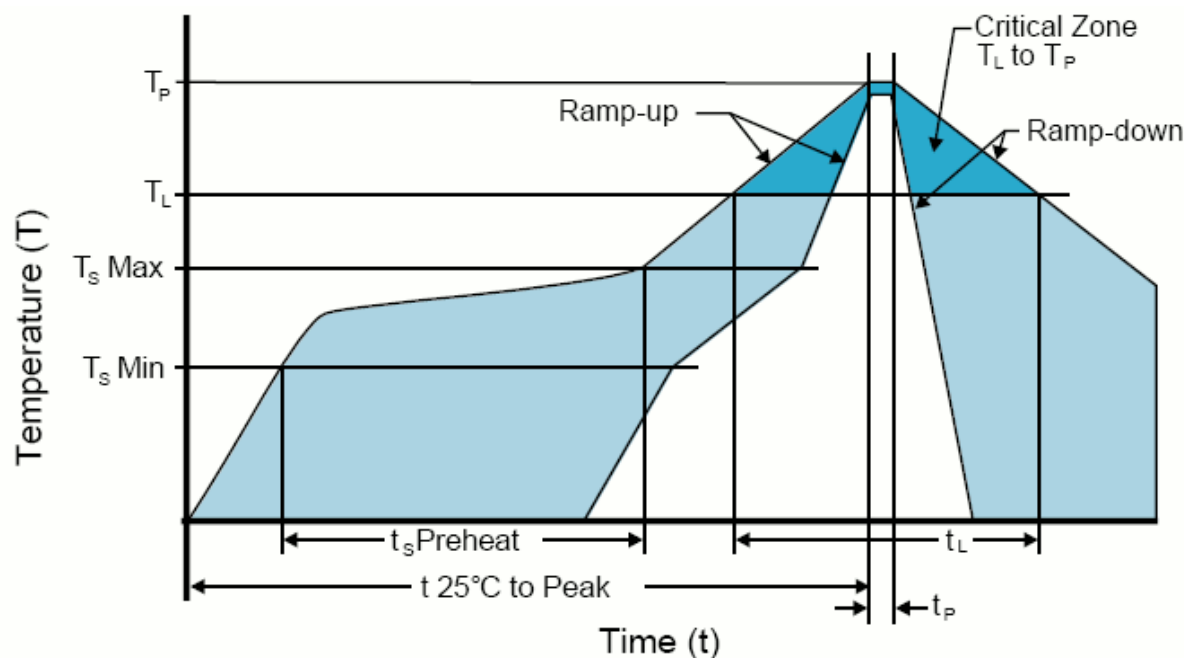


### LOW TEMPERATURE SOLDER BATH (WAVE SOLDER)

T <sub>s</sub> MAX to T <sub>L</sub> (Ramp-up Rate)	5°C/Second Maximum
Preheat	
- Temperature Minimum (T <sub>s</sub> MIN)	N/A
- Temperature Typical (T <sub>s</sub> TYP)	150°C
- Temperature Maximum (T <sub>s</sub> MAX)	N/A
- Time (t <sub>s</sub> )	30 - 60 Seconds
Ramp-up Rate (T <sub>L</sub> to T <sub>P</sub> )	5°C/Second Maximum
Time Maintained Above:	
- Temperature (T <sub>L</sub> )	150°C
- Time (t <sub>L</sub> )	200 Seconds Maximum
Peak Temperature (T <sub>P</sub> )	245°C Maximum
Target Peak Temperature (T <sub>P</sub> Target)	245°C Maximum 1 Time / 235°C Maximum 2 Times
Time within 5°C of actual peak (t <sub>P</sub> )	5 Seconds Maximum 1 Time / 15 Seconds Maximum 2 Times
Ramp-down Rate	5°C/Second Maximum
Time 25°C to Peak Temperature (t)	N/A
Moisture Sensitivity Level	Level 1
Additional Notes	Temperatures shown are applied to back of PCB board and device leads only. Do not use this method for product with the Gull Wing option.



## RECOMMENDED SOLDER REFLOW METHOD



### LOW TEMPERATURE INFRARED/CONVECTION

T <sub>s</sub> MAX to T <sub>L</sub> (Ramp-up Rate)	5°C/Second Maximum
Preheat	
- Temperature Minimum (T <sub>s</sub> MIN)	N/A
- Temperature Typical (T <sub>s</sub> TYP)	150°C
- Temperature Maximum (T <sub>s</sub> MAX)	N/A
- Time (t <sub>s</sub> MIN)	60 - 120 Seconds
Ramp-up Rate (T <sub>L</sub> to T <sub>P</sub> )	5°C/Second Maximum
Time Maintained Above:	
- Temperature (T <sub>L</sub> )	150°C
- Time (t <sub>L</sub> )	200 Seconds Maximum
Peak Temperature (T <sub>P</sub> )	185°C Maximum
Target Peak Temperature (T <sub>P</sub> Target)	185°C Maximum 2 Times
Time within 5°C of actual peak (t <sub>p</sub> )	10 Seconds Maximum 2 Times
Ramp-down Rate	5°C/Second Maximum
Time 25°C to Peak Temperature (t)	N/A
Moisture Sensitivity Level	Level 1
Additional Notes	Temperatures shown are applied to body of device. Use this method only for product with the Gull Wing Option.

### High Temperature Manual Soldering

260°C Maximum for 5 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to device leads only. This method can be utilized with both Gull Wing and Non-Gull Wing devices.)

### Low Temperature Manual Soldering

185°C Maximum for 10 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to device leads only. This method can be utilized with both Gull Wing and Non-Gull Wing devices.)

# Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

[ABRACON:](#)

[EHH1100TS-100.000M](#) [EHH1100TS-13.560M](#)