

REGULATORY COMPLIANCE



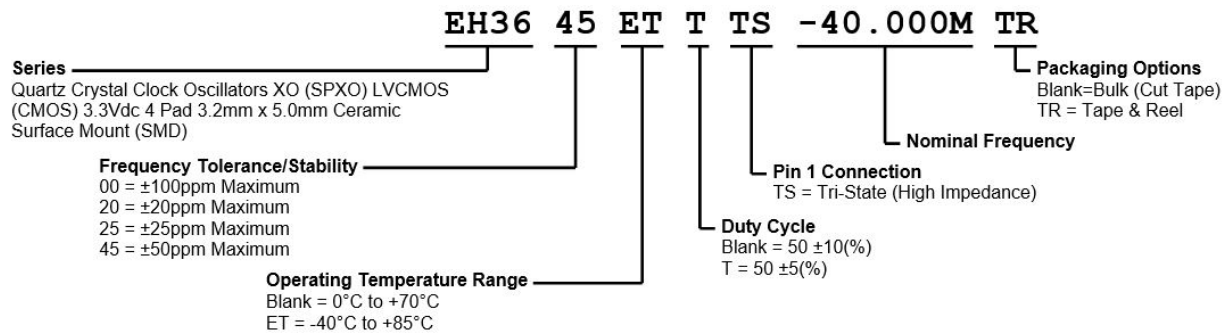
ITEM DESCRIPTION

Quartz Crystal Clock Oscillators XO (SPXO) LVCMOS (CMOS) 3.3Vdc 4 Pad 3.2mm x 5.0mm Ceramic Surface Mount (SMD)

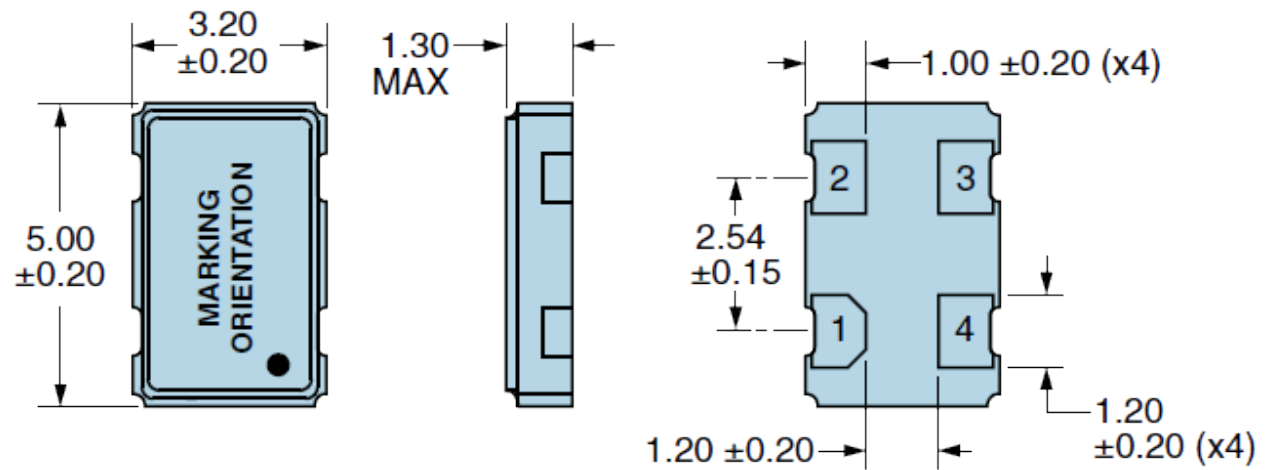
ELECTRICAL SPECIFICATIONS

Nominal Frequency	1MHz to 155.52MHz
Frequency Tolerance/Stability	Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, 1st Year Aging at 25°C, Shock, and Vibration ±100ppm Maximum ±20ppm Maximum ±25ppm Maximum ±50ppm Maximum
Aging at 25°C	±5ppm/year Maximum
Operating Temperature Range	0°C to +70°C -40°C to +85°C
Supply Voltage	3.3Vdc ±10%
Input Current	No Load 35mA Maximum
Output Voltage Logic High (V _{OH})	I _{OH} = -8mA 2.7Vdc Minimum
Output Voltage Logic Low (V _{OL})	I _{OL} = +8mA 0.5Vdc Maximum
Rise/Fall Time	Measured at 20% to 80% of waveform 6nSec Maximum over Nominal Frequency of 1MHz to 70MHz 4nSec Maximum over Nominal Frequency of 70.000001MHz to 155.52MHz
Duty Cycle	Measured at 50% of waveform 50 ±10(%) 50 ±5(%)
Load Drive Capability	30pF Maximum over Nominal Frequency of 1MHz to 70MHz 15pF Maximum over Nominal Frequency of 70.000001MHz to 155.52MHz
Output Logic Type	CMOS
Pin 1 Connection	Tri-State (High Impedance)
Tri-State Input Voltage (V _{IH} and V _{IL})	70% of V _{DD} Minimum to enable output, 20% of V _{DD} Maximum to disable output, No Connect to enable output.
Absolute Clock Jitter	±250pSec Maximum, ±100pSec Typical
One Sigma Clock Period Jitter	±50pSec Maximum, ±40pSec Typical
Start Up Time	10mSec Maximum
Storage Temperature Range	-55°C to +125°C

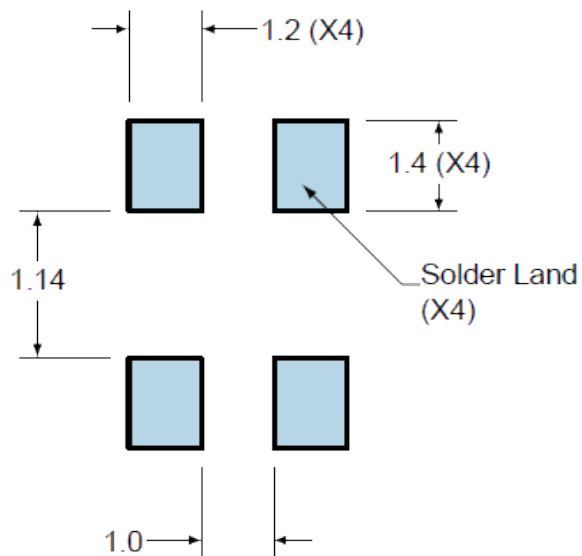
PART NUMBERING GUIDE



MECHANICAL DIMENSIONS



SUGGESTED SOLDER PAD LAYOUT

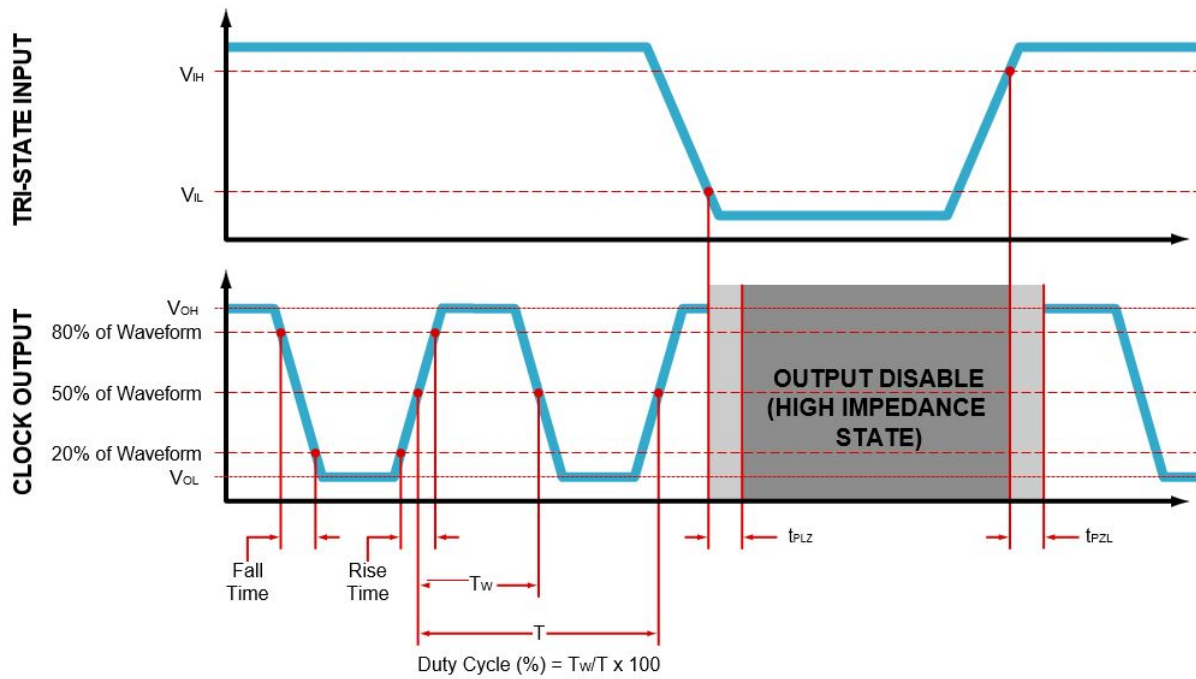


PIN	CONNECTION
1	Tri-State
2	Ground/Case Ground
3	Output
4	Supply Voltage

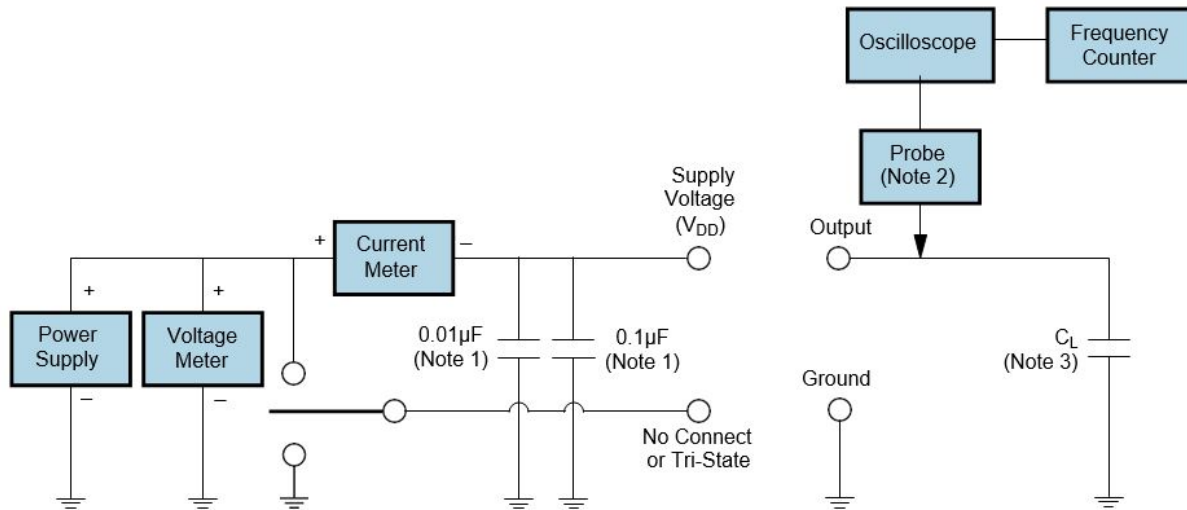
All Tolerances are ± 0.1

All Dimensions in Millimeters

OUTPUT WAVEFORM & TIMING DIAGRAM



TEST CIRCUIT FOR CMOS OUTPUT

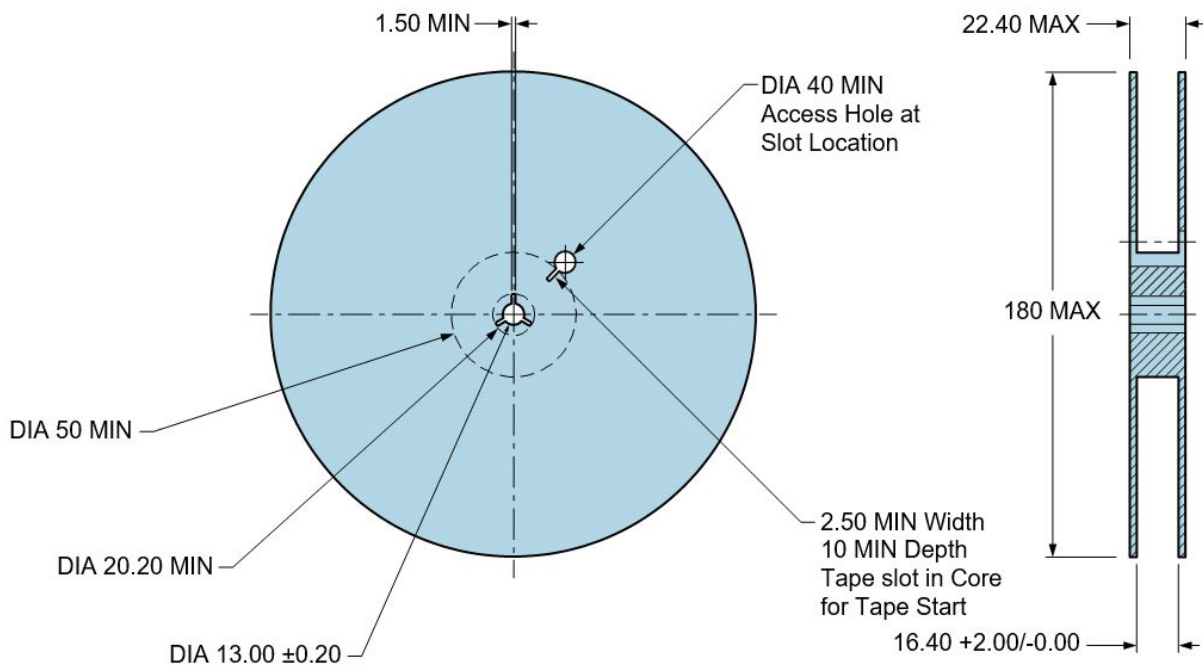


Note 1: An external $0.1\mu\text{F}$ low frequency tantalum bypass capacitor in parallel with a $0.01\mu\text{F}$ high frequency ceramic bypass Capacitor close to the package ground and V_{DD} pin is required.

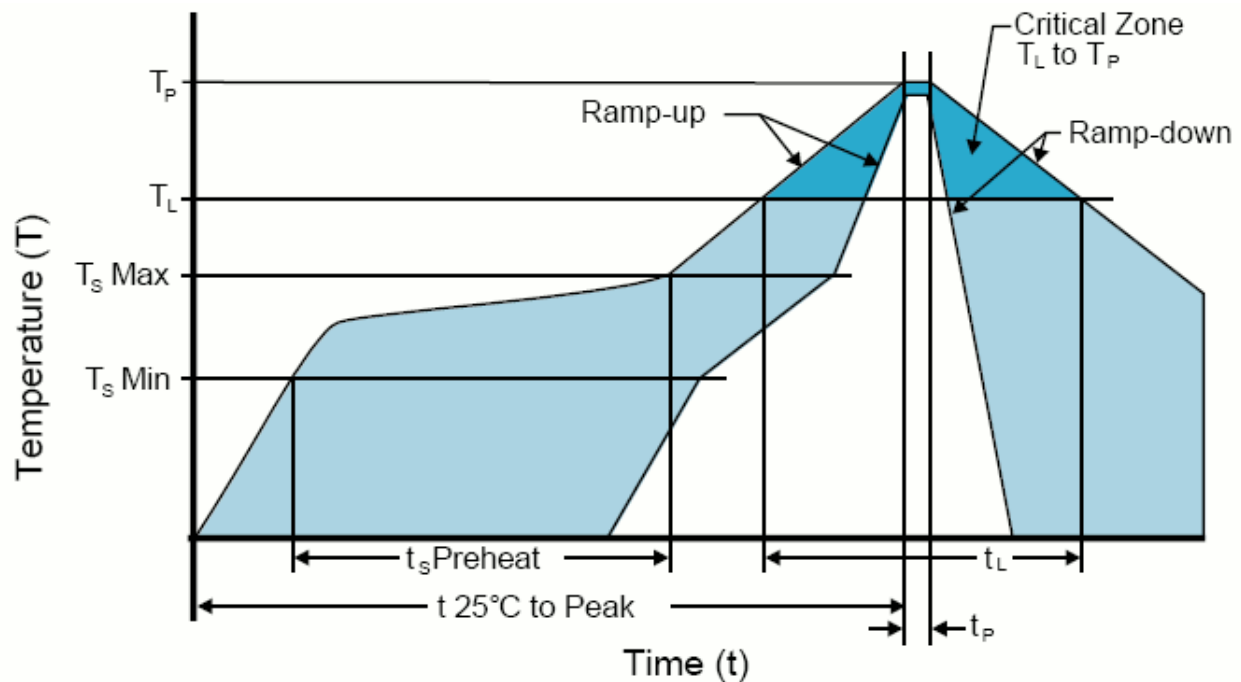
Note 2: A low capacitance ($<12\text{pF}$), 10X attenuation factor, high impedance ($>10\text{Mohms}$), and high bandwidth ($>300\text{MHz}$) passive probe is recommended.

Note 3: Capacitance value C_L includes sum of all probe and fixture capacitance.

Compliant to EIA-481



RECOMMENDED SOLDER REFLOW METHOD



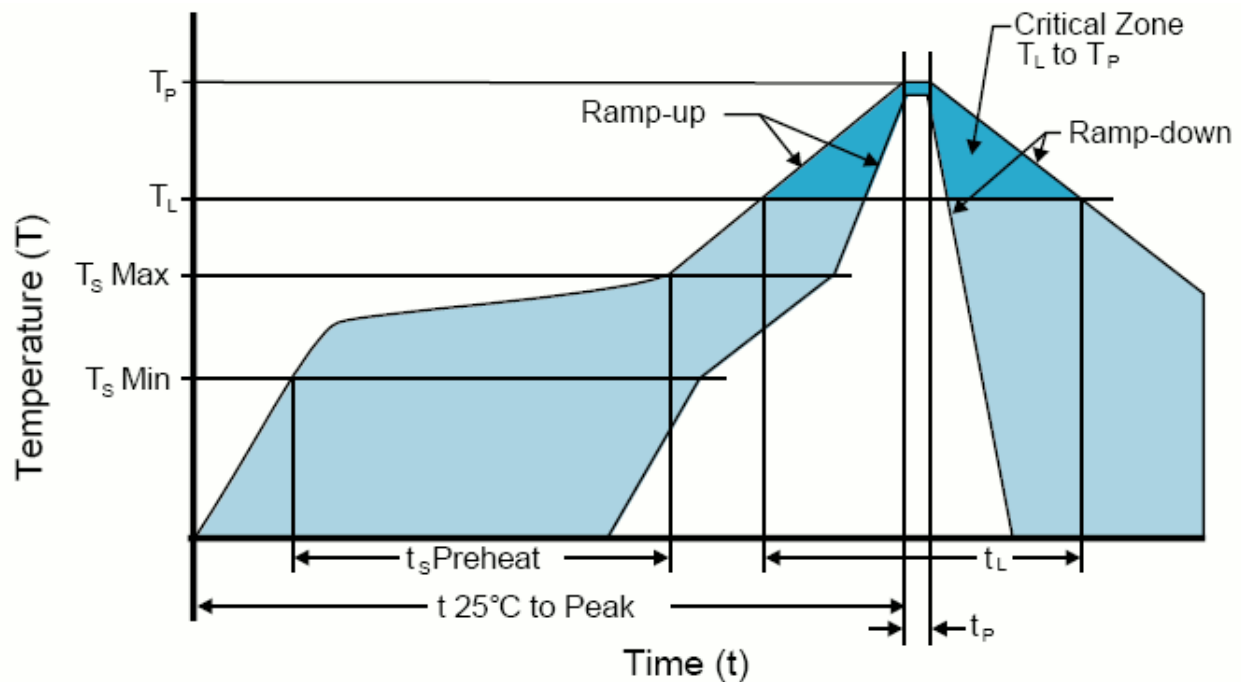
HIGH TEMPERATURE INFRARED/CONVECTION

T _S MAX to T _L (Ramp-up Rate)	3°C/Second Maximum
Preheat	
- Temperature Minimum (T _S MIN)	150°C
- Temperature Typical (T _S TYP)	175°C
- Temperature Maximum (T _S MAX)	200°C
- Time (t _s)	60 - 180 Seconds
Ramp-up Rate (T _L to T _P)	3°C/Second Maximum
Time Maintained Above:	
- Temperature (T _L)	217°C
- Time (t _L)	60 - 150 Seconds
Peak Temperature (T _P)	260°C Maximum for 10 Seconds Maximum
Target Peak Temperature (T _P Target)	250°C +0/-5°C
Time within 5°C of actual peak (t _p)	20 - 40 Seconds
Ramp-down Rate	6°C/Second Maximum
Time 25°C to Peak Temperature (t)	8 Minutes Maximum
Moisture Sensitivity Level	Level 1
Additional Notes	Temperatures shown are applied to body of device.

High Temperature Manual Soldering

260°C Maximum for 5 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)

RECOMMENDED SOLDER REFLOW METHOD



LOW TEMPERATURE INFRARED/CONVECTION

T_S MAX to T_L (Ramp-up Rate)	5°C/Second Maximum
Preheat	
- Temperature Minimum (T_S MIN)	N/A
- Temperature Typical (T_S TYP)	150°C
- Temperature Maximum (T_S MAX)	N/A
- Time (t_s)	60 - 120 Seconds
Ramp-up Rate (T_L to T_P)	5°C/Second Maximum
Time Maintained Above:	
- Temperature (T_L)	150°C
- Time (t_L)	200 Seconds Maximum
Peak Temperature (T_P)	240°C Maximum
Target Peak Temperature (T_P Target)	240°C Maximum 2 Times / 230°C Maximum 1 Time
Time within 5°C of actual peak (t_P)	10 Seconds Maximum 2 Times / 80 Seconds Maximum 1 Time
Ramp-down Rate	5°C/Second Maximum
Time 25°C to Peak Temperature (t)	N/A
Moisture Sensitivity Level	Level 1
Additional Notes	Temperatures shown are applied to body of device.

Low Temperature Manual Soldering

185°C Maximum for 10 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)

Mouser Electronics

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