

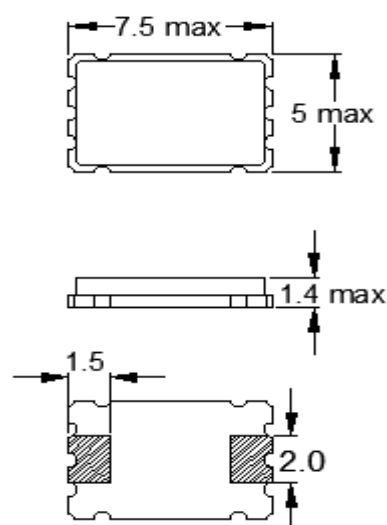
Features

- Tolerances down to ± 30 PPM
- Stabilities down to ± 15 PPM
- Temperature Ranges as wide as -40°C to $+85^{\circ}\text{C}$

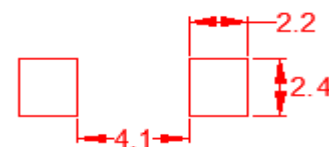
STANDARD SPECIFICATIONS	
PARAMETERS	MAX (Unless otherwise noted)
Frequency Range	6.000 ~ 50.000 MHz
Frequency Tolerance @ 25°C	(See options below)
Frequency Stability, ref 25°C	(See options below)
Temperature Range	
Operating (T_{OPR})	(See options below)
Storage (T_{STG})	$-40^{\circ}\text{C} \sim +85^{\circ}\text{C}$
Shunt Capacitance (C_0)	7 pF
Load Capacitance (C_L)	(See options below)
Drive Level	0.5 mW
Aging per year (@ 25°C)	± 5 PPM
Maximum Soldering Temp / Time	260°C / 10 Seconds x 2
Moisture Sensitivity Level (MSL) per J-STD-033	1
Termination Finish	Au over Ni
Seal Method	Resin
Lead (Pb) Free	Yes
RoHS Compliant	Yes

Frequency Range (MHz)	Operating Mode	Max ESR Ω
6.000 ~ 7.999	Fundamental	80
8.000 ~ 9.999	Fundamental	60
10.000 ~ 15.999	Fundamental	50
16.000 ~ 50.000	Fundamental	40

DIMENSIONS / MECHANICAL SPECIFICATIONS



Recommended Solder Pad Layout



Dimensions in mm

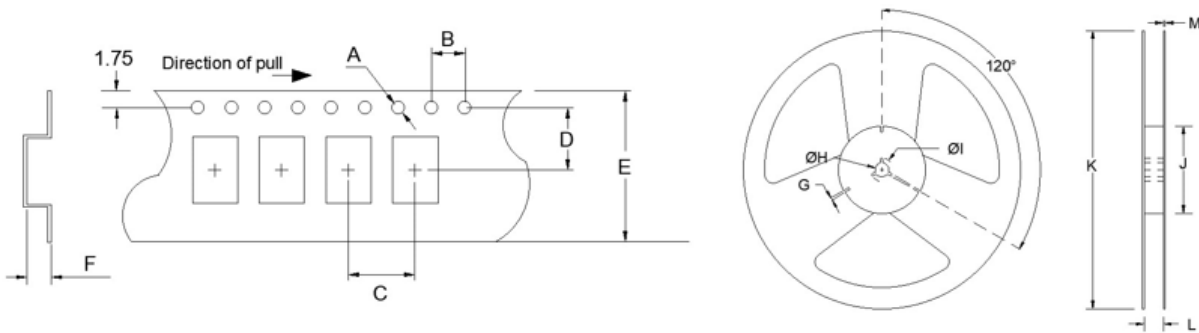
Note: Dimensional drawing is for reference to critical specifications defined by size measurements. Certain non-critical visual attributes, such as side castellations, etc. may vary.

AVAILABLE OPERATING TEMPERATURES AND STABILITIES*

Operating Temperature	± 15 PPM	± 20 PPM	± 25 PPM	± 30 PPM	± 50 PPM
$-0^{\circ}\text{C} \sim +75^{\circ}\text{C}$	O	O	O	O	O
$-10^{\circ}\text{C} \sim +60^{\circ}\text{C}$	O	O	O	O	O
$-10^{\circ}\text{C} \sim +70^{\circ}\text{C}$	O	O	O	O	O
$-20^{\circ}\text{C} \sim +70^{\circ}\text{C}$	O	O	O	O	O
$-30^{\circ}\text{C} \sim +85^{\circ}\text{C}$	X	O	O	O	O
$-40^{\circ}\text{C} \sim +85^{\circ}\text{C}$	X	O	O	O	O

Key: O = Available, X = Not Available, Δ = Consult Fox Technical Support, *Does not imply a stocked part.

TAPE SPECIFICATIONS (mm)							REEL SPECIFICATIONS (mm)						
A	B	C	D	E	F	REEL QTY	G	H	I	J	K	L	M
ø1.5	4.0	8.0	7.5	16.0	2.3	-T1 = 1,000 -T2 = 2,000 Default	2.0	ø13	ø21	ø80	ø180 ø250	17.5	2.0



Available Options & Part Identification for Ceramic SMD Crystal FC7AS ¹							
Sample PN: FC7ASCCLM40.0-T2							
F	C7AS	C	C	L	M	40.0	-T2
Fox	Model Number	Tolerance B = ±50 PPM C = ±30 PPM	Stability B = ±50 PPM C = ±30 PPM D = ±25 PPM E = ±20 PPM F = ±15 PPM	Load Capacitance² E = 10pF G = 12pF J = 15pF L = 18pF M = 20pF	Operating Temperature C = 0 to +70°C D = -10 to +60°C E = -10 to +70°C F = -20 to +70°C K = -30 to +85°C M = -40 to +85°C	Frequency (MHz)	Values Added Options Blank = Bulk T1 = 1,000 pcs T2 = 2,000 pcs

1 Not all frequency, tolerance, stability, load, and operating temperature combinations may be available.

2 Listed load capacitances represent the most commonly used. Other load capacitances are available. Contact us for assistance

Reliability Test Conditions
Please contact Abracon Quality Assurance department

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

ABRACON:

[FC7ASBAMM9.216-BULK](#) [FC7ASBBMD14.7456-BULK](#) [FC7ASBBMD14.7456-T2](#) [FC7ASBBMD25.0-T1](#)
[FC7ASBBMD9.8304-BULK](#) [FC7ASBBMD22.1184-T1](#) [FC7ASBBMD24.0-BULK](#) [FC7ASCCLM6.4-T2](#)
[FC7ASBBMD11.0592-BULK](#) [FC7ASBBMD14.7456-T1](#) [FC7ASBBMD15.0-BULK](#) [FC7ASBBMD14.31818-BULK](#)
[FC7ASBBMD24.0-T1](#) [FC7ASCCLM6.4-BULK](#) [FC7ASBBMD25.0-BULK](#) [FC7ASCCGM7.6-T2](#) [FC7ASBBMD11.0592-](#)
[T1](#) [FC7ASBBMD11.0592-T2](#) [FC7ASBBMD14.31818-T1](#) [FC7ASBBMD24.576-BULK](#) [FC7ASBBMD15.0-T1](#)
[FC7ASBBMD15.0-T2](#) [FC7ASBBMD22.1184-BULK](#) [FC7ASBBMD16.0-BULK](#) [FC7ASBBGD24.0-T2](#) [FC7ASBBMD20.0-](#)
[BULK](#) [FC7ASBBMF21.888-BULK](#) [FC7ASBBAM30.0-T2](#) [FC7ASBBGD24.0-BULK](#) [FC7ASBBMD10.0-BULK](#)
[FC7ASBBMD16.0-T1](#) [FC7ASBBMM16.0-BULK](#) [FC7ASCCEC7.3728-T1](#) [FC7ASBBMD24.0-T2](#) [FC7ASBBMM16.0-T1](#)
[FC7ASBBMM9.216-T2](#) [FC7ASCCEC7.3728-T2](#) [FC7ASBAMM9.216-T1](#) [FC7ASBAMM9.216-T2](#)
[FC7ASBBMD14.31818-T2](#) [FC7ASBBMD22.1184-T2](#) [FC7ASBBMD24.576-T1](#) [FC7ASBBMD9.8304-T1](#)
[FC7ASBBMF21.888-T1](#) [FC7ASBBMF21.888-T2](#) [FC7ASBBMM16.0-T2](#) [FC7ASCCLC6.0-T1](#) [FC7ASCCLC6.0-T2](#)