PLASTIC PACKAGE ULTRA MINIATURE PURE SILICONTM CLOCK OSCILLATORS

ASDM

Request Samples 🕥

Check Inventory

2.5 x 2.0 x 0.85 mm RoHS/RoHS II Compliant MSL Level = 1

Features

- Low Power Consumption
- Exceptional Stability Over Temp. at -40°C to +85°C
- Low Cost-Compact QFN Plastic Packaging
- Compact Package design

Applications

- CCD Clock for VTR Camera
- Equipment Connected to PCs
- Low Profile Equipment
- Computers and Peripherals
- Portable Electronics
- Consumer Electronics
- Vibrant, Shock-Prone & Humid Environments for Industrial Equipment
- Demanding Military & Automotive Electronics

Common Key Electrical Specifications

Pai	rameters	Min.	Тур.	Max.	Units	Notes
Frequency Range:		1.0		150	MHz	
Operating Temperature:		0		+70	°C	See options
Storage Temperature:		-55		+150	°C	
Supply Voltage (Vdd):		-	+1.8~+3.3		V	See options
	1.0 to 39.9999MHz		7	15	mA	Vdd=3.3V
Supply Current	40.0 to 79.9999MHz		8	15		No load
(no load):	80.0 to 124.9999MHz		9	15		RL=∞
	125.0 to 150MHz		10	15		T=25°C
	V _{OH}	$0.8*V_{dd}$			N7	1 5 . F
Output Voltage:	V _{OL}			0.2*V _{dd}	V	15pF
Rise Time:	Tr		1.3	3.0		15pF; T=25°C
Fall Time:	Tf		1.3	3.0	ns	20%/80%*VDD
Output Load:		15pF 1	$15 \text{pF} \max / 10 \text{k}\Omega \min.$		pF	
Symmetry:		45		55	%	@1/2Vdd
Startup Time:			1.5	3.0	ms	
Disable Time:			20	100	ns	
Disable Stand-by Current:				15	uA	
Tri-state Function (Stand-by) :			"1" (VIH≥0.75*Vdd) or			
			Open: Oscillation "0" (VIH<0.25*Vdd) : Hi Z		V	
Cycle to cycle jitter:			60		ps	F=100MHz
Aging:		-5.0		+5.0	ppm	First year



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Absolute Maximum Ratings

Item	Min.	Max.	Units	Condition
Supply Voltage	-0.3	+4.0	V	
Input Voltage	-0.3	Vdd+0.3	V	
Junction Temp.		+150	°C	
Storage Temp.	-55	+150	°C	
Soldering Temp.		+260	°C	40sec max
ESD			V	
HBM		2,000		
MM		200		
CDM		500		



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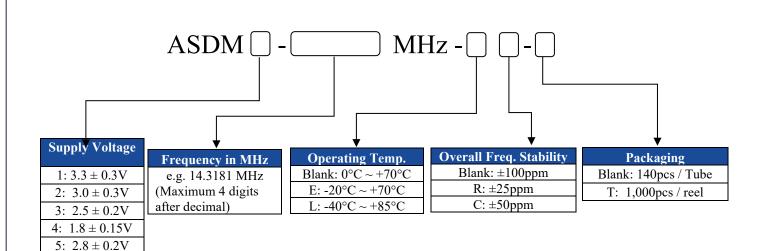
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Options and Part Identification (left blank if standard)

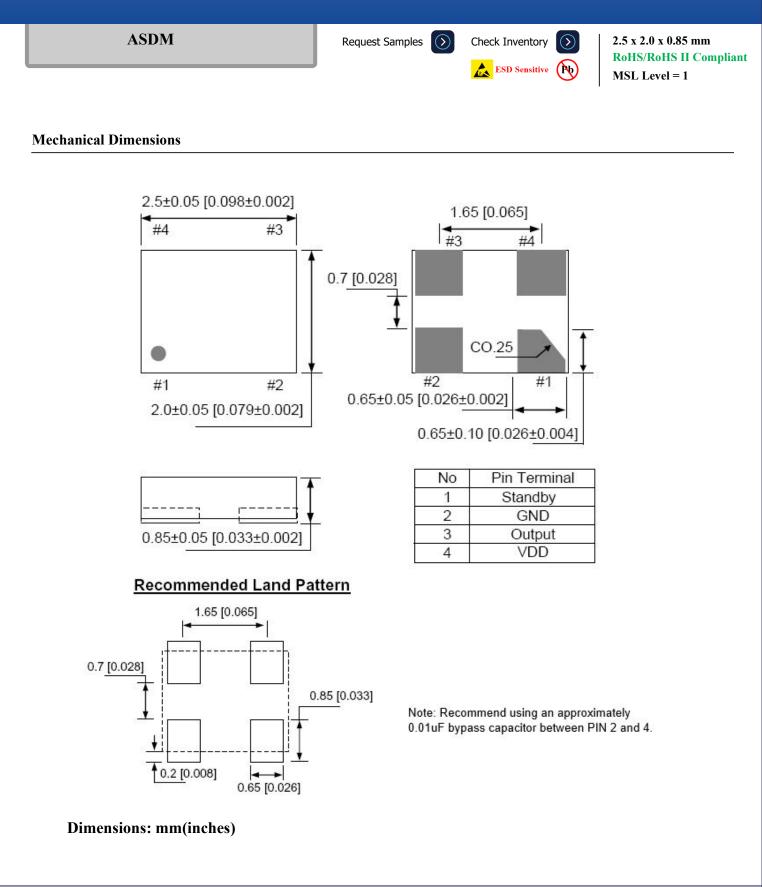




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Request Samples (\mathbf{b}) Check Inventory (\mathbf{S})

(Pb)

ESD Sensitive

2.5 x 2.0 x 0.85 mm **RoHS/RoHS II Compliant** MSL Level = 1

220 °C

220 °C

Reflow Profile [JEDEC J-STD-020]

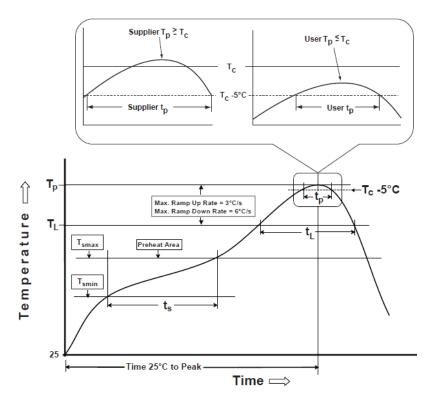


Table 1				
SnPb Eutectic Process Classification Temperatures (T _c)				
Package Thickness	Volume mm ³ <350	Volume mm ³ <u>></u> 350		

235 °C

220 °C

Table 2 -Eroc

Dh

<2.5 mm

<u>></u>2.5 mm

	lassification Temperatures (T _c)				
Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000		
<1.6 mm	260 °C	260 °C	260 °C		
1.6 mm - 2.5 mm	260 °C	250 °C	245 °C		
>2.5 mm	250 °C	245 °C	245 °C		

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat / soak		
Temperature minimum (T _{smin})	100°C	150°C
Temperature maximum (T _{smax})	150°C	200°C
Time (T _{smin} to T _{smax}) (t _s)	60 - 120 sec.	60 - 120 sec.
Average ramp-up rate (T _{smax} to T _P)	3°C/sec. max	3°C/sec. max
Liquidous temperature (T _L)	183°C	217°C
Time at liquidous (t _L)	60 - 150 sec.	60 - 150 sec.
Peak package body temperature (T _P)*	see Table 1	see Table 2
Time (t _p)** within 5°C of the specified classification temperature (T _C)	20 sec.	30 sec.
Ramp-down rate (T _p to T _{smax})	6°C/sec. max	6°C/sec. max
Time 25°C to peak temperature	6 min. max	8 min. max
Reflow cycles	2 max	2 max

*Tolerance for peak profile temperature (T_P) is defined as a supplier minimum and a user maximum.

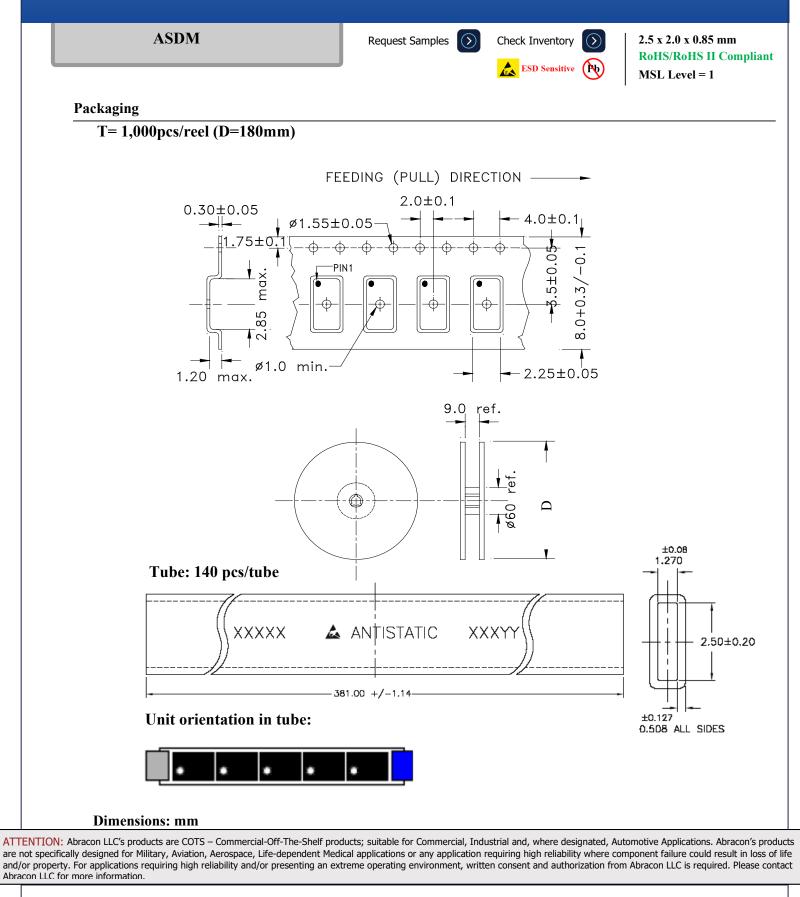
**Tolerance for time at peak profile temperature (t_p) is defined as supplier minimum and a user maximum.



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