

DISTINCTIVE CHARACTERISTICS

Compact Size

- Perfect for rack mount router and other applications with space limitations.
Compact body size: 19.0mm (.748") x 18.0mm (.709") compared to
Standard body size: 23.13mm (.911") x 20.59mm (.811")
- Vibrant Illumination
- Low Energy Consumption

Programmable LCD

Variety of LED Backlighting Colors

Rubber Dome

Epoxy Sealed Straight PC Terminals

Snap-in standoff for easy, secure mounting and alignment



Programmable to display graphics, alphanumeric characters and animated sequences.

Integrated liquid crystal display provides wide viewing angle with high contrast and clarity.

Viewing area 14.5mm x 11.8mm (horizontal x vertical) at 36 x 24 pixels.

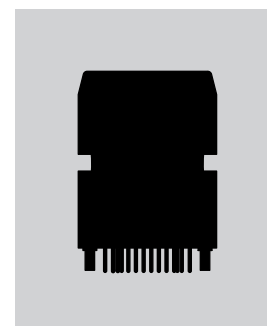
Dome gives crisp tactile feedback to positively indicate circuit transfer.

High reliability and long life of one million actuations minimum.

Epoxy sealed terminals prevent entry of solder flux and other contaminants.

Optional accessories available to simplify production process.

Actual Size



PART NUMBERS & DESCRIPTIONS

Part Number	Switch Description	LCD Mode	LED Color
IS15BSBFP4RGB	SPST Momentary ON Gold Contacts Straight PC Terminals	Black & White FSTN Positive	* Red/Green/Blue
IS15BSAFP4CF	SPST Momentary ON Gold Contacts Straight PC Terminals	Black & White FSTN Positive	* Red/Green

* Simultaneous illumination of LEDs achieves infinite colors.

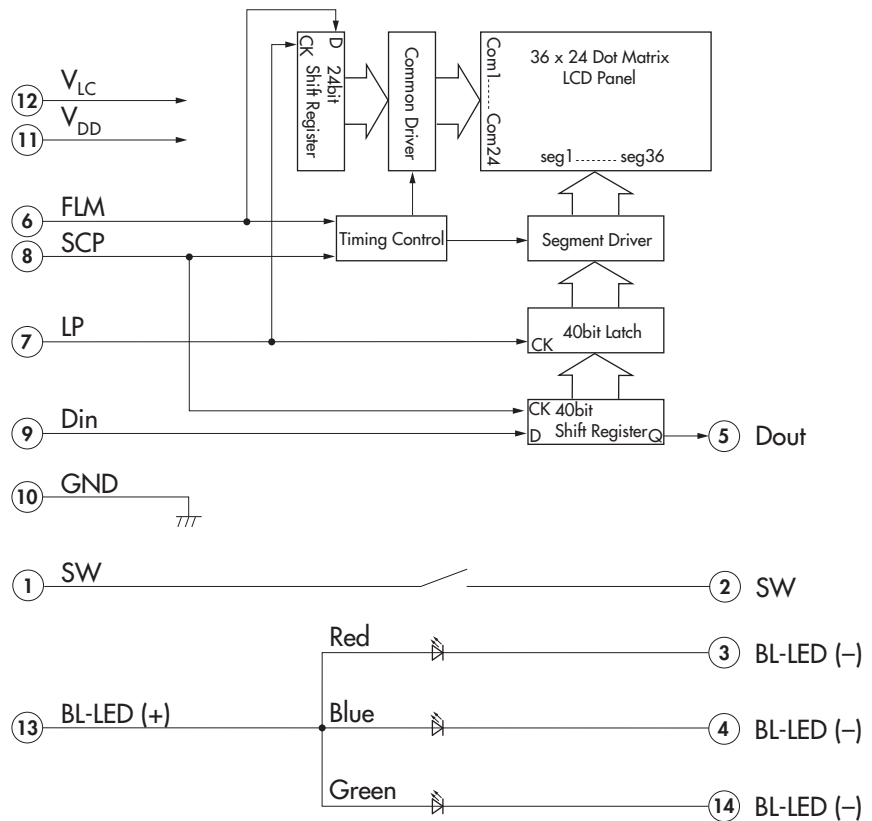
CHARACTERISTICS OF DISPLAY

Viewing Area	14.5mm x 11.8mm (horizontal x vertical)
Pixel Size	0.371mm x 0.445mm (horizontal x vertical)
Backlight LED	RGB: red/green/blue Bicolor: red/green

BLOCK DIAGRAM & PIN CONFIGURATIONS FOR RGB



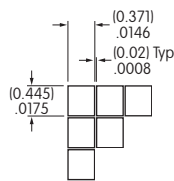
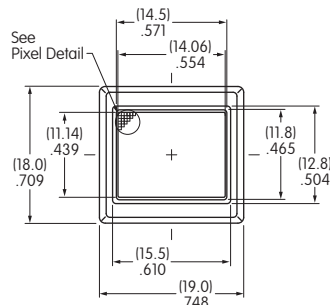
IS15BSBFP4RGB
RGB LED Backlight
Black and White LCD



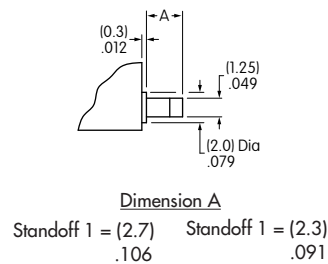
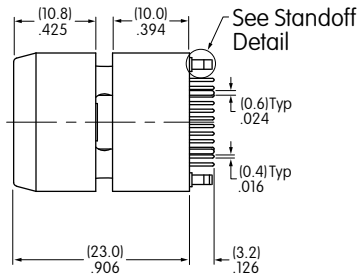
Pin No.	Symbol	Name	Function
①	SW	Terminal of Switch	Normally open
②	SW	Terminal of Switch	Normally open
③	BL-LED (-)	Terminal of Backlight LED	Cathode for red
④	BL-LED (-)	Terminal of Backlight LED	Cathode for blue
⑤	Dout	Data Output	Display serial output. Can be used to connect to Din of the next SWITCH. As a result, many SWITCHES can be controlled with one clock and data signal.
⑥	FLM	First Line Marker	The marking signal for the first line data of LCD display. The first line of LCD will be selected by the falling edge of LP signal during the high level (FLM).
⑦	LP	Latch Pulse	Line data latch pulse will latch content of internal 40-bit shift register at falling edge for one line of display. LP will also increment the display line by one.
⑧	SCP	Serial Clock Pulse	Clock used by 40-bit internal shift register of the switch, shifting the display data bit presented at Din at falling edge.
⑨	Din	Data Input	Display serial data bit. Note: to map the display data, because of the difference between the number of internal shift register data (40) and the single line of LCD pixels (36), the first four bits of data shifted will be dummy bits.
⑩	GND	Ground	
⑪	V _{DD}	Power	Power source for logic circuit
⑫	V _{LC}	Power	Power source for LCD drive
⑬	BL-LED (+)	Terminal of Backlight LED	Anode for common
⑭	BL-LED (-)	Terminal of Backlight LED	Cathode for green

TYPICAL COMPACT SWITCH DIMENSIONS

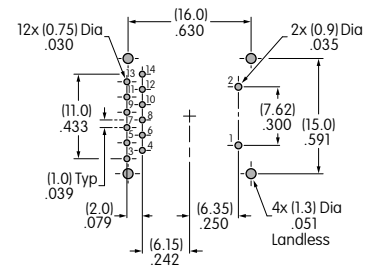
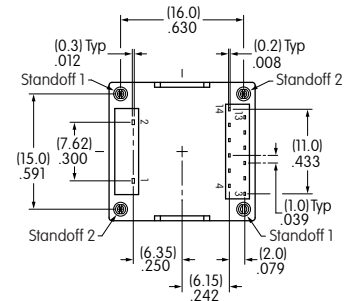
Terminal numbers are not on the switch.



Pixel Detail



Standoff Detail



Footprint

SWITCH SPECIFICATIONS

Circuit	SPST normally open
Electrical Capacity (Resistive Load)	100mA @ 12V DC
Contact Resistance	200 milliohms max @ 20mV 10mA
Insulation Resistance	100 megohms min @ 100V DC
Dielectric Strength	125V AC for 1 minute minimum
Mechanical Endurance	1,000,000 operations minimum
Electrical Endurance	1,000,000 operations minimum
Operating Force	2.2 ± 0.5 Newtons
Total Travel	1.8mm (.071")
Operating Temp. Range	-20°C ~ +60°C (-4°F ~ +140°F)
Storage Temp. Range	-30°C ~ +70°C (-22 °F ~ +158°F)

Optical Characteristics (Temperature at 25°C)

Items	Symbols	Minimum	Typical	Maximum
Contrast Ratio	Cr	—	3.0	—
Viewing Angle (Cr ≥ 1.1)	Up & Down	θ	90°	—
	Right & Left	φ	90°	—

Absolute Maximum Ratings (Temperature at 25°C)

Items	Symbols	Ratings
Supply Voltage for Logics	V_{DD}	-0.3V to +7.0V
Supply Voltage for LCD	V_{LC}	-0.3V to +12.0V
Input Voltage	V_I	-0.3V to V_{DD} +0.3V
Output Voltage	V_O	-0.3V to V_{DD} +0.3V

LCD SPECIFICATIONS

Characteristics of Display

Display Operation Mode	FSTN positive
Display Condition	Transflective with built-in LED backlight
Viewing Angle	6 o'clock
Driving Method	1/24 duty, 1/5 bias (built-in driving circuit)
Pixel Format	36 x 24 pixels (horizontal x vertical)
Backlight LED	RGB: red/green/blue Bicolor: red/green

Recommended Operating Conditions (Temperature at 25°C)

Items	Symbols	Minimum	Typical	Maximum
Supply Voltage for Logics	V_{DD}	3.0V	—	5.5V
Supply Voltage	V_{LC}	—	* 7.3V	—
Input Voltage	V_I	0V	—	V_{DD}
Driving Frequency	f_{FLM}	—	150Hz	—
Clock Operation Frequency	f_{SCP}	—	—	8.0MHz

* LCD voltage (V_{LC}) level depends on refreshing frequency and temperature. The optimal V_{LC} can differ slightly from the stated typical value.

DC Characteristics of LCD Drive (Temperature at -20°C to +60°C and $V_{DD} = 5.0V \pm 10\%$)

Items	Symbols	Test Conditions	Minimum	Typical	Maximum	Unit
High Level Input Voltage	V_{IH}		$0.7V_{DD}$		V_{DD}	V
Low Level Input Voltage	V_{IL}		0		$0.3V_{DD}$	V
High Level Input Leakage Current	I_{LIH}	$V_I = V_{DD}$			10	μA
Low Level Input Leakage Current	I_{LIL}	$V_I = 0V$			10	μA
High Level Output Voltage	V_{OH}	$I_{OH} = -500\mu A$	$V_{DD} - 0.5$			V
Low Level Output Voltage	V_{OL}	$I_{OL} = 500\mu A$			0.5	V
High Level Output Leakage Current	I_{LOH}	$V_O = V_{DD}$			10	μA
Low Level Output Leakage Current	I_{LOL}	$V_O = 0V$			10	μA
Supply Current	I_{DD}	$f_{SCP} = 1.0MHz$			500	μA
LCD Drive Current	I_{LC}	$f_{LP} = 2.4kHz$ $V_{LC} = 7.3V$		500	2,000	μA

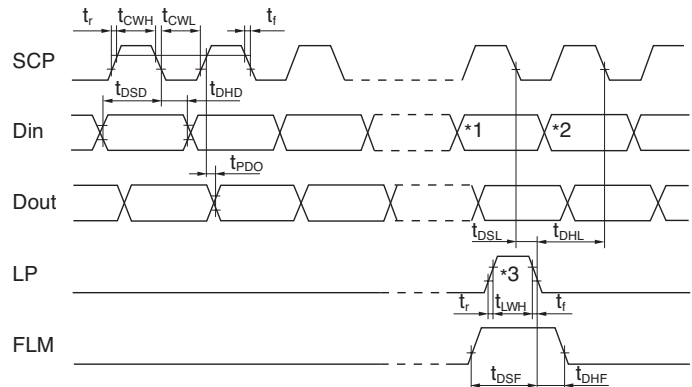
Timing Characteristics of LCD Drive IC

(Temperature at -20°C to +60°C and $V_{DD} = 5.0V \pm 10\%$)

Items	Symbols	Minimum	Maximum
Clock Operation Frequency	f_{SCP}		8.0MHz
Latch Pulse Frequency	f_{LP}		50kHz
Clock High Level Pulse Width	t_{CWH}	50ns	
Clock Low Level Pulse Width	t_{CWL}	50ns	
Data Setup Time	t_{DSD}	45ns	
Data Hold Time	t_{DHD}	50ns	
Data Output Delay Time	t_{PDO}		25ns
Latch Setup Time	t_{DSL}	50ns	
Latch Hold Time	t_{DHL}	50ns	
Latch High Level Width	t_{LWH}	50ns	
FLM Setup Time	t_{DSF}	50ns	
FLM Hold Time	t_{DHF}	50ns	
SCP, LP Rise/Fall Time	t_r/t_f		15ns

Timing Diagram

- *1 Last data on first line
- *2 Beginning data on second line
- *3 Location of LP signal on first line



LED CHARACTERISTICS

Typical Electrical Characteristics (Temperature at 25°C)

Backlight Color	Symbols	Red	Green	Blue	Red/Green	Unit
Forward Current	I_F	10	8.5	8.0	15/15	mA

ABSOLUTE MAXIMUM FOR LEDs

Electrical Characteristics (Temperature at 25°C)

Backlight Color	Symbols	Red	Green	Blue	Red/Green	Unit
Forward Current	I_F	20	20	20	20	mA
Forward Voltage	V_F	2.0 ($I_F = 10mA$)	2.8 ($I_F = 8.5mA$)	2.8 ($I_F = 8.0mA$)	1.9/1.9	V
Reverse Voltage	V_R	4.0	4.0	4.0	4.0	V
Current Reduction Rate Above 25°C	$\Delta I_F(DC)$	-0.33	-0.33	-0.33	-0.26	mA/°C
*Power Dissipation (LED Overall 115mW)	P_D	40	60	60	130 maximum	mW

*For uniform light emission, Power Dissipation should not exceed the Absolute Maximum Rating, and the Forward Current should not exceed the derated Absolute Forward Current.

PRECAUTIONS FOR HANDLING & STORAGE OF LCD 36 x 24 DEVICES

Handling



1. The IS Series devices are electrostatic sensitive.
2. Limit operating force to keytop to 100.0N maximum, as excessive pressure may damage the LCD device.
3. The IS series devices are not process sealed.
4. If the LCD is accidentally broken, avoid contact with the liquid and wash off any liquid spills to the skin or clothing.
5. Clean cap surface with dry cloth. If further cleaning is needed, wipe with dampened cloth using neutral cleanser and dry with clean cloth. Do not use organic solvent.
6. Recommended soldering time and temperature limits:
 Do not exceed 70°C at the LCD level.
 Wave Soldering: see Profile B in the Supplement section.
 Manual Soldering for Switch: see Profile A in the Supplement section.
 Manual Soldering for Display: see Profile B in the Supplement section.
7. Recommendation for backlight color uniformity: Use constant current driver. For current limiting resistor method, the power source should be at least twice the backlight LED forward voltage.
8. The VLC voltage should not be applied before logic voltage. If VLC voltage is present before logic voltage, it may cause the driver logic to freeze and damage the LCD, and the driver logic may become damaged.
9. Backlight Forward Current should not exceed the derated Absolute Maximum Forward Current based on the temperature.
10. Excessive images may result after the same image is emitted continuously for an extended period of time.

Storage

1. Store in original container and away from direct sunlight.
2. Keep away from static electricity.
3. Avoid extreme temperatures, high humidity, gaseous substances, and all forms of chemical contamination.

Toggles

Rockers

Pushbuttons

Illuminated PB

E Programmable

Keylocks

Rotaries

Slides

Tactiles

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