

High Drive Fundamental Quartz Crystal Oscillator

■GENERAL DESCRIPTION

The NJU6360 series is a C-MOS fundamental quartz crystal oscillator that consists of an oscillation amplifier, 3-stage divider and 3-state output buffer.

The 5-stage divider generates only one frequency selected of $f_0, f_0/2, f_0/4, f_0/8, f_0/16$ and $f_0/32$ by internal circuits is output.

The oscillation amplifier is realized very low stand-by current using NAND circuit.

The 3-state output buffer is C-MOS compatible and can drive 50pF(@5V) C-MOS load.

■PACKAGE OUTLINE

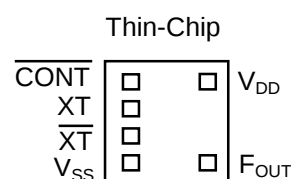


NJU6360XC-D

■FEATURES

- Operating Voltage 2.7 to 5.5V
- Maximum Oscillation Frequency 50MHz
- Low Operating Current
- High Fan-out $I_{OH}/I_{OL}=8mA@3.3V$
 $I_{OH}/I_{OL}=16mA@5.0V$
- 5-Stage Divider Maximum Divider $f_0/32$
- Oscillation Stop and Output Stand-by Function
- 3-State Output Buffer
- Oscillation Capacitors C_g and C_d on-chip
- Package Outline Thin-Chip
- C-MOS Technology

■PAD LOCATION



■LINE-UP TABLE

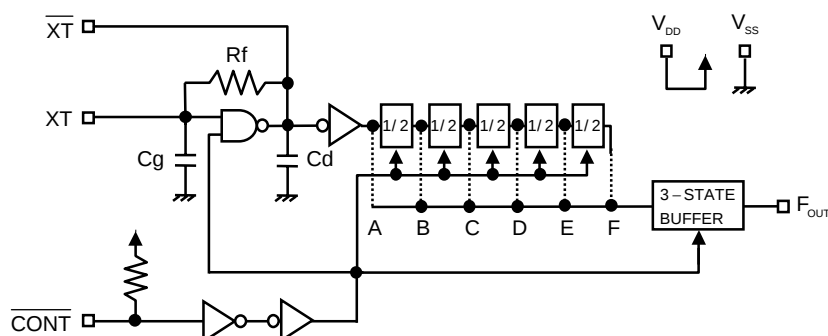
Type No.	F_{OUT}	Internal Connect	C_g/C_d
NJU6360	A	f_0	Connected A Line
	B	$f_0/2$	Connected B Line
	C	$f_0/4$	Connected C Line
	D	$f_0/8$	Connected D Line
	E	$f_0/16$	Connected E Line
	F	$f_0/32$	Connected F Line

■COORDINATES

No	Pad Name	X	Y
1	$\overline{CONT}$	-178	231
2	XT	-178	77
3	$\overline{XT}$	-178	-77
4	V_{SS}	-178	-231
5	F_{OUT}	206	-231
6	V_{DD}	206	231

Starting Point: Chip Center Unit[um]
 Chip Size: 0.7x0.75mm
 Thin-Chip Thickness(-D): 200±20um
 Pad Size: 90x90um

■BLOCK DIAGRAM



■ TERMINAL DESCRIPTION

SYMBOL	FUNCTION	
$\overline{\text{CONT}}$	Oscillation and 3-state Output Buffer Control	
	$\overline{\text{CONT}}$	F_{OUT}
	H or OPEN	Output either one frequency selected of f_0 , $f_0/2$, $f_0/4$, $f_0/8$, $f_0/16$ and $f_0/32$ Note1)
	L	Oscillation Stop and High impedance Output
$\overline{\text{XT}}$	Quartz Crystal Connecting Terminals	
$\overline{\text{XT}}$		
V_{SS}	$V_{\text{SS}}=0\text{V}$	
F_{OUT}	Frequency Output	
V_{DD}	$V_{\text{DD}}=2.5/3.0\text{V}/5.0\text{V}$	

Note1) Refer to the line-up table.

■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATING	UNIT
Supply Voltage	V_{DD}	-0.5 to +7.0	V
Input Voltage	V_{IN}	$V_{\text{SS}}-0.5$ to $V_{\text{DD}}+0.5$	V
Output Voltage	V_{O}	-0.5 to $V_{\text{DD}}+0.5$	V
Input Current	I_{IN}	± 10	MA
Output Current	I_{O}	± 25	MA
Operating Temperature Range	Topr	-40 to +85	°C
Storage Temperature Range	Tstg	-55 to +125	°C

Note2) If the supply voltage(V_{DD}) is less than 7.0V, the input voltage must not over the V_{DD} level though 7.0V is limit specified.

Note3) Decoupling capacitor should be connected between V_{DD} and V_{SS} due to the stabilized operation for the circuit.

■ELECTRICAL CHARACTERISTICS

(Ta=25°C)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Operating Voltage	V_{DD}		2.7		5.5	V

($V_{DD}=3.3V$, Ta=25°C)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Operating Current	I_{DD}	A version, fosc=16MHz, $C_L=30pF$			8	mA
		B version, fosc=16MHz, $C_L=30pF$			6	
		C version, fosc=16MHz, $C_L=30pF$			4	
		D version, fosc=16MHz, $C_L=30pF$			3	
		E version, fosc=16MHz, $C_L=30pF$			3	
		F version, fosc=16MHz, $C_L=30pF$			3	
Oscillation Stopping Current	I_{STB}	$\overline{CONT} = V_{SS}$, No load		2	5	uA
Stand-by Current	I_{st}	$\overline{CONT} = XT = V_{SS}$, No load Note4)			1	uA
Input Voltage	V_{IH}		2.31		3.3	V
	V_{IL}		0		0.99	V
Output Current	I_{OH}	$V_{OH}=2.97V$	8			mA
	I_{OL}	$V_{OL}=0.33V$	8			mA
Input Current	I_{IN}	$\overline{CONT} = 0.8V_{DD}$		10.0	15.0	uA
		$\overline{CONT} = 0.2V_{DD}$		1.8	3.0	uA
3-state Off Leakage Current	I_{OZ}	$\overline{CONT} = V_{SS}$, $F_{OUT} = V_{DD}$ or V_{SS}			±0.1	uA
Feedback Resistance	R_f			255		KΩ
Internal Capacitor	C_g/C_d	fosc=16MHz, A/B/C/D/E/F version		15/15		pF
Maximum Oscillation Frequency	F_{MAX}		50			MHz
Output Signal Symmetry	SYM	$C_L=15pF$, @ $V_{DD}/2$	45	50	55	%
		$C_L=30pF$, @ $V_{DD}/2$	45	50	55	
Output Signal Rise Time	t_r	$C_L=15pF$, 10% to 90%		3	6	ns
		$C_L=30pF$, 10% to 90%		4	8	
Output Signal Fall Time	t_f	$C_L=15pF$, 90% to 10%		3	6	ns
		$C_L=30pF$, 90% to 10%		4	8	
Output Disable time	T_{PLZ}	$C_L=15pF$, $R_{UP}=10k\Omega$			150	Ns
Output Enable Time	T_{PZL}	$C_L=15pF$, $R_{UP}=10k\Omega$			150	Ns

Note4) Excluding input current on $\overline{CONT}$ Terminal.

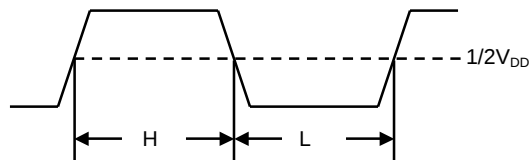
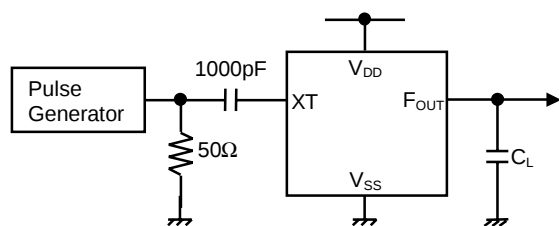
(V_{DD}=5.0V, Ta=25°C)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Operating Current	I _{DD}	A version, fosc=16MHz, C _L =50pF			15	mA
		B version, fosc=16MHz, C _L =50pF			11	
		C version, fosc=16MHz, C _L =50pF			9	
		D version, fosc=16MHz, C _L =50pF			7	
		E version, fosc=16MHz, C _L =50pF			7	
		F version, fosc=16MHz, C _L =50pF			7	
Oscillation Stopping Current	I _{STB}	CONT = V _{SS} , No load		5	10	UA
Stand-by Current	I _{st}	CONT = XT = V _{SS} , No load Note4)			1	UA
Input Voltage	V _{IH}		3.5		5.0	V
	V _{IL}	V _{OH} =4.5V	0		1.5	V
Output Current	I _{OH}	V _{OL} =0.5V	12			mA
	I _{OL}	CONT = 0.8V _{DD}	12			mA
Input Current	I _{IN}	CONT = 0.2V _{DD}		27.0	40.0	UA
		CONT = V _{SS} , F _{OUT} = V _{DD} or V _{SS}		5.5	8.0	uA
3-state Off Leakage Current	I _{OZ}				±0.1	uA
Feedback Resistance	R _f			255		KΩ
Internal Capacitor	C _g /C _d	fosc=16MHz, A/B/C/D/E/F version		15/15		pF
Maximum Oscillation Frequency	F _{MAX}		50			MHz
Output Signal Symmetry	SYM	C _L =15pF, @V _{DD} /2	45	50	55	%
		C _L =50pF, @V _{DD} /2	45	50	55	
Output Signal Rise Time	tr	C _L =15pF, 10% to 90%		3	6	ns
		C _L =50pF, 10% to 90%		4	8	
Output Signal Fall Time	tf	C _L =15pF, 90% to 10%		3	6	ns
		C _L =50pF, 90% to 10%		4	8	
Output Disable time	T _{PLZ}	C _L =15pF, R _{Up} =10kΩ			100	ns
Output Enable Time	T _{PZL}	C _L =15pF, R _{Up} =10kΩ			100	ns

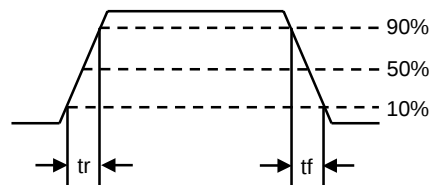
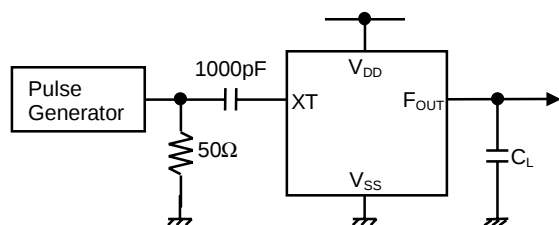
Note4) Excluding input current on CONT Terminal.

MEASUREMENT CIRCUITS

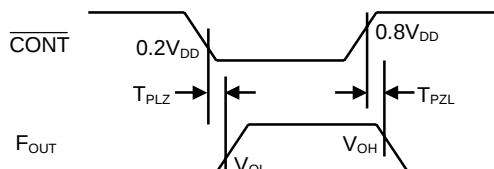
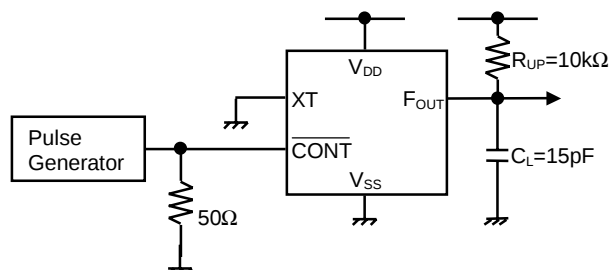
(1) Output Signal Symmetry ($C_L=15/30/50\text{pF}$)



(2) Output Signal Rise/Fall Time ($C_L=15/30/50\text{pF}$)



(3) Output Disable/Enable Time ($C_L=15\text{pF}, R_{UP}=10\text{k}\Omega$)



[CAUTION]
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