

# NHD-0440AZ-FL-YBW

## Character Liquid Crystal Display Module

|       |                            |
|-------|----------------------------|
| NHD-  | Newhaven Display           |
| 0440- | 4 Lines x 40 Characters    |
| AZ-   | Model                      |
| F-    | Transflective              |
| L-    | Yellow/Green LED Backlight |
| Y-    | STN – Yellow/Green         |
| B-    | 6:00 Optimal View          |
| W-    | Wide Temp.                 |
|       | <b>RoHS Compliant</b>      |

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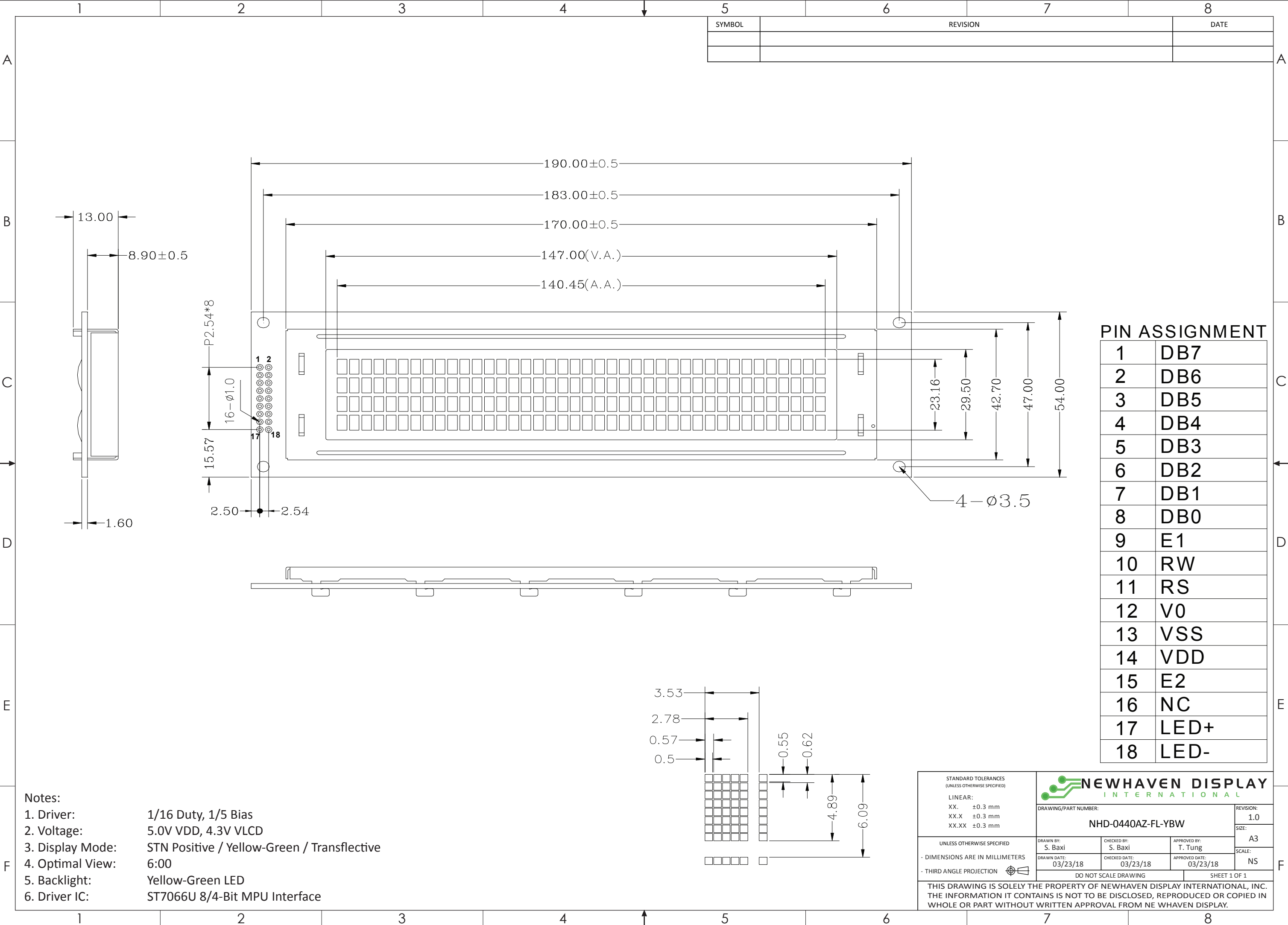
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## Document Revision History

| Revision | Date     | Description                                            | Changed by |
|----------|----------|--------------------------------------------------------|------------|
| 0        | 10/21/08 | Initial Release                                        | -          |
| 1        | 11/2/09  | User Guide Reformat                                    | MC         |
| 2        | 11/16/09 | Updated Block diagram and initialization code          | MC         |
| 3        | 12/16/09 | Updated Backlight Supply Current Information           | MC         |
| 4        | 1/05/11  | Updated Controller Information                         | JT         |
| 5        | 5/6/11   | Electrical characteristics updated                     | AK         |
| 6        | 8/2/16   | Mechanical Drawing, Electrical & Optical Char. Updated | SB         |
| 7        | 9/26/16  | Supply Current Updated                                 | SB         |
| 8        | 3/23/18  | Electrical Characteristics Updated                     | SB         |

## Functions and Features

- 4 lines x 40 characters
- 2 Built-in controllers (ST7066U)
- +5.0V Power Supply
- 1/16 duty, 1/5 bias
- RoHS compliant



| SYMBOL | REVISION | DATE |
|--------|----------|------|
|        |          |      |
|        |          |      |

STANDARD TOLERANCES  
(UNLESS OTHERWISE SPECIFIED)  
  
LINEAR:  
XX.     ±0.3 mm  
XX.X   ±0.3 mm  
XX.XX  ±0.3 mm

UNLESS OTHERWISE SPECIFIED  
- DIMENSIONS ARE IN MILLIMETERS  
- THIRD ANGLE PROJECTION

DRAWING/PART NUMBER:  
**NHD-0440AZ-FL-YBW**

DRAWN BY:  
S. Baxi

CHECKED DATE:  
03/23/18

CHECKED BY:  
S. Baxi

CHECKED DATE:  
03/23/18

APPROVED BY:  
T. Tung

APPROVED DATE:  
03/23/18

DO NOT SCALE DRAWING

SHEET 1 OF 1

REVISION:  
1.0  
SIZE:  
A3  
SCALE:  
NS

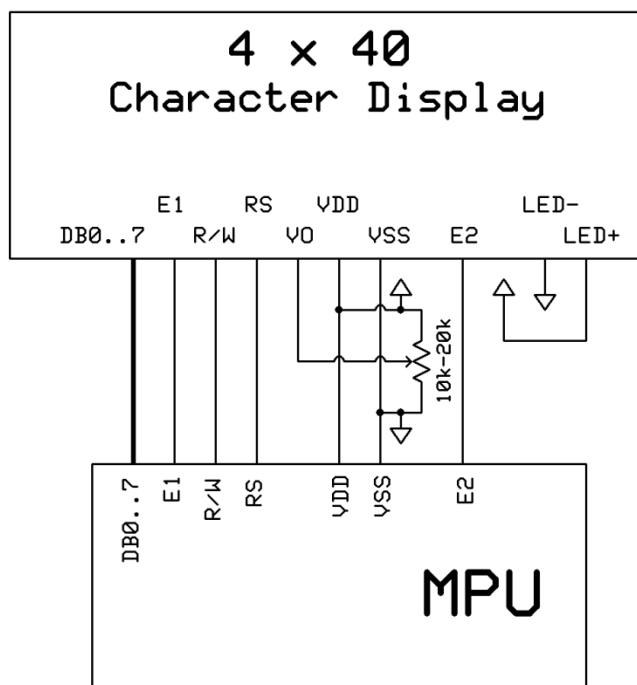
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## Pin Description and Wiring Diagram

| Pin No. | Symbol          | External Connection | Function Description                                                                                      |
|---------|-----------------|---------------------|-----------------------------------------------------------------------------------------------------------|
| 1-4     | DB7-DB4         | MPU                 | Four high order bi-directional three-state data bus lines.                                                |
| 5-8     | DB3-DB0         | MPU                 | Four low order bi-directional three-state data bus lines. These four are not used during 4-bit operation. |
| 9       | E1              | MPU                 | Operation enable signal. Falling edge triggered for top 2 lines.                                          |
| 10      | R/W             | MPU                 | Read/Write select signal, R/W=1: Read R/W=0: Write                                                        |
| 11      | RS              | MPU                 | Register Select signal. RS=0: Command, RS=1: Data                                                         |
| 12      | V <sub>0</sub>  | Power Supply        | Supply Voltage for contrast (approx. 0.5V)                                                                |
| 13      | V <sub>SS</sub> | Power Supply        | Ground                                                                                                    |
| 14      | V <sub>DD</sub> | Power Supply        | Supply Voltage for logic (+5.0V)                                                                          |
| 15      | E2              | MPU                 | Operation Enable signal. Falling edge triggered for bottom 2 lines.                                       |
| 16      | NC              | -                   | No Connect                                                                                                |
| 17      | LED+            | Power Supply        | Backlight Anode (+5.0V via on board resistor)                                                             |
| 18      | LED-            | Power Supply        | Backlight Cathode (Ground)                                                                                |

**Recommended LCD connector:** 2.54mm pitch pins

**Backlight connector:** ---      **Mates with:** ---



## Electrical Characteristics

| Item                        | Symbol           | Condition                                        | Min.                | Typ. | Max.            | Unit |
|-----------------------------|------------------|--------------------------------------------------|---------------------|------|-----------------|------|
| Operating Temperature Range | T <sub>OP</sub>  | Absolute Max                                     | -20                 | -    | +70             | °C   |
| Storage Temperature Range   | T <sub>ST</sub>  | Absolute Max                                     | -30                 | -    | +80             | °C   |
| Supply Voltage              | V <sub>DD</sub>  | -                                                | 4.8                 | 5.0  | 5.2             | V    |
| Supply Current              | I <sub>DD</sub>  | V <sub>DD</sub> = 5.0V<br>T <sub>OP</sub> = 25°C | 2                   | 3    | 5               | mA   |
| Supply for LCD (contrast)   | V <sub>LCD</sub> |                                                  | 4.1                 | 4.3  | 4.5             | V    |
| "H" Level input             | V <sub>IH</sub>  | -                                                | 0.7*V <sub>DD</sub> | -    | V <sub>DD</sub> | V    |
| "L" Level input             | V <sub>IL</sub>  | -                                                | V <sub>SS</sub>     | -    | 0.6             | V    |
| "H" Level output            | V <sub>OH</sub>  | -                                                | 3.9                 | -    | V <sub>DD</sub> | V    |
| "L" Level output            | V <sub>OL</sub>  | -                                                | V <sub>SS</sub>     | -    | 0.4             | V    |
|                             |                  |                                                  |                     |      |                 |      |
| Backlight Supply Voltage    | V <sub>LED</sub> | -                                                | 4.8                 | 5.0  | 5.2             | V    |
| Backlight Supply Current    | I <sub>LED</sub> | V <sub>LED</sub> = 5.0V                          | 240                 | 320  | 480             | mA   |

## Optical Characteristics

| Item                   |        | Symbol         | Condition              | Min. | Typ. | Max. | Unit |
|------------------------|--------|----------------|------------------------|------|------|------|------|
| Optimal Viewing Angles | Top    | φY+            | CR ≥ 2                 | -    | 40   | -    | °    |
|                        | Bottom | φY-            |                        | -    | 60   | -    | °    |
|                        | Left   | θX-            |                        | -    | 60   | -    | °    |
|                        | Right  | θX+            |                        | -    | 60   | -    | °    |
| Contrast Ratio         |        | CR             | -                      | 2    | 5    | -    | -    |
| Response Time          | Rise   | T <sub>R</sub> | T <sub>OP</sub> = 25°C | -    | 120  | 150  | ms   |
|                        | Fall   | T <sub>F</sub> |                        | -    | 120  | 150  | ms   |

## Controller Information

Built-in ST7066U controller.

Please download specification at [http://www.newhavendisplay.com/app\\_notes/ST7066U.pdf](http://www.newhavendisplay.com/app_notes/ST7066U.pdf)

## DDRAM Address

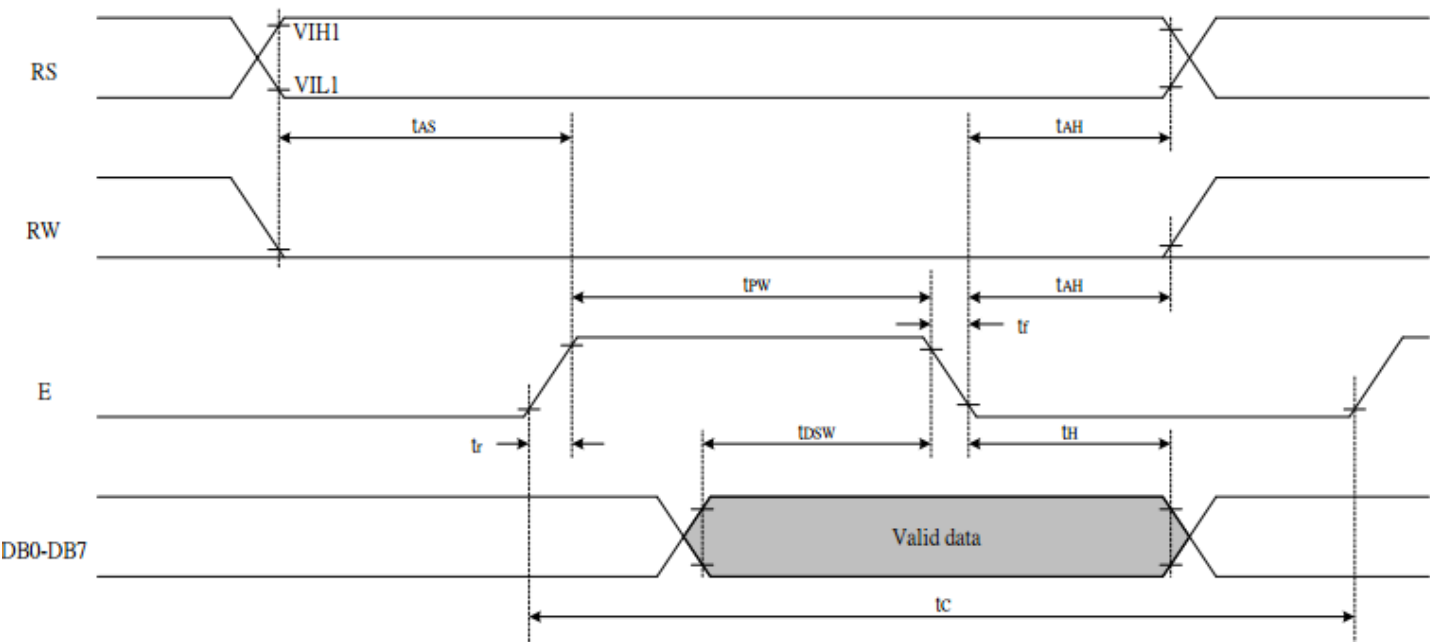
| 1  | 2  | 3  | 4  | 5  | - | - | - | - | - | - | - | - | - | - | 36 | 37 | 38 | 39 | 40 |
|----|----|----|----|----|---|---|---|---|---|---|---|---|---|---|----|----|----|----|----|
| 00 | 01 | 02 | 03 | 04 | - | - | - | - | - | - | - | - | - | - | 23 | 24 | 25 | 26 | 27 |
| 40 | 41 | 42 | 43 | 44 | - | - | - | - | - | - | - | - | - | - | 63 | 64 | 65 | 66 | 67 |
| 00 | 01 | 02 | 03 | 04 | - | - | - | - | - | - | - | - | - | - | 23 | 24 | 25 | 26 | 27 |
| 40 | 41 | 42 | 43 | 44 | - | - | - | - | - | - | - | - | - | - | 63 | 64 | 65 | 66 | 67 |

## Table of Commands

| Instruction                | Instruction code |     |     |     |     |     |     |     |     |     | Description                                                                                                                      | Execution time (f <sub>osc</sub> = 270 KHZ) |
|----------------------------|------------------|-----|-----|-----|-----|-----|-----|-----|-----|-----|----------------------------------------------------------------------------------------------------------------------------------|---------------------------------------------|
|                            | RS               | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |                                                                                                                                  |                                             |
| Clear Display              | 0                | 0   | 0   | 0   | 0   | 0   | 0   | 0   | 0   | 1   | Write "20H" to DDRAM and set DDRAM address to "00H" from AC                                                                      | 1.52ms                                      |
| Return Home                | 0                | 0   | 0   | 0   | 0   | 0   | 0   | 0   | 1   | -   | Set DDRAM Address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed. | 1.52ms                                      |
| Entry mode Set             | 0                | 0   | 0   | 0   | 0   | 0   | 0   | 1   | I/D | SH  | Sets cursor move direction and specifies display shift. These parameters are performed during data write and read.               | 37μs                                        |
| Display ON/OFF control     | 0                | 0   | 0   | 0   | 0   | 0   | 1   | D   | C   | B   | D=1: Entire display on<br>C=1: Cursor on<br>B=1: Blinking cursor on                                                              | 37μs                                        |
| Cursor or Display shift    | 0                | 0   | 0   | 0   | 0   | 1   | S/C | R/L | -   | -   | Sets cursor moving and display shift control bit, and the direction without changing DDRAM data.                                 | 37μs                                        |
| Function set               | 0                | 0   | 0   | 0   | 1   | DL  | N   | F   | -   | -   | DL: Interface data is 8/4 bits<br>N: Number of lines is 2/1<br>F: Font size is 5x11/5x8                                          | 37μs                                        |
| Set CGRAM Address          | 0                | 0   | 0   | 1   | AC5 | AC4 | AC3 | AC2 | AC1 | AC0 | Set CGRAM address in address counter                                                                                             | 37μs                                        |
| Set DDRAM Address          | 0                | 0   | 1   | AC6 | AC5 | AC4 | AC3 | AC2 | AC1 | AC0 | Set DDRAM address in address counter.                                                                                            | 37μs                                        |
| Read busy Flag and Address | 0                | 1   | BF  | AC6 | AC5 | AC4 | AC3 | AC2 | AC1 | AC0 | Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.           | 0s                                          |
| Write data To Address      | 1                | 0   | D7  | D6  | D5  | D4  | D3  | D2  | D1  | D0  | Write data into internal RAM (DDRAM/CGRAM).                                                                                      | 37μs                                        |
| Read data From RAM         | 1                | 1   | D7  | D6  | D5  | D4  | D3  | D2  | D1  | D0  | Read data from internal RAM (DDRAM/CGRAM).                                                                                       | 37μs                                        |

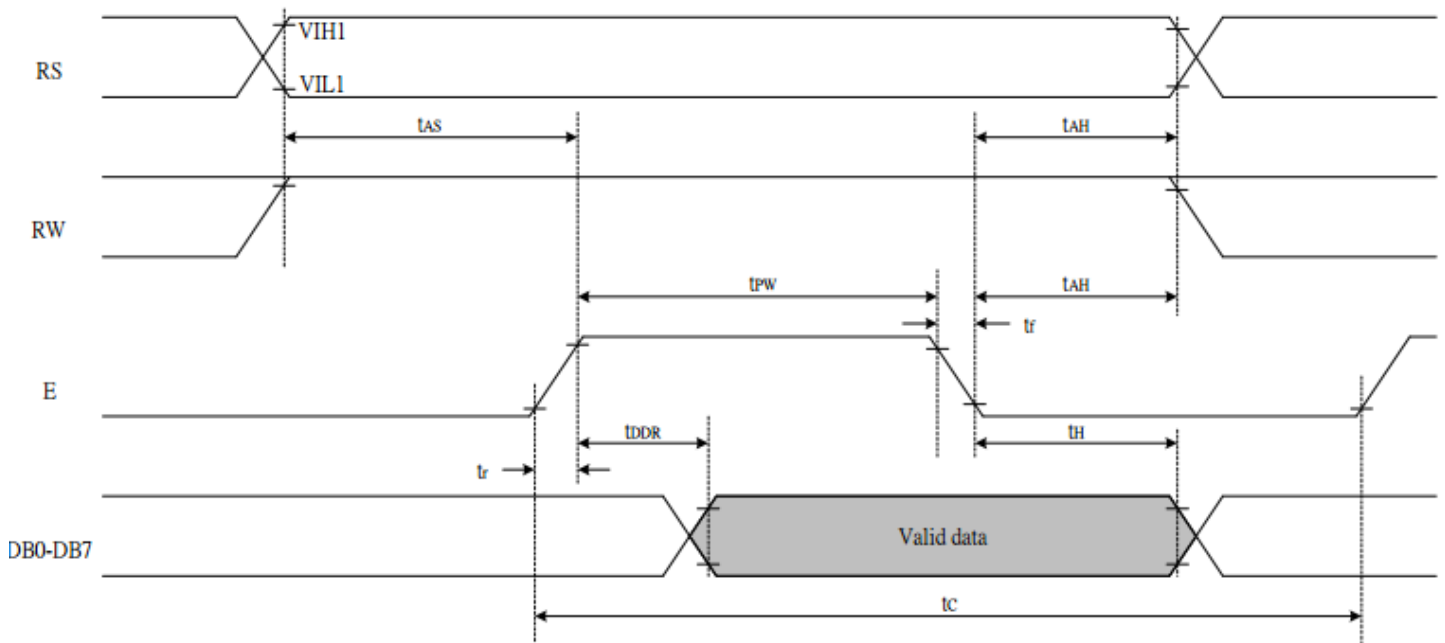
# Timing Characteristics

## Writing data from MPU to ST7066U



| Write Mode (Writing data from MPU to ST7066U) |                       |                 |      |   |    |    |
|-----------------------------------------------|-----------------------|-----------------|------|---|----|----|
| $T_C$                                         | Enable Cycle Time     | Pin E           | 1200 | - | -  | ns |
| $T_{PW}$                                      | Enable Pulse Width    | Pin E           | 140  | - | -  | ns |
| $T_R, T_F$                                    | Enable Rise/Fall Time | Pin E           | -    | - | 25 | ns |
| $T_{AS}$                                      | Address Setup Time    | Pins: RS,RW,E   | 0    | - | -  | ns |
| $T_{AH}$                                      | Address Hold Time     | Pins: RS,RW,E   | 10   | - | -  | ns |
| $T_{DSW}$                                     | Data Setup Time       | Pins: DB0 - DB7 | 40   | - | -  | ns |
| $T_H$                                         | Data Hold Time        | Pins: DB0 - DB7 | 10   | - | -  | ns |

## Reading data from ST7066U to MPU



| <i>Read Mode (Reading Data from ST7066U to MPU)</i> |                       |                 |      |   |     |    |
|-----------------------------------------------------|-----------------------|-----------------|------|---|-----|----|
| $T_C$                                               | Enable Cycle Time     | Pin E           | 1200 | - | -   | ns |
| $T_{PW}$                                            | Enable Pulse Width    | Pin E           | 140  | - | -   | ns |
| $T_{R,T_F}$                                         | Enable Rise/Fall Time | Pin E           | -    | - | 25  | ns |
| $T_{AS}$                                            | Address Setup Time    | Pins: RS,RW,E   | 0    | - | -   | ns |
| $T_{AH}$                                            | Address Hold Time     | Pins: RS,RW,E   | 10   | - | -   | ns |
| $T_{DDR}$                                           | Data Setup Time       | Pins: DB0 - DB7 | -    | - | 100 | ns |
| $T_H$                                               | Data Hold Time        | Pins: DB0 - DB7 | 10   | - | -   | ns |

## Built-in Font Table

| b7-b4<br>b3-b0 | 0000             | 0001 | 0010 | 0011 | 0100 | 0101 | 0110 | 0111 | 1000 | 1001 | 1010 | 1011 | 1100 | 1101 | 1110 | 1111 |
|----------------|------------------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|
| 0000           | CG<br>RAM<br>(1) |      |      | 0    | a    | P    | ˘    | F    |      |      |      | —    | 9    | 3    | a    | p    |
| 0001           | (2)              |      | !    | 1    | A    | Q    | a    | A    |      |      | •    | 7    | 7    | 4    | ä    | q    |
| 0010           | (3)              |      | "    | 2    | B    | R    | b    | r    |      |      | "    | 4    | W    | ×    | ß    | ø    |
| 0011           | (4)              |      | #    | 3    | C    | S    | c    | s    |      |      | ¡    | 9    | 7    | e    | z    | ×    |
| 0100           | (5)              |      | \$   | 4    | D    | T    | d    | t    |      |      | √    | I    | †    | þ    | µ    | ø    |
| 0101           | (6)              |      | %    | 5    | E    | U    | e    | u    |      |      | •    | †    | †    | 1    | æ    | 0    |
| 0110           | (7)              |      | &    | 6    | F    | V    | f    | v    |      |      | 7    | †    | 二    | ヨ    | ρ    | Σ    |
| 0111           | (8)              |      | '    | 7    | G    | W    | g    | w    |      |      | 7    | †    | 又    | う    | g    | π    |
| 1000           | (1)              |      | <    | 8    | H    | X    | h    | x    |      |      | 4    | 9    | *    | リ    | フ    | 又    |
| 1001           | (2)              |      | >    | 9    | I    | Y    | i    | y    |      |      | 6    | 7    | ノ    | ル    | マ    | y    |
| 1010           | (3)              |      | *    | :    | J    | Z    | j    | z    |      |      | ±    | コ    | ∩    | レ    | j    | ¥    |
| 1011           | (4)              |      | +    | :    | K    | L    | k    | l    |      |      | *    | 9    | ヒ    | ロ    | ×    | ¥    |
| 1100           | (5)              |      | ,    | <    | L    | ¥    | l    | l    |      |      | †    | 9    | フ    | ワ    | ¢    | ¥    |
| 1101           | (6)              |      | —    | =    | N    | I    | n    | }    |      |      | ユ    | ズ    | △    | コ    | ±    | ÷    |
| 1110           | (7)              |      | .    | >    | N    | ^    | n    | →    |      |      | ヨ    | ヒ    | ホ    | °    | ん    |      |
| 1111           | (8)              |      | /    | ?    | 0    | _    | o    | ←    |      |      | 6    | 9    | マ    | "    | 6    | ■    |

## Example Initialization Program

```
/******  
void command1(char i)          //Top half of the display  
{  
    P1 = i;  
    W = 0;  
    RS = 0;  
    E1 = 1;  
    delay(2);  
    E1 = 0;  
}  
void command2(char i)          //Bottom half of the display  
{  
    P1 = i;  
    W = 0;  
    RS = 0;  
    E2 = 1;  
    delay(2);  
    E2 = 0;  
}  
/******  
void writedata1(char i)        //Top half of the display  
{  
    P1 = i;  
    W = 0;  
    RS = 1;  
    E1 = 1;  
    delay(2);  
    E1 = 0;  
}  
void writedata2(char i)        //Bottom half of the display  
{  
    P1 = i;  
    W = 0;  
    RS = 1;  
    E2 = 1;  
    delay(2);  
    E2 = 0;  
}  
/******  
void init_LCD()  
{  
    delay(15);  
    command1(0x30);    //Wake up  
    command2(0x30);  
    delay(5);  
    command1(0x30);    //Wake up  
    command2(0x30);  
    delay(5);  
    command1(0x30);    //Wake up  
    command2(0x30);  
    delay(5);  
    command1(0x38);    //Function Set = 8bit mode; 2-line; 5x8  
    command2(0x38);  
    command1(0x08);    //Turn off display  
    command2(0x08);  
    command1(0x01);    //Clear display  
    command2(0x01);  
    command1(0x06);    //Entry mode cursor increment  
    command2(0x06);  
    command1(0x0c);    //Turn on display; no cursor  
    command2(0x0c);  
}  
/******
```

## Quality Information

| Test Item                             | Content of Test                                                                                                                 | Test Condition                                                                      | Note |
|---------------------------------------|---------------------------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------|------|
| High Temperature storage              | Endurance test applying the high storage temperature for a long time.                                                           | +80°C , 48hrs                                                                       | 2    |
| Low Temperature storage               | Endurance test applying the low storage temperature for a long time.                                                            | -30°C , 48hrs                                                                       | 1,2  |
| High Temperature Operation            | Endurance test applying the electric stress (voltage & current) and the high thermal stress for a long time.                    | +70°C 48hrs                                                                         | 2    |
| Low Temperature Operation             | Endurance test applying the electric stress (voltage & current) and the low thermal stress for a long time.                     | -20°C , 48hrs                                                                       | 1,2  |
| High Temperature / Humidity Operation | Endurance test applying the electric stress (voltage & current) and the high thermal with high humidity stress for a long time. | +40°C , 90% RH , 48hrs                                                              | 1,2  |
| Thermal Shock resistance              | Endurance test applying the electric stress (voltage & current) during a cycle of low and high thermal stress.                  | 0°C,30min -> 25°C,5min -> 50°C,30min = 1 cycle<br>10 cycles                         |      |
| Vibration test                        | Endurance test applying vibration to simulate transportation and use.                                                           | 10-55Hz , 15mm amplitude.<br>60 sec in each of 3 directions X,Y,Z<br>For 15 minutes | 3    |
| Static electricity test               | Endurance test applying electric static discharge.                                                                              | VS=800V, RS=1.5kΩ, CS=100pF<br>One time                                             |      |

**Note 1:** No condensation to be observed.

**Note 2:** Conducted after 4 hours of storage at 25°C, 0%RH.

**Note 3:** Test performed on product itself, not inside a container.

## Precautions for using LCDs/LCMs

See Precautions at [www.newhavendisplay.com/specs/precautions.pdf](http://www.newhavendisplay.com/specs/precautions.pdf)

## Warranty Information and Terms & Conditions

[http://www.newhavendisplay.com/index.php?main\\_page=terms](http://www.newhavendisplay.com/index.php?main_page=terms)

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