

PHOTOCOUPLER

PS9552,PS9552L1,PS9552L2,PS9552L3

2.5 A OUTPUT CURRENT, HIGH CMR IGBT GATE DRIVE PHOTOCOUPLER 8-PIN DIP PHOTOCOUPLER

-NEPOC Series-

DESCRIPTION

The PS9552, PS9552L1, PS9552L2 and PS9552L3 are optically coupled isolators containing a GaAlAs LED on the input side and a photo diode, a signal processing circuit and a power output transistor on the output side on one chip.

The PS9552 Series is designed specifically for high common mode transient immunity (CMR), high output current and high switching speed.

The PS9552 Series is suitable for driving IGBTs and MOS FETs.

The PS9552 Series is in a plastic DIP (Dual In-line Package).

The PS9552L1 is lead bending type for long creepage distance.

The PS9552L2 is lead bending type for long creepage distance (Gull-wing) for surface mount.

The PS9552L3 is lead bending type (Gull-wing) for surface mounting.

FEATURES

- Long creepage distance (8 mm MIN.: PS9552L1, PS9552L2)
- Large peak output current (2.5 A MAX., 2.0 A MIN.)
- High speed switching (tPLH, tPHL = 0.5 μ s MAX.)
- UVLO (Under Voltage Lock Out) protection with hysteresis
- High common mode transient immunity (CMH, CML = $\pm 25 \text{ kV}/\mu \text{s MIN.}$)
- Ordering number of tape product: PS9552L2-E3: 1 000 pcs/reel

: PS9552L3-E3: 1 000 pcs/reel

<R> • Pb-Free product

- Safety standards
 - UL approved: No. E72422
 - CSA approved: No. CA 101391 (CA5A, CAN/CSA-C22.2 60065, 60950)

DIN EN60747-5-2 (VDE0884 Part2) approved: No. 40019182 (Option)

- BSI approved: No. 8937, 8938
- SEMKO approved: No. 615433
- NEMKO approved: No. P06207243
- DEMKO approved: No. 314091
- FIMKO approved: No. FI 22827

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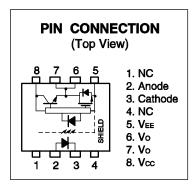
APPLICATIONS

- · IGBT, Power MOS FET Gate Driver
- · Industrial inverter
- IH (Induction Heating)

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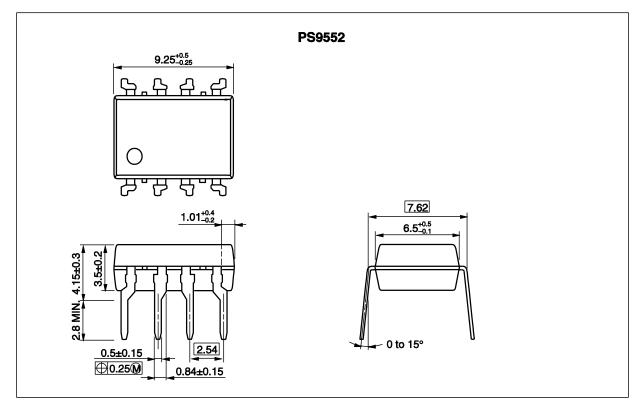
Document No. PN10589EJ07V0DS (7th edition) Date Published September 2009 NS

The mark <R> shows major revised points.

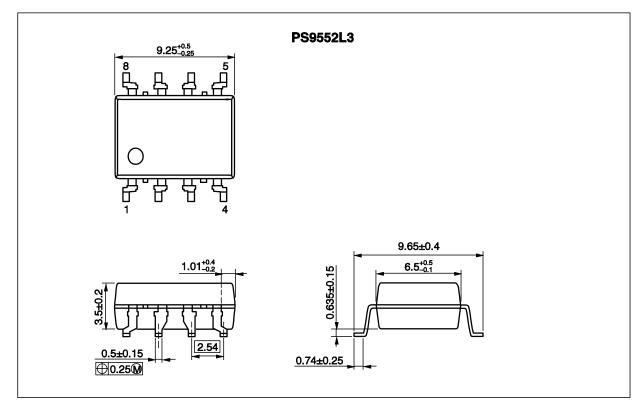


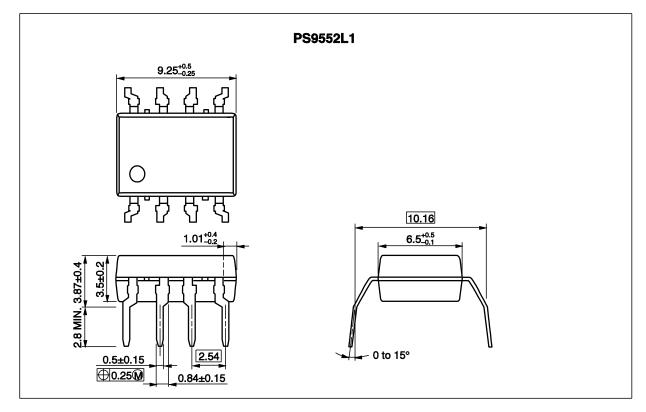
<R> PACKAGE DIMENSIONS (UNIT: mm)

DIP Type



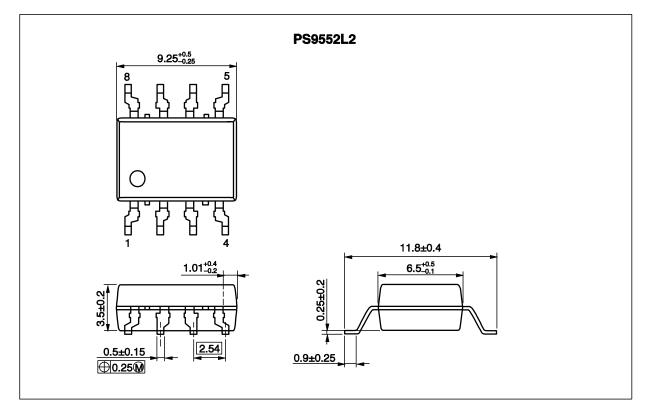
Lead Bending Type (Gull-wing) For Surface Mount





Lead Bending Type For Long Creepage Distance

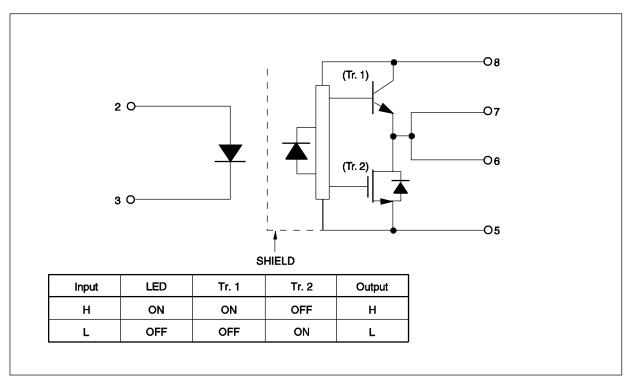
Lead Bending Type (Gull-wing) For Long Creepage Distance (Surface Mount)



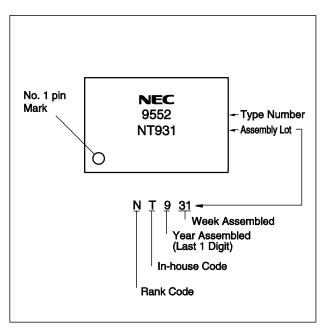
PHOTOCOUPLER CONSTRUCTION

Parameter	PS9552, PS9552L3	PS9552L1, PS9552L2
Air Distance (MIN.)	7 mm	8 mm
Outer Creepage Distance (MIN.)	7 mm	8 mm
Isolation Distance (MIN.)	0.4 mm	0.4 mm

FUNCTIONAL DIAGRAM



<R> MARKING EXAMPLE



Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number* ¹
PS9552	PS9552-AX	Pb-Free	Magazine case 50 pcs	Standard products	PS9552
PS9552L1	PS9552L1-AX	(Ni/Pd/Au)		(UL, CSA, BSI,	PS9552L1
PS9552L2	PS9552L2-AX			SEMKO, NEMKO,	PS9552L2
PS9552L3	PS9552L3-AX			DEMKO, FIMKO	PS9552L3
PS9552L2-E3	PS9552L2-E3-AX		Embossed Tape 1 000 pcs/reel	approved)	PS9552L2
PS9552L3-E3	PS9552L3-E3-AX				PS9552L3
PS9552-V	PS9552-V-AX		Magazine case 50 pcs	DIN EN60747-5-2	PS9552
PS9552L1-V	PS9552L1-V-AX			(VDE0884 Part2)	PS9552L1
PS9552L2-V	PS9552L2-V-AX			Approved (Option)	PS9552L2
PS9552L3-V	PS9552L3-V-AX				PS9552L3
PS9552L2-V-E3	PS9552L2-V-E3-AX		Embossed Tape 1 000 pcs/reel		PS9552L2
PS9552L3-V-E3	PS9552L3-V-E3-AX				PS9552L3

ORDERING INFORMATION

*1 For the application of the Safety Standard, following part number should be used.

	Parameter	Symbol	Ratings	Unit
Diode	Forward Current	lf	25	mA
	Peak Transient Forward Current (Pulse Width < 1 µs)		1.0	A
	Reverse Voltage	VR	5	V
Detecto r	High Level Peak Output Current ^{*1}	Іон (реак)	2.5	A
	Low Level Peak Output Current ^{*1}	IOL (PEAK)	2.5	А
	Supply Voltage	(Vcc - Vee)	0 to 35	V
	Output Voltage	Vo	0 to Vcc	V
	Power Dissipation *2	Pc	250	mW
Isolation	Voltage *3	BV	5 000	Vr.m.s.
Total Power Dissipation *4		Рт	300	mW
Operating Frequency *5		f	50	kHz
Operating	Operating Ambient Temperature		-40 to +100	°C
Storage Temperature		Tstg	-55 to +125	°C

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C, unless otherwise specified)

- *1 Maximum pulse width = 10 μ s, Maximum duty cycle = 0.2%
- *2 Reduced to 4.8 mW/°C at $T_A = 70^{\circ}C$ or more.
- *3 AC voltage for 1 minute at $T_A = 25^{\circ}$ C, RH = 60% between input and output. Pins 1-4 shorted together, 5-8 shorted together.
- *4 Reduced to 5.4 mW/°C at $T_A = 70^{\circ}C$ or more.
- *5 IOH (PEAK) ≤ 2.0 A (≤ 0.3 μ s), IOL (PEAK) ≤ 2.0 A (≤ 0.3 μ s)

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	(Vcc - Vee)	15		30	V
Forward Current (ON)	IF (ON)	7	10	16	mA
Forward Voltage (OFF)	VF (OFF)	-2		0.8	V
Operating Ambient Temperature	TA	-40		100	°C

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ELECTRICAL CHARACTERISTICS (T_A = -40 to $+100^{\circ}$ C, V_{CC} = 15 to 30 V, I_F (ON) = 7 to 16 mA, V_F (OFF) = -2 to 0.8 V, V_{EE} = GND, unless otherwise specified)

	Parameter	Symbol	Conditions	MIN.	TYP.*1	MAX.	Unit
Diode	Forward Voltage	VF	I⊧ = 10 mA, T₄ = 25°C	1.3	1.65	2.1	V
	Input Capacitance	C⊪	f = 1 MHz, V _F = 0 V, T _A = 25°C		30		pF
Detector	High Level Output Current	Іон	$V_0 = (V_{cc} - 4 V)^{*2}$	0.5	2.0		А
			Vo = (Vcc – 15 V) *3	2.0			
	Low Level Output Current	lol	$V_{O} = (V_{EE} + 2.5 \text{ V})^{*2}$	0.5	2.0		А
			Vo = (VEE + 15 V) *3	2.0			
	High Level Output Voltage	Vон	lo = -100 mA *4	Vcc-3.5	Vcc-2.5	Vcc-1.5	V
	Low Level Output Voltage	Vol	lo = 100 mA		0.1	0.5	V
	High Level Supply Current	Іссн	Vo = open, IF = 7 to 16 mA		2.0	5.0	mA
	Low Level Supply Current	lcc∟	V_0 = open, V_F = -2 to +0.8 V		2.0	5.0	mA
	UVLO Threshold	VUVLO+	Vo > 5 V, I⊧ = 10 mA	11.0	12.3	13.5	V
		Vuvlo-		9.5	10.7	12.0	
	UVLO Hysteresis	UVLOHYS	Vo > 5 V, IF = 10 mA		1.6		V
Coupled	Threshold Input Current $(L \rightarrow H)$	Iflh	lo = 0 mA, Vo > 5 V		2.0	5.0	mA
	Threshold Input Voltage $(H \rightarrow L)$	Vfhl	lo = 0 mA, Vo < 5 V	0.8			V

*1 Typical values at $T_A = 25^{\circ}C$.

*2 Maximum pulse width = 50 μ s, Maximum duty cycle = 0.5%.

*3 Maximum pulse width = 10 μ s, Maximum duty cycle = 0.2%

*4 VoH is measured with the DC load current in this testing (Maximum pulse width = 2 ms, Maximum duty cycle = 20%).

SWITCHING CHARACTERISTICS (T_A = -40 to +100°C, V_Cc = 15 to 30 V, I_F (ON) = 7 to 16 mA, V_F (OFF) = -2 to 0.8 V, V_{EE} = GND, unless otherwise specified)

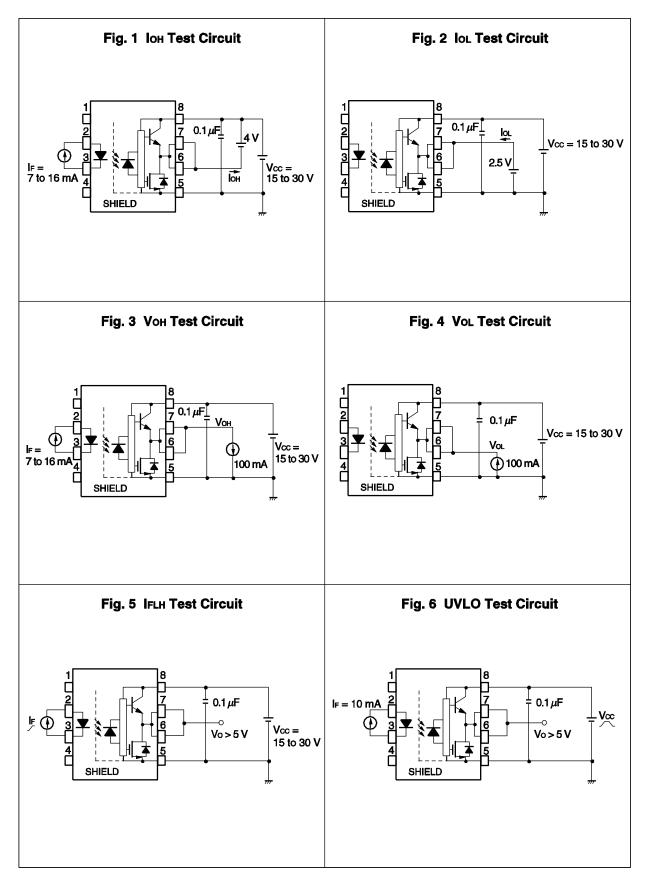
Parameter	Symbol	Conditions	MIN.	TYP.*1	MAX.	Unit
Propagation Delay Time (L \rightarrow H)	tрLн	R_g = 10 Ω , C_g = 10 nF, f = 10 kHz,	0.1	0.3	0.5	μs
Propagation Delay Time (H \rightarrow L)	tрнL	Duty Cycle = 50% ^{*2} , I⊧ = 7 to 16 mA	0.1	0.3	0.5	μs
Pulse Width Distortion (PWD)	tphl-tplh				0.3	μs
Propagation Delay Time (Difference Between Any Two Products)	tphl—tplh		-0.35		0.35	μs
Rise Time	tr			0.1		μS
Fall Time	tr			0.1		μs
UVLO (Turn On Delay)	tuvlo on	Vo > 5 V, IF = 10 mA		0.8		μs
UVLO (Turn Off Delay)	tuvlo off	Vo < 5 V, IF = 10 mA		0.6		μs
Common Mode Transient Immunity at High Level Output ^{*3}	СМн	$ \begin{split} T_{\text{A}} &= 25^{\circ}\text{C}, \ \text{IF} = 10 \ \text{to} \ 16 \ \text{mA}, \ \text{Vcc} = 30 \ \text{V}, \\ V_{\text{O} \ (\text{MIN})} &= 26 \ \text{V}, \ \text{Vcm} = 1.5 \text{k} \ \text{V} \end{split} $	25			kV/µs
Common Mode Transient Immunity at Low Level Output ^{*3}	CM∟	$T_{A} = 25^{\circ}C, I_{F} = 0 \text{ mA}, V_{CC} = 30 \text{ V},$ Vo (max) = 1 V, V_{CM} = 1.5k V	25			kV/µs

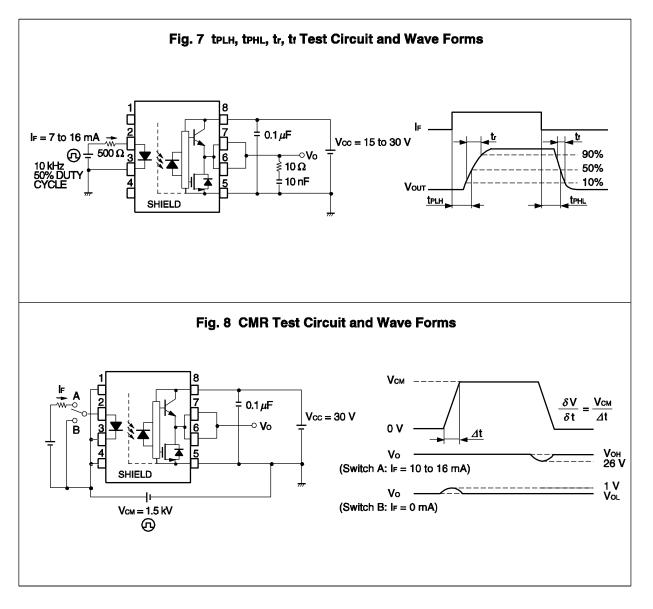
*1 Typical values at $T_A = 25^{\circ}C$.

*2 This load condition is equivalent to the IGBT load at 1 200 V/75 A.

*3 Connect pin 1 and pin 4 to the LED common.

TEST CIRCUIT





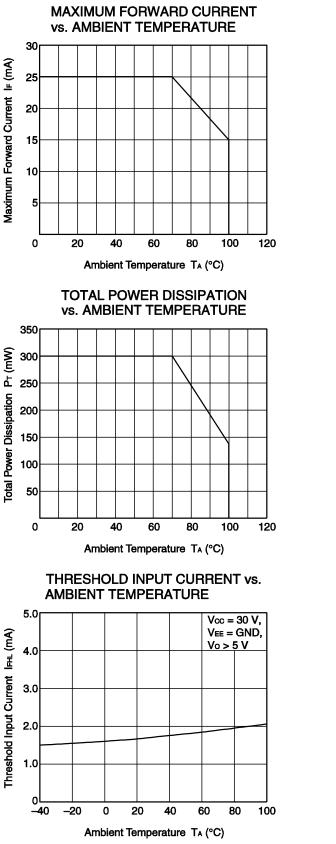
Remark CMR Test : Connect pin 1 and pin 4 to the LED common.

DETECTOR POWER DISSIPATION

vs. AMBIENT TEMPERATURE

300

TYPICAL CHARACTERISTICS (T_A = 25°C, unless otherwise specified)



Pc (mW) 250 Detector Power Dissipation 200 150 100 50 0 20 40 60 80 100 120 Ambient Temperature T_A (°C) FORWARD CURRENT vs. FORWARD VOLTAGE 100 Forward Current IF (mA) 10 = +100°C +85°C +50°C• +25°C• 0°C• 20°C 0.1 -40°C 0.01 1.0 1.2 1.4 1.6 1.8 2.0 2.2 2.4 Forward Voltage VF (V) **OUTPUT VOLTAGE vs.** FORWARD CURRENT 35 30 Output Voltage Vo (V) 25 20 15 10 5

Forward Current IF (mA)

3

4

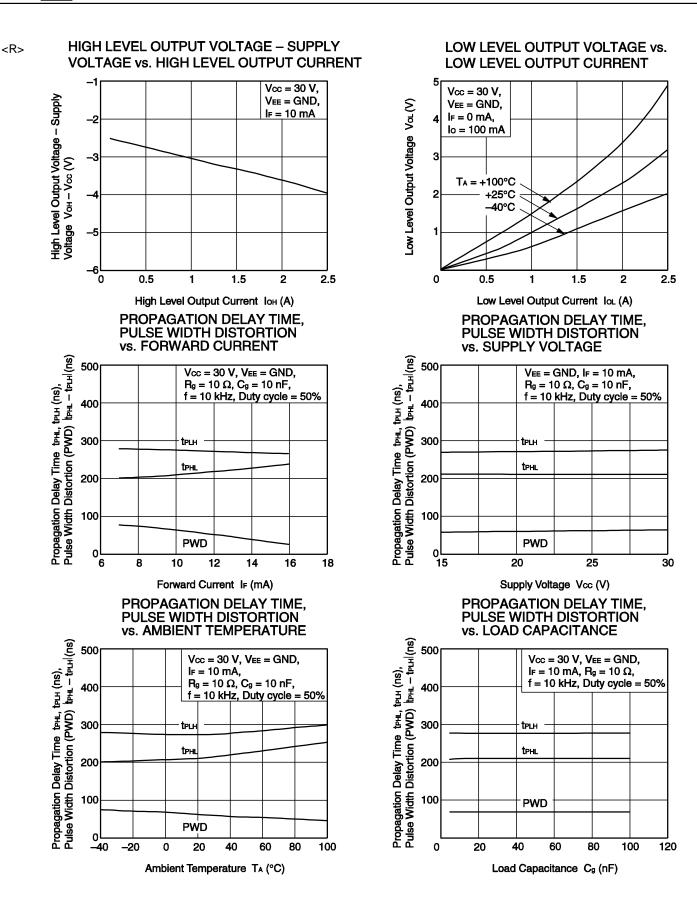
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Remark The graphs indicate nominal characteristics.

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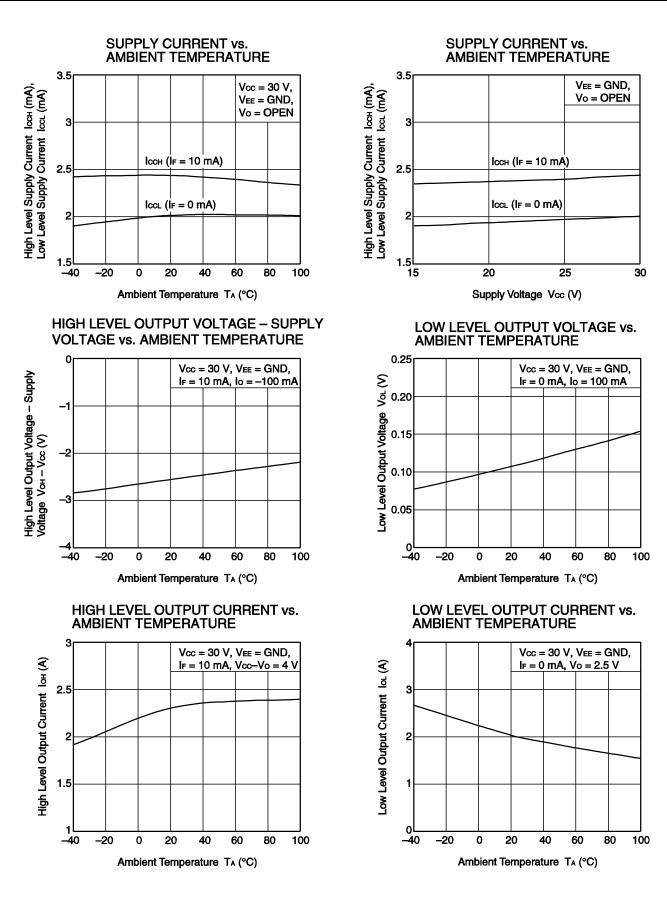
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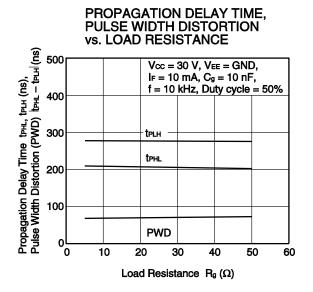


Remark The graphs indicate nominal characteristics.

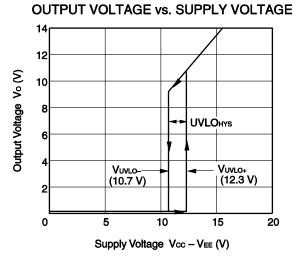
Data Sheet PN10589EJ07V0DS



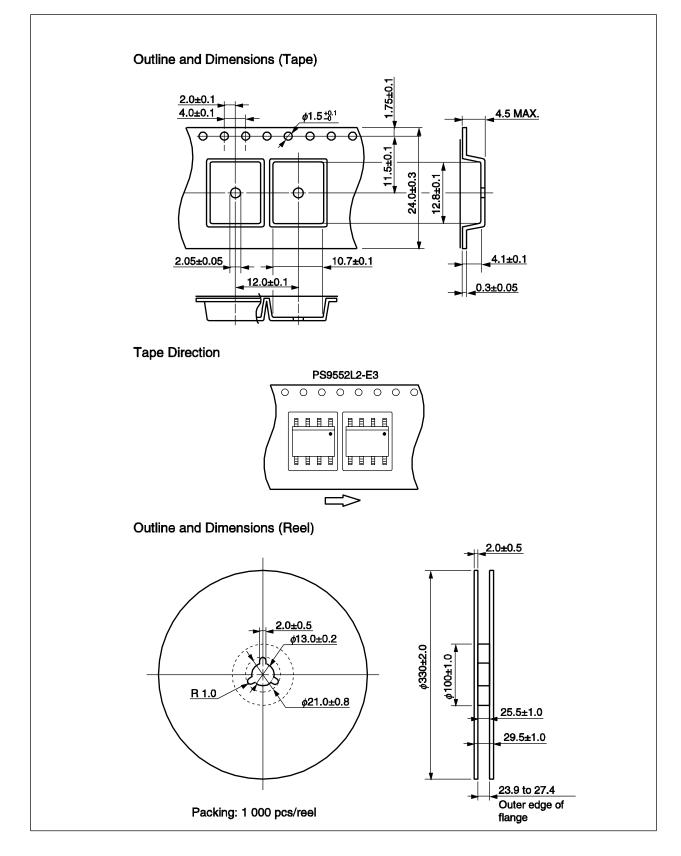
Remark The graphs indicate nominal characteristics.

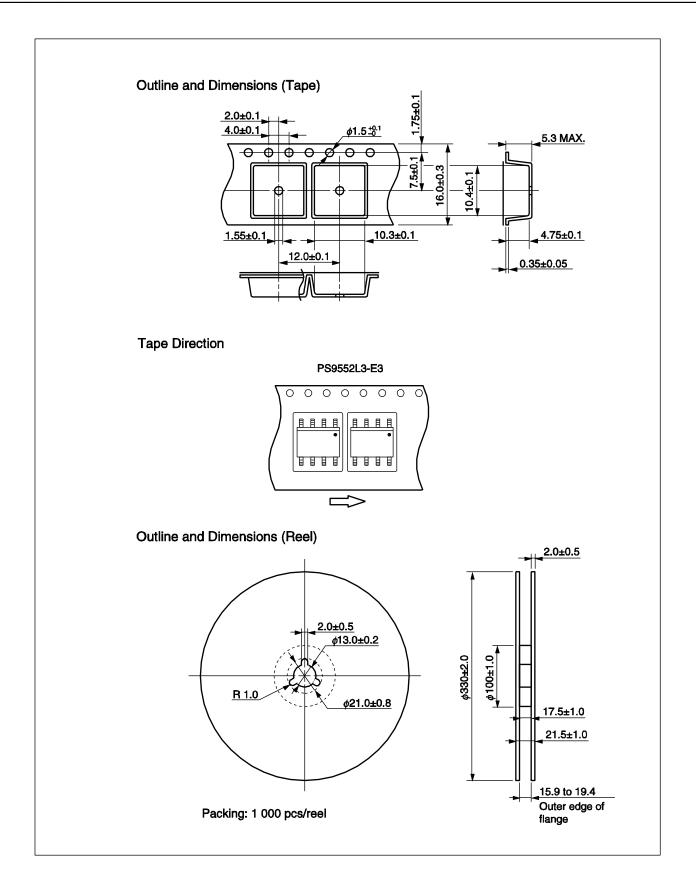


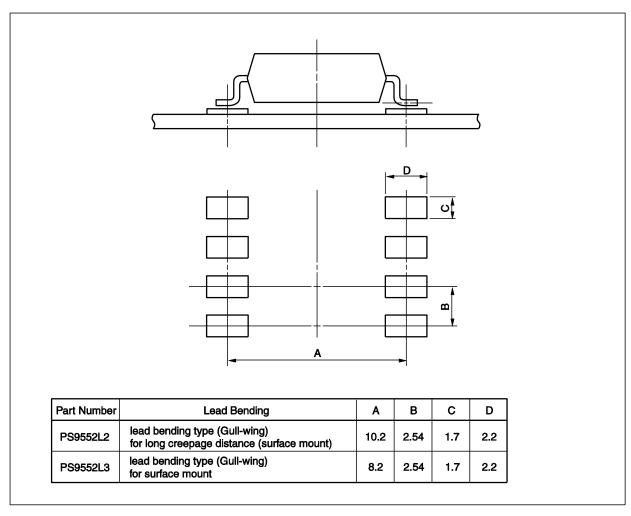
Remark The graphs indicate nominal characteristics.



TAPING SPECIFICATIONS (UNIT: mm)







RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)

NOTES ON HANDLING

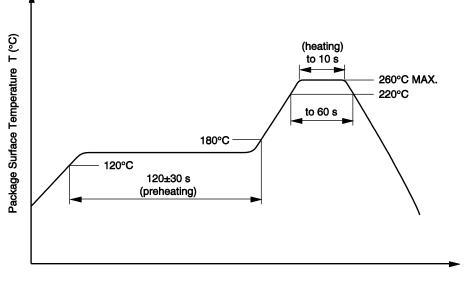
1. Recommended soldering conditions

(1) Infrared reflow soldering

- · Peak reflow temperature
- Time of peak reflow temperature
- Time of temperature higher than 220°C
- Time to preheat temperature from 120 to 180°C
- Number of reflows
- Flux

260°C or below (package surface temperature) 10 seconds or less 60 seconds or less 120±30 s Three Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

Recommended Temperature Profile of Infrared Reflow



Time (s)

(2) Wave soldering

• Flux

- Temperature 260°C or below (molten solder temperature)
- Time 10 seconds or less
- Preheating conditions 120°C or below (package surface temperature)
- Number of times One (Allowed to be dipped in solder including plastic mold portion.)
 - Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

(3) Soldering by Soldering Iron

Peak Temperature (lead part temperature)	350°C or below
 Time (each pins) 	3 seconds or less
• Flux	Rosin flux containing small amount of chlorine (The flux with a
	maximum chlorine content of 0.2 Wt% is recommended.)

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead

(b) Please be sure that the temperature of the package would not be heated over $100^\circ C$

(4) Cautions

• Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

USAGE CAUTIONS

- 1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
- 2. Board designing
 - (1) By-pass capacitor of more than 0.1 μ F is used between Vcc and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm.
 - (2) In older to avoid malfunctions and characteristics degradation, IGBT collector or emitter traces should not be closed to the LED input.
 - (3) Pins 1, 4 (which is an NC^{*1} pin) can either be connected directly to the GND pin on the LED side or left open.

Unconnected pins should not be used as a bypass for signals or for any other similar purpose because this may degrade the internal noise environment of the device.

- *1 NC: Non-Connection (No Connection)
- **3.** Make sure the rise/fall time of the forward current is 0.5 μ s or less.
- 4. In order to avoid malfunctions, make sure the rise/fall slope of the supply voltage is 3 V/ μ s or less.
- 5. Avoid storage at a high temperature and high humidity.

<R> SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter	Symbol	Spec.	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		55/100/21	
Dielectric strength maximum operating isolation voltage Test voltage (partial discharge test, procedure a for type test and random test) $U_{pr} = 1.5 \times U_{IORM}, P_d < 5 pC$	Uiorm Upr	1 130 1 695	V _{peak} V _{peak}
Test voltage (partial discharge test, procedure b for all devices) U_{Pr} = 1.875 \times U $_{\text{IORM}},~P_{\text{d}}$ $<$ 5 pC	Upr	2 119	V _{peak}
Highest permissible overvoltage	Utr	8 000	Vpeak
Degree of pollution (DIN EN 60664-1 VDE0110 Part 1)		2	
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303 Part 11))	СТІ	175	
Material group (DIN EN 60664-1 VDE0110 Part 1)		lli a	
Storage temperature range	Tstg	-55 to +125	°C
Operating temperature range	TA	-40 to +100	°C
Isolation resistance, minimum value $V_{IO} = 500 V dc at T_A = 25^{\circ}C$ $V_{IO} = 500 V dc at T_A MAX. at least 100^{\circ}C$	Ris MIN. Ris MIN.	10 ¹² 10 ¹¹	Ω Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve) Package temperature Current (input current IF, Psi = 0) Power (output or total power dissipation) Isolation resistance	Tsi Isi Psi	175 400 700	°C mA mW
$V_{IO} = 500 V dc at T_A = Tsi$	Ris MIN.	10 ⁹	Ω

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M8E0904E

Caution GaAs Products	This product uses gallium arsenide (GaAs). GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.
	 Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
	 Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
	Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
	Do not burn, destroy, cut, crush, or chemically dissolve the product.
	Do not lick the product or in any way allow it to enter the mouth.

Mouser Electronics

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