

Note: This datasheet may be out of date

Please download the latest datasheet of LDM182G4510CC001 from the official website of Murata Manufacturing

### LDM182G4510CC001



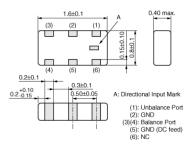






# Appearance & Shape





\*Terminal of "NC" should be fixed to the no connected pattern.



Chip type SMD baluns constructed with copper conductor and ceramic material.

Ideal for high-frequency applications.

Small-size and low-loss baluns can be customized for the balance impedance of 50ohm to 200ohm.

- 1. Available in the 2400MHz to 2500MHz frequency range.
- 2. Impedance at balanced terminals is 100ohm.
- 3. Small, Low-profiled SMD.
- 4. Low loss.
- 5. Available in tape and reel packing for automatic mounting.



## **Packaging Information**

Packaging	Specifications	Minimum Order Quantity
-	180mm Paper Tape	4000

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### Attention

1. This datasheet is downloaded from the website of Murata Manufacturing Co., Ltd. Therefore, it's specifications are subject to change or our products in it may be discontinued without advance notice. Please check with our sales representatives or product engineers before ordering.

2. This datasheet has only typical specifications because there is no space for detailed specifications.

Therefore, please review our product specifications or consult the approval sheet for product specifications before ordering



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Center Frequency	2450.00MHz	
Frequency Range	2400.00MHz to 2500.00MHz	
Insertion Loss I)	0.75dB max. (at 25°C)	
Insertion Loss II)	0.85dB max. (-40 to +85°C)	
Unbalance Impedance (Nom.)	50Ω	
Balance Impedance (Differential) (Nom.)	100Ω	
Unbalance Port VSWR	1.92 max. (Balance Port:at 100ohm)	
Power Capacity	0.5W	
Operating Temperature Range	-40°C to 85°C	
L x W (size)	1.60x0.80mm	
Thickness(max.)	0.4mm	

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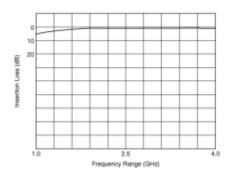


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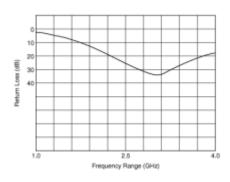
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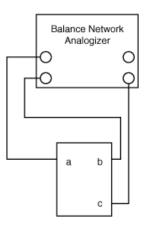




Insertion Loss Characteristics

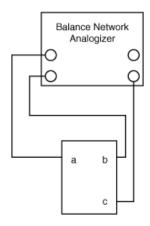


Characteristics of Unbalance Port VSWR



a : unbalance port b, c : balance port

Measurement Circuit of Insertion Loss



a : unbalance port b, c : balance port

Measurement Circuit of Unbalance Port VSWR

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