

Adjustable Non-isolated 12/15-Amp POLA DC/DC Converters

DESCRIPTION

The MPDTH series non-isolated DC/DC power modules are small in size but big on performance and flexibility. Their high output current, compact footprint and industry-leading features offer system designers versatile modules for powering complex multi-processor digital systems. Standard inputs are 3.3V., 5V. or 12 Volts DC nominal.

Two mounting styles are offered, either a pinned through-hole model with slightly lower height or a surface mount model, both with identical length and width. Respective models all share the same pinout and comply with the POLA DC/DC converter standard.

The output voltage is adjustable in three ranges from 0.8 V. to 5.5 V and can be set with a single external resistor. Margin controls provide a fixed step to the output voltage either 5% up or down. The series also includes Auto-Track sequencing, enabling modules to track each other's output voltage in multipowered systems during power-up and power-down.

Other operating features include a remote on/off control and the ability to start up into an existing (prebias) output voltage. Extensive self-protection features include input under-voltage lockout (UV), output short circuit protection and output overcurrent shutdown (OC) using an automatic restart. All models meet RoHS6 hazardous substance compliance.



FEATURES

- Compatible to Point-of-Load Alliance (POLA) Specifications
- Surface-mount or through-hole small footprint package
- Three user-selectable output voltage ranges covering 0.8V to 5.5V
- Outline dimensions less than one square inch
- 3.3V, 5V and 12 Vdc nominal inputs
- High efficiency up to 95 % typical
- Tracking input for seguenced startup/shutdown
- Sense input for load accuracy compensation
- Outstanding thermal performance and derating
- -40 to +85 degrees Celsius operating range
- Extensive self-protection, voltage limits and short circuit features
- On/Off control, trim, margin controls and pre-bias start-up functions

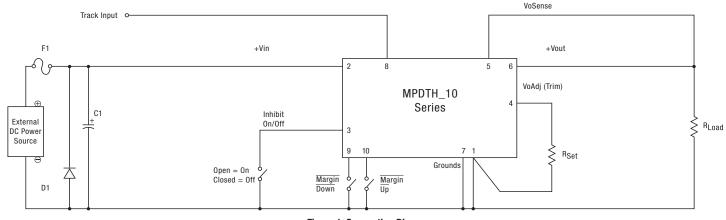


Figure 1. Connection Diagram





Notes:

- 1. Murata Power Solutions strongly recommends an external fuse, F1. Fuse rating = 2 x lin max.
- 2. Consider using diode D1 to blow the fuse in case of reversed polarity.
- C1: 470µF/25V, aluminum electrolytic capacitor, required for MPDTH03010, 05010 models. C1: 560µF/25V, aluminum electrolytic capacitor, required for MPDTH12010 models
- 4. Track input is referred to ground.5. See trim information for RSet.



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SPECIFICATION SUMMARY AND ORDERING GUIDE ①												
Output					Inp	ut ⑨						
	Vout	lout (Amps,	Power	Ripple&Noise	Regu	ılation	Vin nom.	Range	Efficiency		Package	
Model Number	(Volts)	max.)	(Watts)	(mV pk-pk)	Line	Load	(Volts)	(Volts)	(%)	Case(inches)	Case(mm)	Type ②
MPDTH03010WAH	0.8-2.5	15	37.5	20 ⑥	±1.25%	±1.5%	3.3	3-3.6	93 ③	1.37x0.62x0.35	34.80x15.75x9.0	TH
MPDTH03010WAS	0.8-2.5	15	37.5	20 ⑥	±1.25%	±1.5%	3.3	3-3.6	93 ③	1.37x0.62x0.38	34.80x15.75x9.57	SM
MPDTH05010WAH	0.8-3.6	15	54	30 ⑦	±1.25%	±1.5%	5.0	4.5-5.5	95 ④	1.37x0.62x0.35	34.80x15.75x9.0	TH
MPDTH05010WAS	0.8-3.6	15	54	30 ⑦	±1.25%	±1.5%	5.0	4.5-5.5	95 ④	1.37x0.62x0.38	34.80x15.75x9.57	SM
MPDTH12010WAH	1.2-5.5	12	66	33 ®	±1.25%	±1.5%	12.0	10.8-13.2	94 ⑤	1.37x0.62x0.35	34.80x15.75x9.0	TH
MPDTH12010WAS	1.2-5.5	12	66	33 ®	±1.25%	±1.5%	12.0	10.8-13.2	94 ⑤	1.37x0.62x0.38	34.80x15.75x9.57	SM

Notes:

- All specifications are typical at nominal input line voltage, nominal output voltage and full load, Ta=+25 deg.C. unless otherwise noted. See detailed specifications.
- "TH" is a pinned through-hole package. "SM" is a surface mount package.
- ③ Vin=3.3V, Vout=2.5V, lout=10A
- Vin=5V, Vout=3.3V, lout=10A

- S Vin=12V. Vout=5V. lout=10A
- © Vin=3.3V, Vout=2.5V, lout=15A
- Vin=5V, Vout=3.3V, lout=15A
- Vin=12V, Vout=5V, Iout=15A
- ® RoHS6 compliance does not claim EU RoHS exemption 7b (lead in solder).

MPDTH**010 Series Performance and Functional Specifications All specifications are typical unless noted.

	Input
Input Voltage Range	See Ordering Guide
Start-Up Voltage	2.45 Volts (MPDTH03010) 4.3 Volts (MPDTH05010) 9.5 Volts (MPDTH12010)
Undervoltage Shutdown	2.4 Volts (MPDTH03010) 3.7 Volts (MPDTH05010) 9.0 Volts (MPDTH12010)
External Input Capacitor (See note 3)	470 μF, required (MPDTH03010 and MPDTH05010) 560 μF required (MPDTH12010)
Remote On/Off Inhibit Control	
Positive Logic Inhibit Current (grounded)	OFF = -0.2V to +0.8 V max. or ground pin ON = open pin (internal pullup) to +Vin -130 µA (MPDTH03010 and MPDTH05010) [5] -240 µA (MPDTH12010) [5]
Tracking Input	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,
Current when grounded Slew rate Tracking Input Range	-130 μA 1 Volt per mSec max. -0.3V to (+Vin + 0.3V)
Margin Controls	-5% or +5% adjustment of Vout when the respective margin control is grounded. Current, -8 μA sink.
	Output
Minimum Loading	No minimum load
Nominal Accuracy	±2 % max. of Vsetting
Voltage Adjustment Range	See Ordering Guide
Temperature Coefficient	±40 ppm. per °C. of Vout

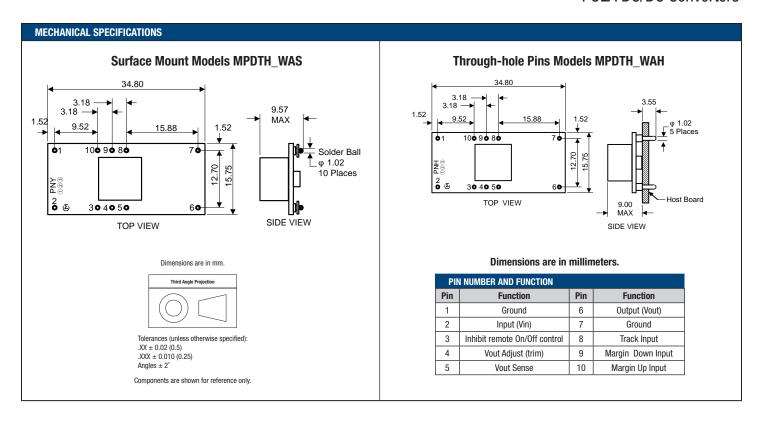
Specification Notes:

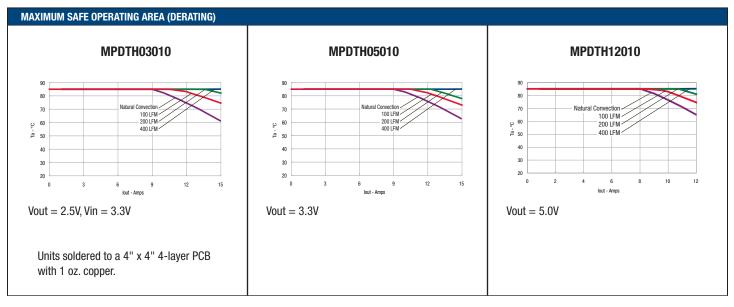
- All specifications are typical unless noted. General conditions for Specifications are +25 deg.C ambient, Vin=nominal, Vout=+2.5V, full load. Cin=470 mF (required for models MPDTH03010, 05010), Cout= 0 mF. For model MPDTH12010, the required input capacitor is 560 μF. See note 3. Adequate airflow must be supplied for extended testing under power
- (2) An external output capacitor is not required. However, a 330 µF output capacitor improves transient response.
- (3) The required input capacitor must have a ripple current rating of 700 mA RMS or greater for models MPDTH03010 and MPDTH05010. For model MPDTH12010, the ripple rating must be 800 mA RMS or greater.
- (4) Mean Time Before Failure is calculated using the Telcordia (Belcore) TR-332 50% stress, ground benign conditions, Tambient=+40 deg.C.

Ripple/Noise	See Ordering Guide		
Line/Load Regulation	See Ordering Guide		
Efficiency	See Ordering Guide		
Maximum Capacitive Loading			
Cap-ESR>=0.004 Ohms	8,250 μF non-ceramic (MPDTH03010 and MPDTH05010)		
Cap-ESR < 0.004 Ohms	6,600 μF non-ceramic (MPDTH12010)		
Current Limit Inception	20 Amps		
Short Circuit Mode			
Protection Method Short Circuit Duration	Autorecovery after overload removal Continuous, no damage (output shorted to ground)		
Sequencing	Start-up of multiple units may be synchronized by connecting tracking pins.		
VoSense Input	(Vout – VoSense) < 0.3 Volts		
Dynai	mic Characteristics		
Dynamic Load Response	70 µSec to within $\pm 2\%$ of final value (50 to 100% load step, 1A/µSec load step, 330 µF external output cap)		
Switching Frequency	300 KHz (MPDTH03010 and MPDTH05010) 350 KHz (MPDTH12010)		
Environmental			
Calculated MTBF [4]	5.7 million hours,min. (MPDTH03010 and MPDTH03010) 6.4 million hours,min. (MPDTH12010)		
Operating Temperature Range	-40 to +85 deg.C. (no condensation)		
Storage Temperature Range	-40 to +125 deq.C.		

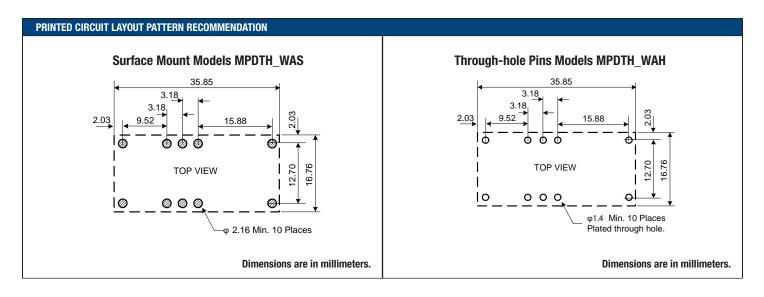
- (5) Track input is grounded or equal to Vin.
- (6) There is no input protection against overvoltage or reversed polarity. Please provide proper fusing and a back-biased diode if there is the possibility of improper polarity. Refer to the Connection Diagram.
- (7) The outputs are not intended to sink appreciable reverse current.
- (8) Output current limit and short circuit protection is non-latching. When the overcurrent fault is removed, the converter will immediately recover.
- (9) Connect VoSense to Vout remotely at the load for optimal accuracy. If not used at the load, connect VoSense to Vout at the converter. Do not exceed 0.3 Volts difference between Vout and VoSense

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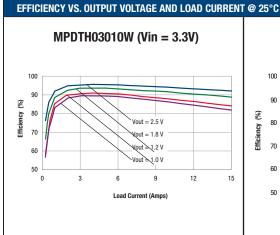


EFFICIENCY VS. OUTPUT VOLTAGE

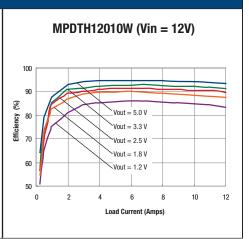
Models MPDTH03010WAS/WAH				
Vout	Efficiency (typ.)	Rset Trim Resistor		
2.5 V.	93%	2.21 ΚΩ		
2.0 V.	92%	4.12 KΩ		
1.8 V.	91%	5.49 KΩ		
1.5 V.	89%	8.87 ΚΩ		
1.2 V.	87%	17.4 ΚΩ		
1.0 V.	85%	36.5 KΩ		
lout=10 Amps				

Models MPDTH05010WAS/WAH				
Vout	Efficiency (typ.)	Rset Trim Resistor		
3.3 V.	95%	698 Ω		
2.5 V.	93%	2.21 ΚΩ		
2.0 V.	92%	4.12 ΚΩ		
1.8 V.	91%	5.49 ΚΩ		
1.5 V.	90%	8.87 ΚΩ		
1.2 V.	88%	17.4 ΚΩ		
1.0 V.	86%	36.5 ΚΩ		
lout=10 Amps				

Models MPDTH12010WAS/WAH				
Vout	Efficiency (typ.)	Rset Trim Resistor		
5.0 V.	94%	280 Ω		
3.3 V.	93%	2.0 ΚΩ		
2.5 V.	91%	4.32 ΚΩ		
2.0 V.	90%	8.06 KΩ		
1.8 V.	89%	11.5 KΩ		
1.5 V.	88%	24.3 ΚΩ		
1.2 V.	86%	Open		
lout=8 Amps				





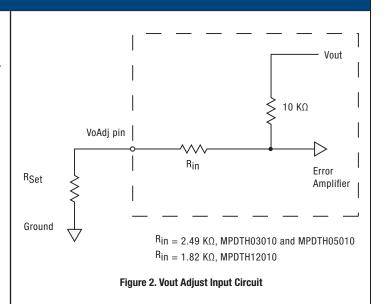


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OUTPUT VOLTAGE ADJUSTMENT

The output voltage may be adjusted over a limited range by connecting an external trim resistor (Rset) between the VoutAdjust (trim) pin and Ground. The Rset resistor must be a 1/10 Watt precision metal film type, $\pm 1\%$ accuracy or lower with low temperature coefficient, ± 100 ppm/°C. or better. Mount the resistor close to the converter with short leads.

In the tables below, the calculated resistance is given and an equivalent using two series-connected standard value resistors. Do not exceed the specified limits of the output voltage or the converter's maximum power rating when applying these resistors. Also, avoid high noise at the Vout-Adjust input. However, to prevent instability, you should never connect any capacitors from VoutAdjust to either Ground or Vout.



MPDTH03010WAS/WAH				
Output Voltage	Calculated Rset (KΩ)	Standard value resistors		
2.5 V.	2.22	2 ΚΩ + 220Ω		
2.0 V.	4.18	$3.9 \text{ K}\Omega + 270 \Omega$		
1.8 V.	5.51	5.1 ΚΩ + 390 Ω		
1.5 V.	8.94	$8.2 \text{ K}\Omega + 750 \Omega$		
1.2 V.	17.5	16 ΚΩ + 1.5 ΚΩ		
1.0 V.	37.5	36 ΚΩ + 1.5 ΚΩ		
0.8 V.	∞	Open		

Rset
$$(K\Omega) = 10 \text{ K}\Omega \text{ x} \frac{0.8\text{V}}{\text{Vout} - 0.8\text{V}} = 2.49 \text{ K}\Omega$$

MPDTH05010WAS/WAH				
Output Voltage	Calculated Rset (KΩ)	Standard value resistors		
3.3 V.	0.71	680 Ω + 30 Ω		
2.5 V.	2.22	2 ΚΩ + 220 Ω		
2.0 V.	4.18	3.9 ΚΩ + 270 Ω		
1.8 V.	5.51	5.1 ΚΩ + 390 Ω		
1.5 V.	8.94	8.2 ΚΩ + 750 Ω		
1.2 V.	17.5	16 ΚΩ + 1.5 ΚΩ		
1.0 V.	37.5	36 ΚΩ + 1.5 ΚΩ		
0.8 V.	∞	Open		

Rset (K
$$\Omega$$
) = 10 K Ω x $\frac{0.8V}{Vout - 0.8V}$ = 2.49 K Ω

MPDTH12010WAS/WAH				
Output	Calculated Rset	Standard value		
Voltage	(ΚΩ)	resistors		
5.0 V.	0.29	270 Ω + 20 Ω		
3.3 V.	1.99	2 ΚΩ		
2.5 V.	4.33	$4.3 \text{ K}\Omega + 30 \Omega$		
2.0 V.	8.18	$7.5 \text{ K}\Omega + 680 \Omega$		
1.8 V.	11.51	11 ΚΩ + 510 Ω		
1.5 V.	24.85	24 ΚΩ + 820 Ω		
1.2 V.	8	Open		

Rset
$$(K\Omega) = 10 \text{ K}\Omega \text{ x} \frac{0.8 \text{V}}{\text{Vout} - 1.2 \text{V}} = 1.82 \text{ K}\Omega$$



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APPLICATION NOTES

Tracking Input

When enabled, the tracking function allows the output to follow an external analog signal presented at the Tracking input. This pin becomes active approximately 50 mS after the input voltage has been applied and the converter reaches regulation. External track inputs must be referred to Ground. Several converters may be Tracked in parallel to generate outputs which rise and fall together.

The Tracking input range is zero Volts up to the nominal set-point output voltage. If the Tracking input rises above the Vout nominal voltage, Vout will remain at the set point after the power up ramp. If Track is unused, it should be connected to +Vin.

Please note that because of the Under Voltage input lockout feature (UVLO), the output cannot follow Track In during power up. Also, the Track feature is disabled whenever the converter is in Inhibit (OFF) mode.

Over-Current Output Protection

If excess current is drawn from the converter, the output will shut down and then periodically attempt to restart with narrow full power cycles. If the excess load is reduced to within the rated maximum load, the converter will immediately restart and operate normally. If the load is still too great, another power-down cycle will begin. This is called "hiccup" operation. It significantly reduces the output power to avoid damage. The converter will quickly and automatically recover once the load is reduced.

Pre-Bias Operation

The MPDTH_WAH/WAS series of converters use synchronous rectification in their output circuits. Normally, synchronous rectification may cause excess current being sunk back in to the converter if there is higher external output voltage present. If such prebias voltage is present on the output at the time of startup, the converter will operate in a manner which avoids excessive reverse current being sunk back into the output. This pre-bias protection will continue until the converter achieves normal regulation at its set-point output voltage.

In order to achieve successful pre-bias voltage protection, several conditions must be met:

- [1] The pre-bias voltage must be lower than the expected final set-point output voltage. If the pre-bias voltage remains higher than the set-point and is capable of back-driving excess current, possible output damage may occur. If the pre-bias voltage has high impedance or has very limited current capability, normal startup will proceed.
- [2] Tracking operation must not be used during pre-bias startup. The Tracking input should be either open or connected to Vin so that normal soft start will proceed. After successful startup of approximately 50 mSec, Tracking may be resumed.
- [3] Carefully examine any output circuits with large external output capacitors and low-ESR. If there is no bleeder circuit or other method to reduce the stored voltage AND the cap voltage is higher than the expected set-point, operation may fail or the output may be damaged. Smaller capacitors with lower energy storage should not interfere with startup. See the recommended Connection Diagram. Use only as much output capacitance as needed.

Also be careful immediately turning the converter back on after the Inhibit control turns the converter off. Any energy stored in larger output capacitors may cause the problems described above.

Margin Up/Down Controls

Ground each Margin control to increase or decrease the output voltage by $\pm 5\%$ from its setpoint value. If not needed, leave the margin controls unconnected (open).

The margin controls are analog inputs and are not logic compatible. If a switch is not used to ground, a small signal transistor (such as an n-channel MOSFET with low off-state leakage) will work if biased fully into either pinchoff/cutoff or saturation. Sink current is approximately -8 mA when ON and the open circuit voltage is approximately 0.8 Volts when OFF. Do not ground both margin controls simultaneously.

For output changes less than $\pm 5\%$, insert an additional resistor in series with the switch or transistor to ground. The margin adjustment equation is:

R-up or R-down (in $K\Omega$) = (499 / delta%) – 99.8

Where "delta%" is the desired change in percent of Vout up or down relative to the set-point output. Be sure to use low tempco precision resistors.

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ISO 9001 and 14001 REGISTERED



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Refer to: http://www.murata-ps.com/requirements/

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