

The Future of Analog IC Technology

DESCRIPTION

The MP2603 is a low cost single cell Li-ion battery charger IC suitable for low charge current applications. It features constant current (CC) and constant voltage (CV) charging modes with programmable charge current (50mA–150mA), trickle charge and charge status indication.

The MP2603 is enabled with an input supply voltage greater than 3.5V. The charger IC is disabled when unplugged from the AC adaptor. To minimize the charger's total solution cost, the MP2603 does not need an external reverse blocking diode, a precision current-sense resistor or a compensation capacitor at the output. MP2603 is available in a 5-pin TSOT23 package.

EVALUATION BOARD REFERENCE

| Board Number | Dimensions |
|--------------|-----------------------|
| EV2603EJ-00B | 1.4"X x 1.1"Y x 0.5"Z |

FEATURES

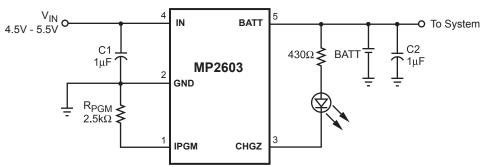
- V_{IN} Surge Up to 25V
- 0.75% V_{BATT} Accuracy
- Low Battery Reverse Current, (< 1uA)
- Programmable Charge Current
- Over Temperature Protection
- Over Current Protection
- Charge Status Indication
- Over Charge Protection
- No External Sense Resistor Required
- No External Reverse Blocking Diode Required
- No Output Capacitor Required for Stability

APPLICATIONS

- Bluetooth Headset
- MP3 Players
- Mini PDAs
- Cell Phones
- Digital Cameras

All MPS parts are lead-free and adhere to the RoHS directive. For MPS green status, please visit MPS website under Products, Quality Assurance page. "MPS" and "The Future of Analog IC Technology" are registered trademarks of Monolithic Power Systems, Inc.

TYPICAL APPLICATION



1 www.MonolithicPower.com MPS Proprietary Information. Patent Protected. Unauthorized Photocopy and Duplication Prohibited. © 2013 MPS. All Rights Reserved.

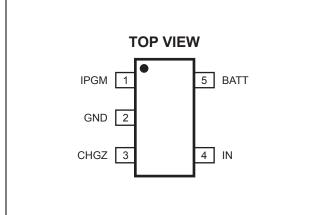


ORDERING INFORMATION

| Part Number* | Package | Top Marking |
|--------------|---------|-------------|
| MP2603EJ | TSOT23 | M3 |

* For Tape & Reel, add suffix –Z (e.g. MP2603EJ–Z). For RoHS compliant packaging, add suffix –LF (e.g. MP2603EJ–LF–Z)

PACKAGE REFERENCE



ABSOLUTE MAXIMUM RATINGS (1)

| IN to GND | 0.3V to +25V |
|------------------------------|--|
| All Other Pins | 0.3V to +6.5 V |
| Continuous Power Dissipation | on (T _A = +25°C) ⁽²⁾ |
| | 0.57W |
| Junction Temperature | |
| Lead Temperature | 260°C |
| Storage Temperature | 65°C to +150°C |
| | (0) |

Recommended Operating Conditions ⁽³⁾

| Supply Voltage V _{IN} | 4.75V to 5.25V |
|--------------------------------|----------------|
| Operating Temperature | 20°C to +85°C |

Thermal Resistance $^{(4)}$ θ_{JA}

 θ_{JC}

Notes:

- 1) Exceeding these ratings may damage the device.
- 2) The maximum allowable power dissipation is a function of the maximum junction temperature T_J (MAX), the junction-toambient thermal resistance θ_{JA} , and the ambient temperature T_A. The maximum allowable continuous power dissipation at any ambient temperature is calculated by P_D (MAX) = (T_J (MAX)-T_A)/ θ_{JA} . Exceeding the maximum allowable power dissipation will cause excessive die temperature, and the regulator will go into thermal shutdown. Internal thermal shutdown circuitry protects the device from permanent damage.
- The device is not guaranteed to function outside of its operating conditions.
- 4) Measured on JESD51-7, 4-layer PCB.



ELECTRICAL CHARACTERISTICS

V_{IN} = 5V, T_A = +25°C, unless otherwise noted.

| Parameter | Symbol | Condition | Min | Тур | Мах | Units |
|--|---------------------|--|------|------|------|-------------------|
| Supply Current from VIN | I _{SUPPLY} | I _{CHG} = 0A | | 0.5 | | mA |
| Input Under Voltage Lockout | | Input Falling | 1.8 | 2.3 | 2.8 | V |
| Battery Reverse Current to BATT Pin | | V _{BATT} = 4V, Input = GND or Float | | | 2 | μA |
| Battery Voltage Regulation | V _{BATT} | I _{CHG} = 5mA, 0°C < T _A < +50°C | 4.16 | 4.20 | 4.24 | V |
| Constant Current Regulation ⁽⁵⁾ | | V_{IN} = 5V, V_{BATT} = 3.8V, R_{PGM} = 2.5k Ω | 135 | 150 | 165 | mA |
| | I _{CHG} | $V_{IN} = 5V, V_{BATT} = 3.8V,$ $R_{PGM} = 1.5k\Omega - 7.2k\Omega,$ $-20^{\circ}C < T_A < +85^{\circ}C$ | 90 | 100 | 110 | %I _{СНG} |
| | | $V_{IN} = 5V, V_{BATT} = 3.8V,$ 7.2k $\Omega < R_{PGM} \le 12.4k\Omega$ -20°C < T _A < +85°C | 70 | | 130 | %I _{СНG} |
| Trickle Charge Current | | V _{IN} = 5V, V _{BATT} = 2.3V | 5 | 10 | 15 | %I _{СНG} |
| Trickle Threshold Voltage | | | 2.45 | 2.60 | 2.75 | V |
| Trickle Voltage Hysteresis | | | | 190 | | mV |
| CHGZ Low-to-High Threshold | | | 3 | 10 | 17 | %I _{СНG} |
| CHGZ Sink Current | | Pin Voltage = 0.2V | 5 | | | mA |
| Dropout Voltage | | I _{CHG} = 150mA, V _{BATT} = 3.8V | | 640 | | mV |
| Thermal Limit ⁽⁶⁾ | | | | 130 | | °C |
| Overcharge Protection | | V _{BATT} = 4.25V | | | 0 | μA |

Notes:

5) I_{CHG} is the target preprogrammed charge current (Die temperature below 110°C).
6) Guaranteed by design.



PIN FUNCTIONS

| Pin # | Name | Description |
|-------|------|---|
| 1 | IPGM | Charge Current Program Pin. Connect to a resistor to program the charge current. Do not connect a capacitor to this pin. |
| 2 | GND | Chip Ground Return. Connect to the system ground. |
| 3 | CHGZ | Open-Drain Charge Status Indication Pin. It can be connected through an LED and a 430Ω resistor to BATT for visual indication or through a $1k\Omega$ resistor to BATT for use with a microcontroller. |
| 4 | IN | Input Supply Pin. Connect to a 5V (\pm 5%) power supply. The maximum surge voltage must be less than 25V. Decouple this pin with a minimum of 0.1µF ceramic capacitor. |
| 5 | BATT | Charger Output. Connect to the battery. Placing a capacitor to GND on this pin is optional. |

BLOCK DIAGRAM

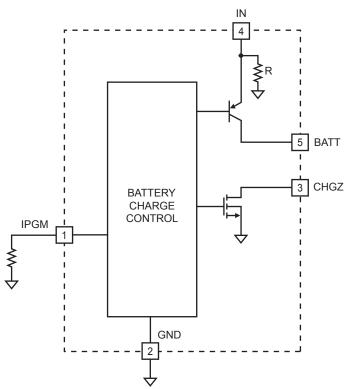
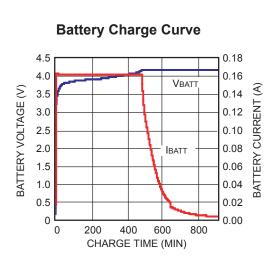


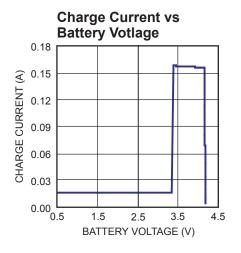
Figure 1—Functional Block Diagram



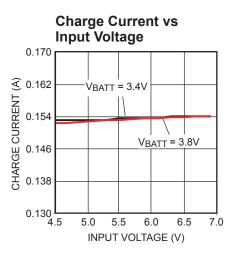
TYPICAL PERFORMANCE CHARACTERISTICS

 V_{IN} = 5V, C1 = 1µF, C2 = 1µF, T_A = +25°C, unless otherwise noted.

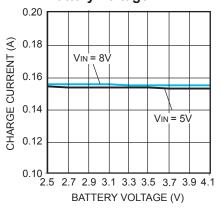




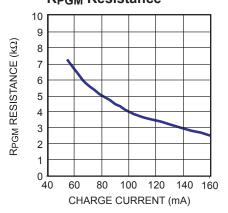
Battery Voltage vs Input Voltage 4.30 BATTERY VOLTAGE (V) 4.24 ICHG = 0mA 4.18 4.12 Існа = 45mA 4.06 4.00 4.5 5.0 5.5 6.0 6.5 7.0 7.5 8.0 INPUT VOLTAGE (V)



Charge Current vs Battery Voltage

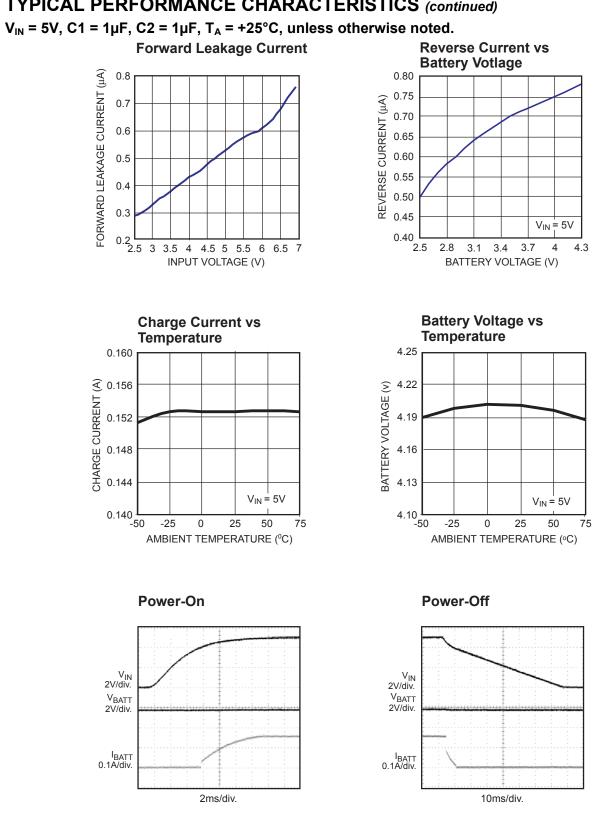


Charge Current vs RPGM Resistance





TYPICAL PERFORMANCE CHARACTERISTICS (continued)





OPERATION

The MP2603 is enabled with a minimum input supply voltage of 3.5V. This power supply must also be higher than both the UVLO threshold and the battery voltage. There is an internal 500k Ω pull-down resistor connecting the IN and GND pins. The MP2603 provides CC/CV charge algorithms to automatically change modes depending on the battery status. Figure 2 shows the typical charging procedure for the MP2603.

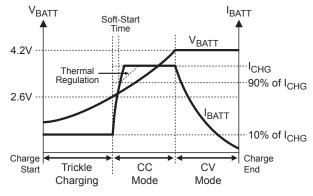


Figure 2—MP2603 Typical Charging Procedure

The charge current, I_{CHG} , is programmed by connecting a resistor (R_{PGM}) from the I_{PGM} pin to ground. The relationship of the charge current and the programming resistance is established by the following equation and graph. The Table1 lists the typical application setting of lchg.

$$I_{CHG} = 150 \text{mA} \times \frac{2.53 \text{k}\Omega}{\text{R}_{PGM}}$$
(1)

Table 1— R_{PGM} and I_{CHG} Relationship

| R _{PGM} (kΩ) | I _{снс} (mA) |
|-----------------------|-----------------------|
| 12.4 | 30 |
| 7.2 | 52 |
| 4.99 | 76 |
| 3.74 | 101 |
| 2.5 | 151 |

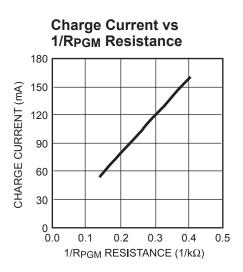


Figure 3— Charge Current vs 1/R_{PGM} Resistance

When battery voltage falls below the trickle threshold (2.6V), charge current is limited to 10% of the programmed value to trickle charge the fully depleted battery. After the battery voltage reaches 2.6V, the charger begins to charge at the constant current of the programmed value (I_{CHG}). This is referred to as Constant Current (CC) mode. Once the battery voltage reaches 4.2V, the charger will operate in the Constant Voltage (CV) mode until the battery is fully charged.

Charge Status (CHGZ)

The open-drain CHGZ pin monitors charge status by connecting to V_{BATT} through an LED and/or a resistor. The end-of-charge, or battery full, is indicated by the CHGZ pin changing from LOW to HIGH (i.e. LED turns off) when I_{CHG} decreases to 10% of the programmed value.

Thermal Protection

To prevent overheating, the MP2603 automatically limits the die temperature to 130°C by reducing the internal current-setting reference voltage. In the thermal protection mode, the charge current reduces automatically, but remains continuous.

www.MonolithicPower.com MPS Proprietary Information. Patent Protected. Unauthorized Photocopy and Duplication Prohibited. © 2013 MPS. All Rights Reserved.



APPLICATION

Flow Chart of Operation

Power-On Reset (POR) feature can be applied to the MP2603 to ensure that the device starts operating in a known state when power is applied. The flow chart in Figure 4 describes the conditions and operation modes of the MP2603. CVC and CCC stand for "constant voltage charge" and "constant current charge", respectively.

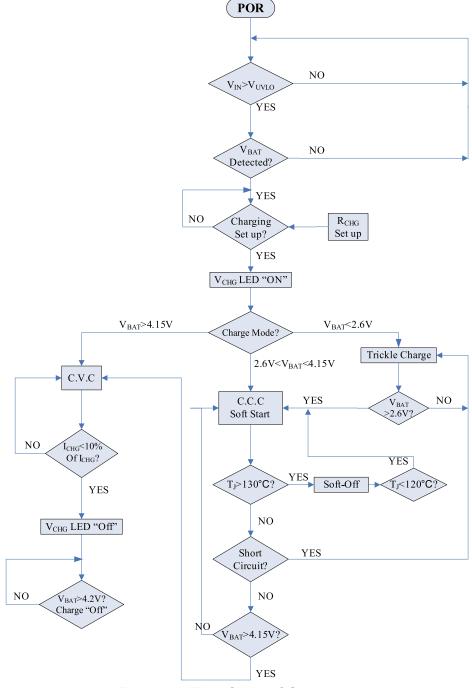


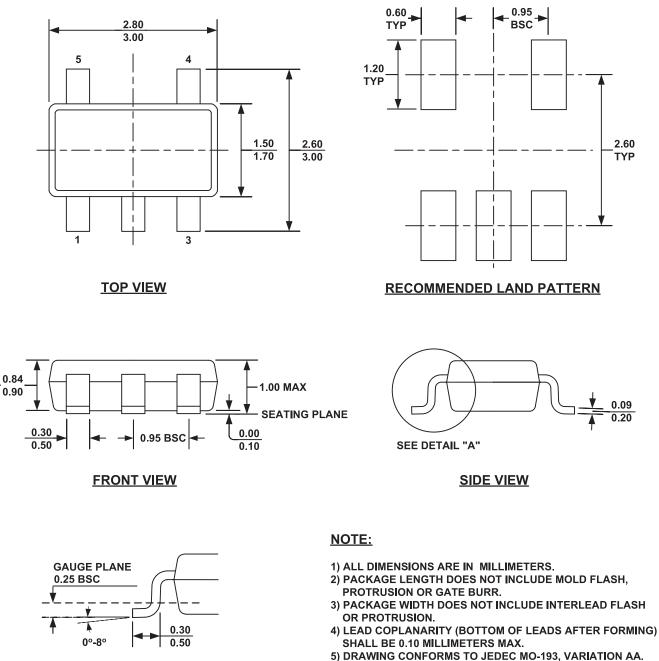
Figure 4—Flow Chart of Operation



TSOT23-5

PACKAGE INFORMATION

DETAIL "A"



6) DRAWING IS NOT TO SCALE.

NOTICE: The information in this document is subject to change without notice. Users should warrant and guarantee that third party Intellectual Property rights are not infringed upon when integrating MPS products into any application. MPS will not assume any legal responsibility for any said applications.

2.60

TYP

0.09

0.20

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Monolithic Power Systems (MPS):

MP2603EJ-LF-P MP2603EJ-LF-Z